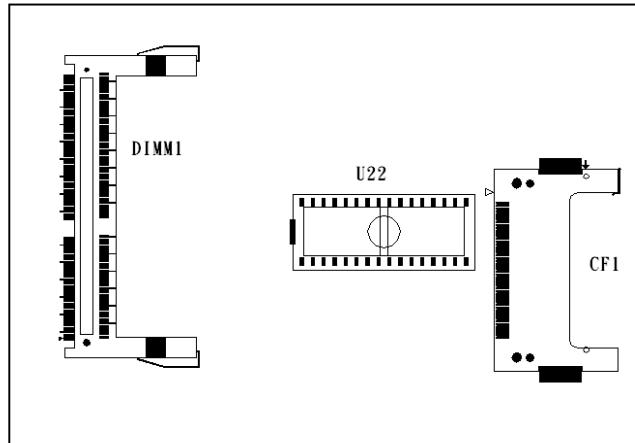
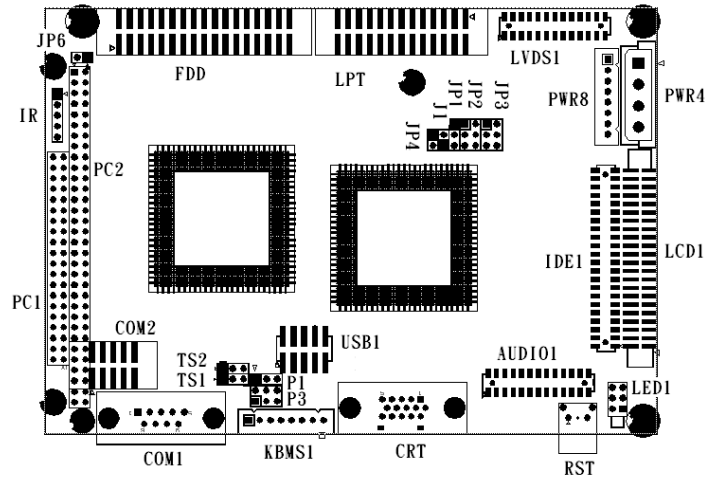


# AR-B1552NP Manual

Version 1.0

## 1. SETTING UP THE SYSTEM

The AR-B1552NP is new generation 3.5" CPU board. This section provides hardware's jumpers setting, the connectors' locations, and the pin assignment.



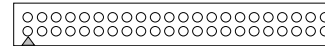
### 1.1. PACKING LIST

- The quick setup manual
- 1 AR-B1552NP all-in-one single CPU board
- Acrosser's AR-B9425NP audio card
- Software Utility CD

### 1.2. SDRAM SOCKET 144 PIN, 128MB MAXIMUM (DIMM1)

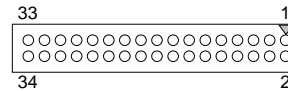
### 1.3. HARD DISK CONNECTOR

#### - IDE1



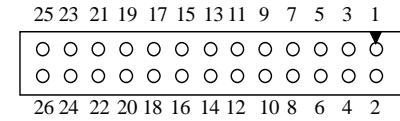
#### - CF1

### 1.4. FDD PORT CONNECTOR (FDD)



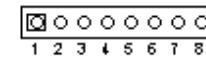
PIN	Signal	Pin	Signal
1-33(odd)	GROUND	18	-DIRECTION
2	-REDUCED WRITE CURRENT	20	-STEP OUTPUT PULSE
4	NOT USED	22	-WRITE DATA
6	NOT USED	24	-WRITE ENABLE
8	-INDEX	26	-TRACK 0
10	-MOTOR ENABLE A	28	-WRITE PROTECT
12	-DRIVE SELECT B	30	-READ DATA
14	-DRIVE SELECT A	32	-SIDE 1 SELECT
16	-MOTOR ENABLE B	34	DISK CHANGE

### 1.5. PARALLEL PORT CONNECTOR (LPT)



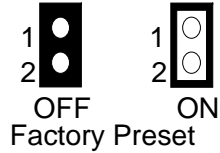
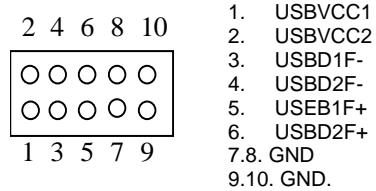
CN8	DB-25	Signal	CN8	DB-25	Signal
1	1	-Strobe	2	14	-Auto Form Feed
3	2	Data 0	4	15	-Error
5	3	Data 1	6	16	-Initialize
7	4	Data 2	8	17	-Printer Select In
9	5	Data 3	10	18	Ground
11	6	Data 4	12	19	Ground
13	7	Data 5	14	20	Ground
15	8	Data 6	16	21	Ground
17	9	Data 7	18	22	Ground
19	10	-Acknowledge	20	23	Ground
21	11	Busy	22	24	Ground
23	12	Paper	24	25	Ground
25	13	Printer Select	26	--	No Used

### 1.6. KEYBOARD & MOUSE CONNECTOR (KBMS1)

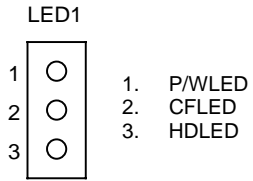


1. KMVCC
2. KBDAT
3. KBCLK
4. KMGND
5. KMVCC
6. MSDAT
7. MSCLK
8. KMGND

### 1.7. USB CONNECTOR (USB1)

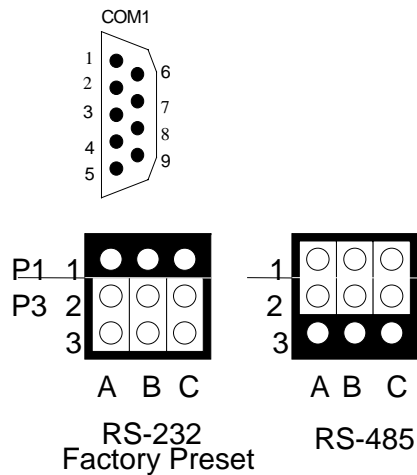


### 1.8. LED HEADER (LED1)

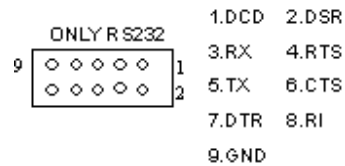


### 1.9. COM1

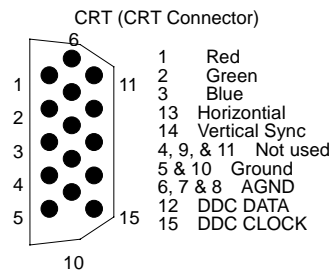
- USED RS232 & RS485
- RS485 (JP6)



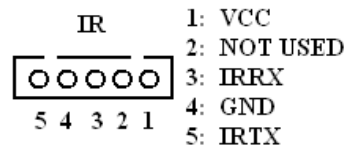
### 1.10. COM2



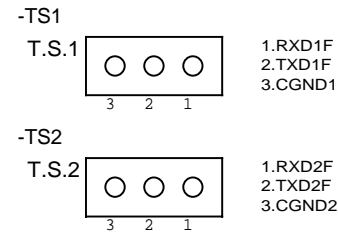
### 1.11. CRT CONNECTOR (CRT)



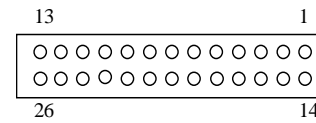
### 1.12 IRDA HEADER (IR)



### 1.13 TOUCH SCREEN



### 1.14 LDS HEADER (LVDS1) (OPTIONAL)



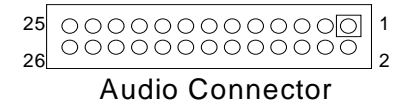
PIN (LVDS1)	FUNCTION
1	TXOUT0-
2	TXOUT0+
3	TXOUT1-
4	TXOUT1+
5	TXOUT2-
6	TXOUT2+
7	TXCLK-
8	TXCLK+
9	TXOUT3-
10	TXOUT3+
11	VTKBP
12	VTX5
13	VTX5
14	GND
15	GND
16	VTX5
17	VTX5
18	NC
19	GND
20	GND
21	VTX12
22	VTX12
23	GND
24	NC
25	NC
26	NC

	9	Data 7	18	22	Ground
	10	-Acknowledge	20	23	Ground

### 1.15. RESET BUTTON (RST)

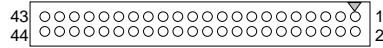


### 1.16. 26-PIN AUDIO CONNECTOR (AUDIO1)



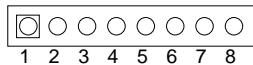
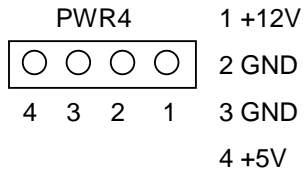
CN9	SIGNAL
1	CDINL
2	LININL
3	CDINR
4	LININR
5	VCC
6	NC
7	LINOUTL
8	MICIN
9	LINOUTR
10	PCBEEP
11	GND
12	NC
13	NC
14	GNDAUD
15	GND
16	NC
17	NC
18	NC
19	NC
20	NC
21	NC
22	NC
23	NC
24	NC
25	GNDAUD
26	GND

### 1.18. LCD PANEL DISPLAY CONNECTOR (LCD1)



Pin	Signal	Pin	Signal
1	GND	2	CLK
3	GND	4	HSYNCR
5	VSYNCR	6	GND
7	Not Used	8	Not Used
9	FPDR0	10	FPDR1
11	FPDR2	12	FPDR3
13	GND	14	FPDR4
15	FPDR5	16	Not Used
17	Not Used	18	FPDR6
19	FPDR7	20	GND
21	FPDR8	22	FPDR9
23	FPDR10	24	FPDR11
25	Not Used	26	Not Used
27	GND	28	FPDR12
29	FPDR13	30	FPDR14
31	FPDR15	32	FPDR16
33	FPDR17	34	GND
35	VCC	36	VCC
37	+12V	38	+12V
39	GND	40	GND
41	DISPENR	42	GKLENR
43	GND	44	VDDEN

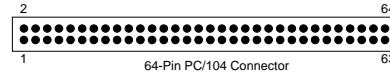
### 1.19. POWER CONNECTOR (PWR4 & PWR8)



- 1 GND
- 2 +5 VDC
- 3 +5 VDC
- 4 GND
- 5 GND
- 6 +12 VDC
- 7 -12 VDC
- 8 -5 VDC

### 1.20. PC104 CONNECTOR

(1) 64 Pin PC/104 Connector Bus A & B (PC2)



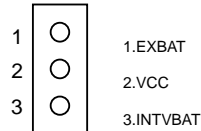
(2) 40 Pin PC/104 Connector Bus A & B (PC1)



### 1.21. BATTERY SELECT (J1 & JP1)

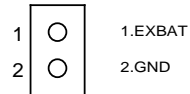
- Internal BAT: JP1 2-3 ON
- External BAT: JP1 1-2 ON

JP1



Factory Preset

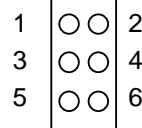
J1



### 1.22. VOLTAGE SELECT (JP2 & JP3)

- LVDS 1

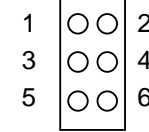
JP3



JUMPER	VOLTAGE	NOTE
1-3 ON	VCC	Factory Preset
2-4 ON		
3-5 ON	VCC3	
4-6 ON		

### ■ LCD 1

JP2



JUMPER	VOLTAGE	NOTE
1-3 ON	VCC3	Factory Preset
2-4 ON		
3-5 ON	VCC	
4-6 ON		

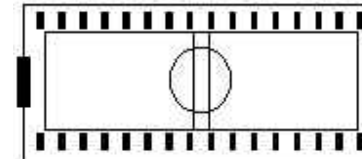
### 1.23. DOC ADDRESS SELECT (JP4)

JP4



JUMPER	ADDRESS	NOTE
OFF	C8000	Factory Preset
ON	D0000	

### 1.24. DOC SOCKET (U22)



**NOTE:**

**OFF = Without Jumper.**  
**ON = With Jumper**

Note:

If the content of setting is inconsistent with CD-ROM. Please refer to the Setting as priority.