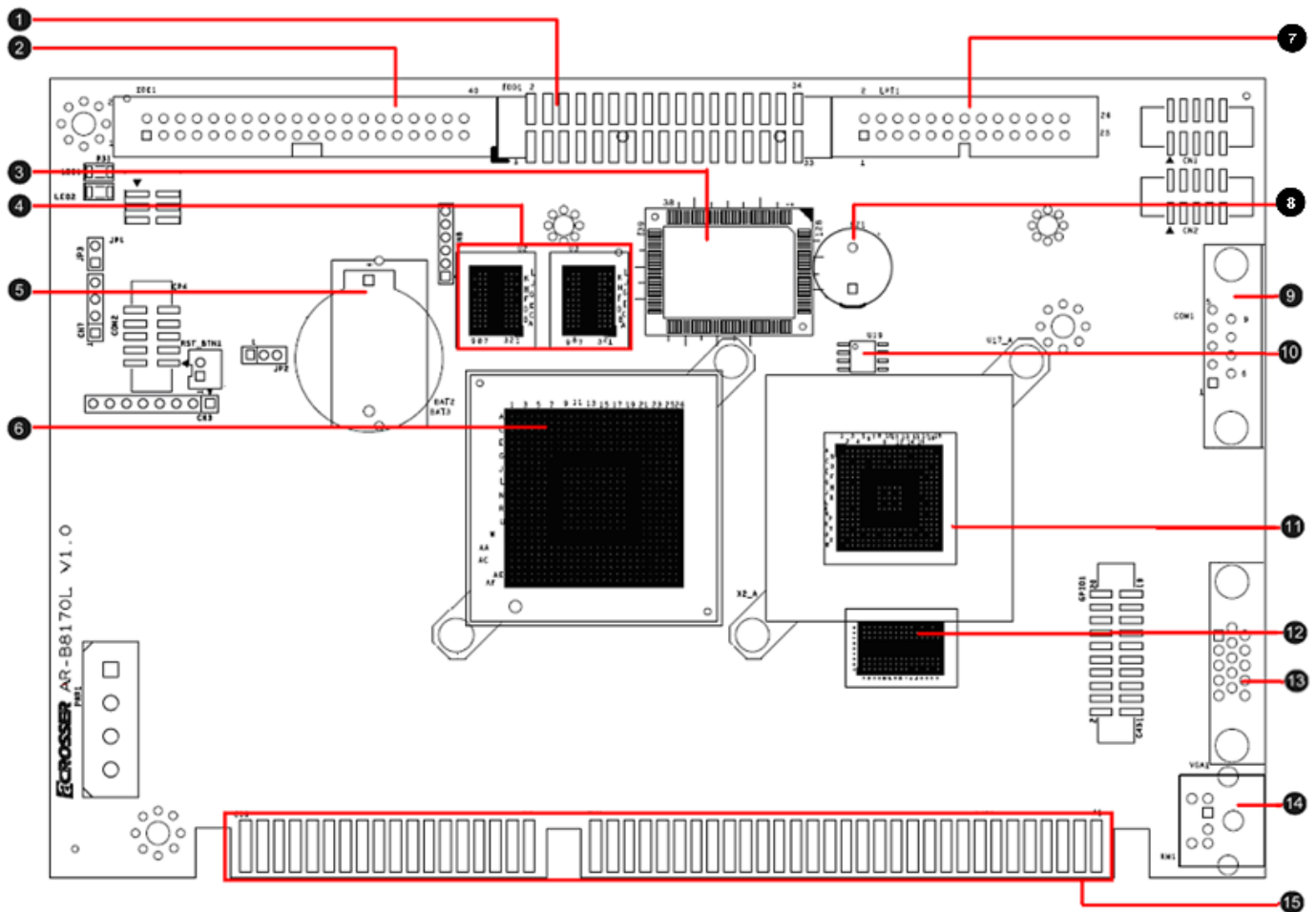


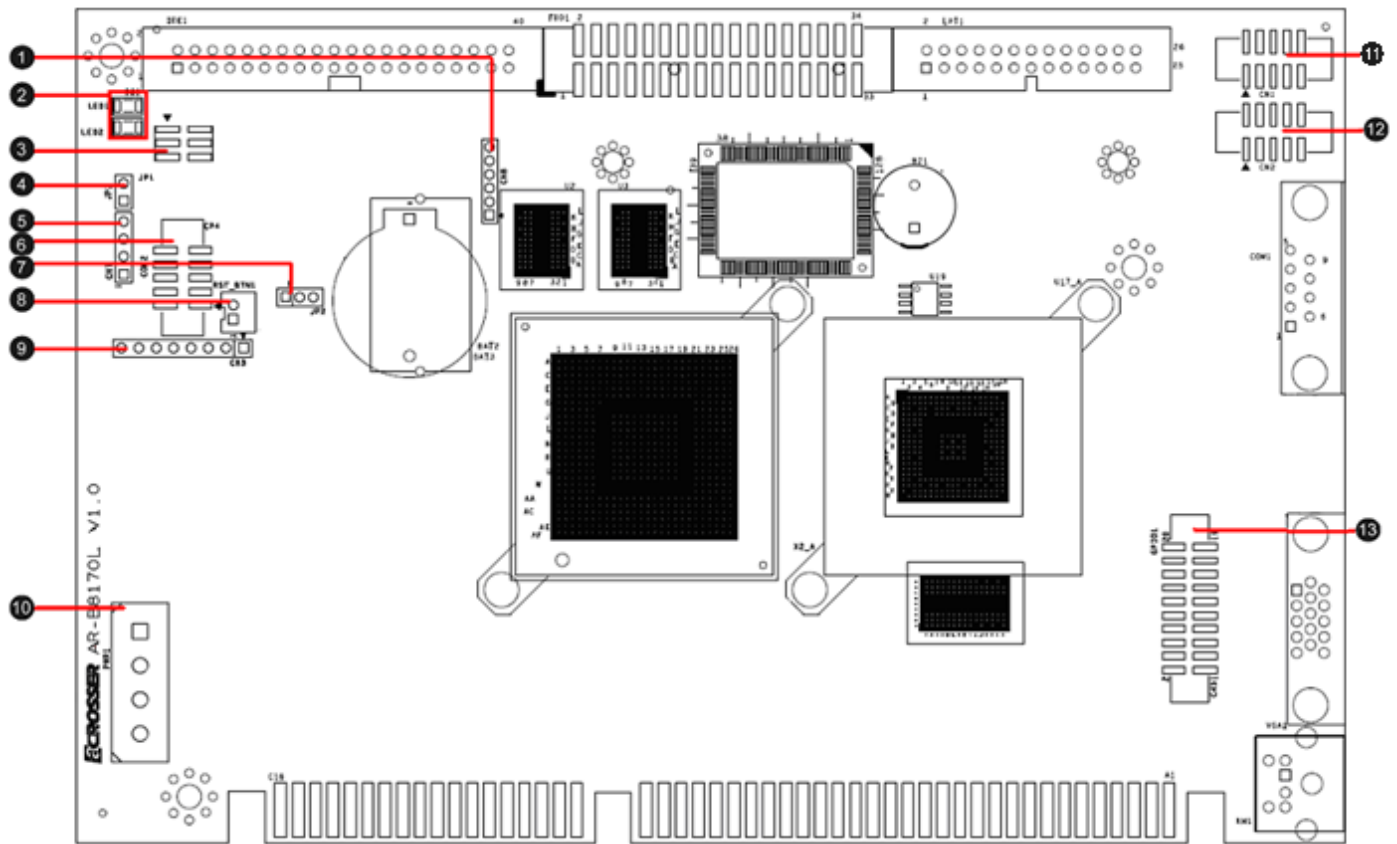
# AR-B8170L Quick Manual

## 1. Mainboard illustration (Top Side)




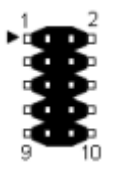

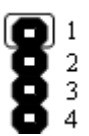
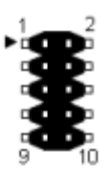
① <b>FDD1</b> FLOPPY CONNECTOR	⑥ <b>U1</b> CPU Vortex DX 800MHz	⑪ <b>U17</b> GPU XGI z9s
② <b>IDE1</b> 39PIN IDE CONNECTOR	⑦ <b>LPT1</b> PRINTER PORT	⑫ <b>U18</b> GPU DDR2 64MB
③ <b>U31 (Optional)</b> SUPER IO W83697HG	⑧ <b>BZ1</b> BUZZER	⑬ <b>VGA1</b> VGA DB15 CONNECTOR
④ <b>U2, U3</b> CPU DDR2 128MBx2	⑨ <b>COM1</b> RS232 DB9 CONNECTOR	⑭ <b>KM1</b> Keyboard/Mouse CONNECTOR
⑤ <b>BAT3</b> BATTERY for RTC/SRAM	⑩ <b>U19</b> VGA SPI FLASH 512KB	⑮ <b>ISA1</b> ISA BUS


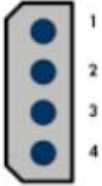


## 2.Pin definitions & Jumper settings (Top Side)



① <b>CN8</b> Reserved	⑥ <b>COM2</b> RS232 CONNECTOR	⑪ <b>CN1</b> USB 0/1 CONNECTOR
② <b>LED1/LED2</b> SYSTEM STATUS LED	⑦ <b>JP2</b> Clear CMOS DATA	⑫ <b>CN2</b> USB 2/3 CONNECTOR
③ <b>JP1</b> RS232/422/485 SELECTION	⑧ <b>RST_BTN1 (Optional)</b> Reserved	⑬ <b>GPIO1</b> GPIO 0/1 CONNECTOR
④ <b>JP3</b> RS422/485 TERMINATION SELECTION	⑨ <b>CN3</b> Reserved	
⑤ <b>CN7</b> RS422/485 CONNECTOR	⑩ <b>PWR1</b> POWER INPUT	

## 2.1 Connectors and Jumpers Setting

2.1 CN8		2.2 LED1/LED2																																			
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