

AR-B5230 Series Board

EPIC form factor, onboard VGA, LVDS with DDR2-SODIMM

Built in two LAN, CF type-II

User Manual

Manual Rev.: 2.0

Book Number: AR-B6050-2011.02.11

Revision

Version	Date	Author	Description
1.0	2009/07/02	Roger Nan	Draft
2.0	2011/02/11	Roger Nan	Modified

Copyright 2011

All Rights Reserved.

Manual's first edition:

For the purpose of improving reliability, design and function, the information in this document is subject to change without prior notice and does not represent a commitment on the part of the manufacturer.

In no event will the manufacturer be liable for direct, indirect, special, incidental, or consequential damages arising out of the use or inability to use the product or documentation, even if advised of the possibility of such damages.

This document contains proprietary information protected by copyright. All rights are reserved. No part of this Manual may be reproduced by any mechanical, electronic, or other means in any form without prior written permission of the manufacturer.

Trademarks

AR-B5230 Series is a registered trademarks of Acrosser; IBM PC is a registered trademark of the International Business Machines Corporation; Pentium is a registered trademark of Intel Technologies Inc; Award is a registered trademark of Award Software International Inc; other product names mentioned herein are used for identification purposes only and may be trademarks and/or registered trademarks of their respective companies.

Table of Contents

1 Introduction	5
1.1 Specifications	6
1.2 Package Contents	8
1.3 Block Diagram	9
2 H/W Information	10
2.1 Locations of Connector and Jumper Setting.....	11
2.2 Connector and Jumper Setting Table	13
3 Bios Setting	18
3.1 MAIN SETUP	19
3.2 Advanced Chipset Setup	21
3.3 Power Setup.....	23
3.4 PnP/PCI Setup	24
3.5 Peripherals Setup	25
3.6 PC Health Setup.....	27
3.7 Boot Setup	28
3.8 Exit Setup	29

1 INTRODUCTION

Welcome to the AR-B5230 Series (5230/5231) EPIC board. The AR-B5230 Series (5230/5231) incorporates the advanced Intel® 915GME(5230)/910GML(5231) Chipset. It supports the Pentium M and Celeron M processors, while coming with a 400MHz Front Side Bus.

1.1 Specifications

CPU: Socket for Intel uFC-PGA 478 for Pentium M, Celeron M, Coolers required

Support CPU type:

- CM-0.6G/400/512K/BGA(AR-B5231)
- CM-1.0G/400/0K/BGA(AR-B5231)
- CM-1.3G/400/512K/PGA(320)
- CM-1.5G/400/1M/PGA(370)
- PM-1.6G/400/1M/PGA
- PM-2.0G/533/2M/PGA(760)
- PM-1.8G/400/2M/PGA(745)

BIOS: AWARD

System Chipset: Intel 915GME(5230)/910GMLE(5231) + ICH6M

System Memory: One SO-DIMM socket support 400/533MHz(5230) 400MHz(5231) DDR2 SDRAM up to 1GB

Graphic controller: Internal Intel 82915GME(5230)/82910GMLE(5231) integrated GMA 900 graphic controller

VGA Memory: Intel DVMT 3.0 supports Max 128 MB shared video memory

Display mode:

- CRT (always on)
- DVII
- LCD : Dual Channel 18-bits LVDS Interface

Audio: AC'97 Audio out/Audio in/Mic in

Ethernet:

- Intel 82562EZ 10/100Mbps LAN PHY
- Intel 82541PI Giga LAN controller

Storage:

- One PATA
- One SATA
- One CF: Compact Flash Type-II support UDMA

Serial port:

- One RS232 (COM1)

- One RS232/422/485 (COM2)
- Two RS232 (COM3, COM4)

USB:

- Two external ports
- Two internal ports

PCI-104 slot:

PS/2: One PS/2 connector for keyboard and mouse

GPIO: 8 bit GPIO

Watch dog: Software programmable 1~63 Seconds

Hardware monitor:

- CPU voltage
- CPU and System temperature

CPU/System Fan speed control:

- AT : 12V single voltage input (BIOS default)
- ATX: Power switch pin header and pin header for external 5VSB input

Battery: Lihium Battery, 3V/220mAH

OS: Win XP, Win XP Embedded, Win CE, Linux

Dimension: 115mm x 165mm (4.528 x 6.496 inches)

Operating Temperature: 0~60°C (32~140°F)

Storage Temperature: -20~80°C (-4~176°F)

Relative Humidity: 0 to 90% @ 40°C, non-condensing (95% @ 40°C, Non-Condensing by request)

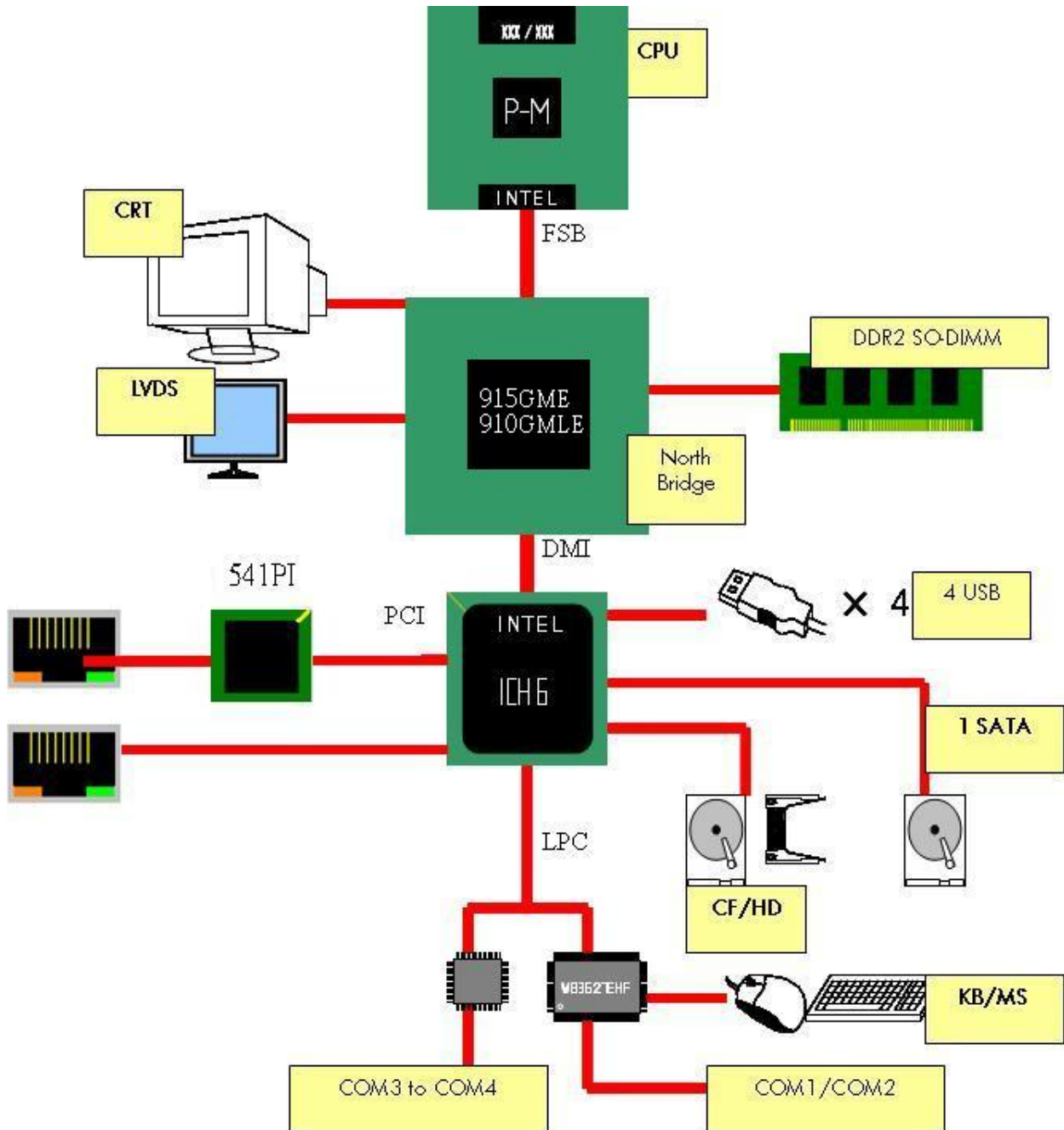
EMC: CE, FCC Class A

1.2 Package Contents

Before you begin to install your AR-B5230 Series (5230/5231) board, please make sure that the following items are inside the AR-B5230 Series (5230/5231) package.

- The quick manual x1
- AR-B5230 Series (5230/5231) board x1
- Software utility CD x1
- Fan module x1
- Power cable for AT x1
- Power cable for ATX x1
- COM port cable x2
- KB/MS cable x1
- 40/44 pin IDE connector x1
- USB cable x1
- Audio cable x1
- SATA cable x1
- TV out cable (AR-B5230SD only) x1

1.3 Block Diagram

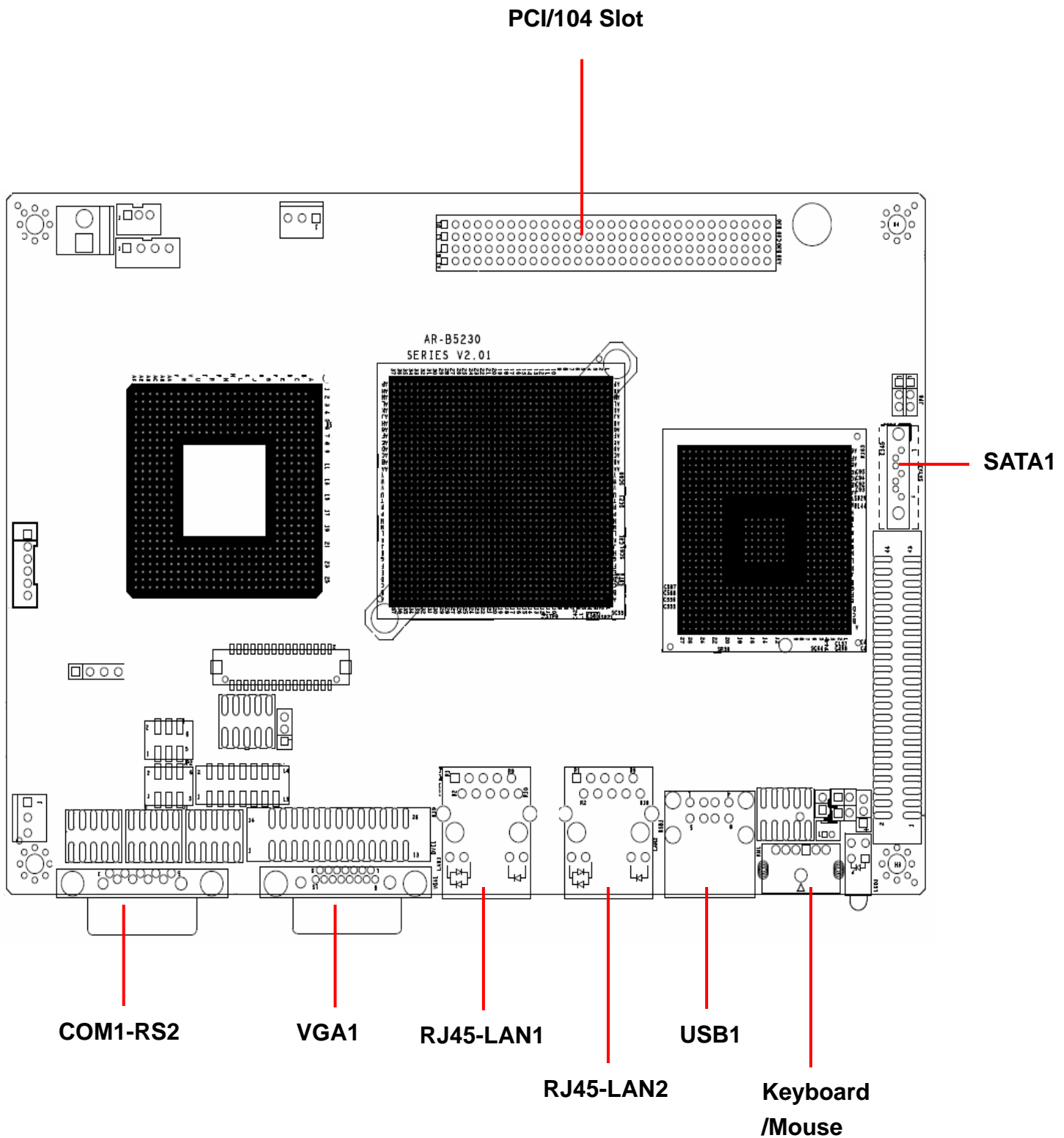


2 H/W INFORMATION

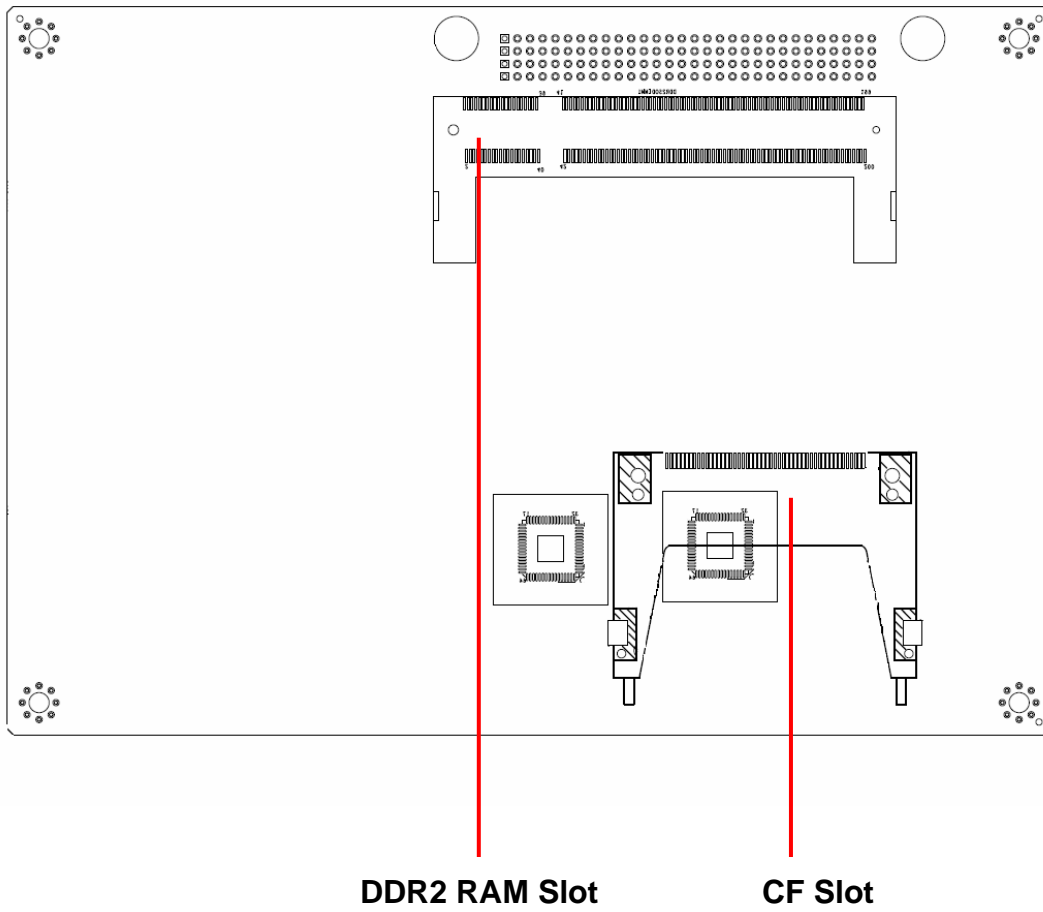
This chapter describes the installation of AR-B5230 Series (5230/5231). At first, it shows the Function diagram and the layout of AR-B5230 Series (5230/5231). It then describes the unpacking information which you should be careful with, as well as the jumper/switch settings for the AR-B5230 Series (5230/5231) configuration

2.1 Locations of Connector and Jumper Setting

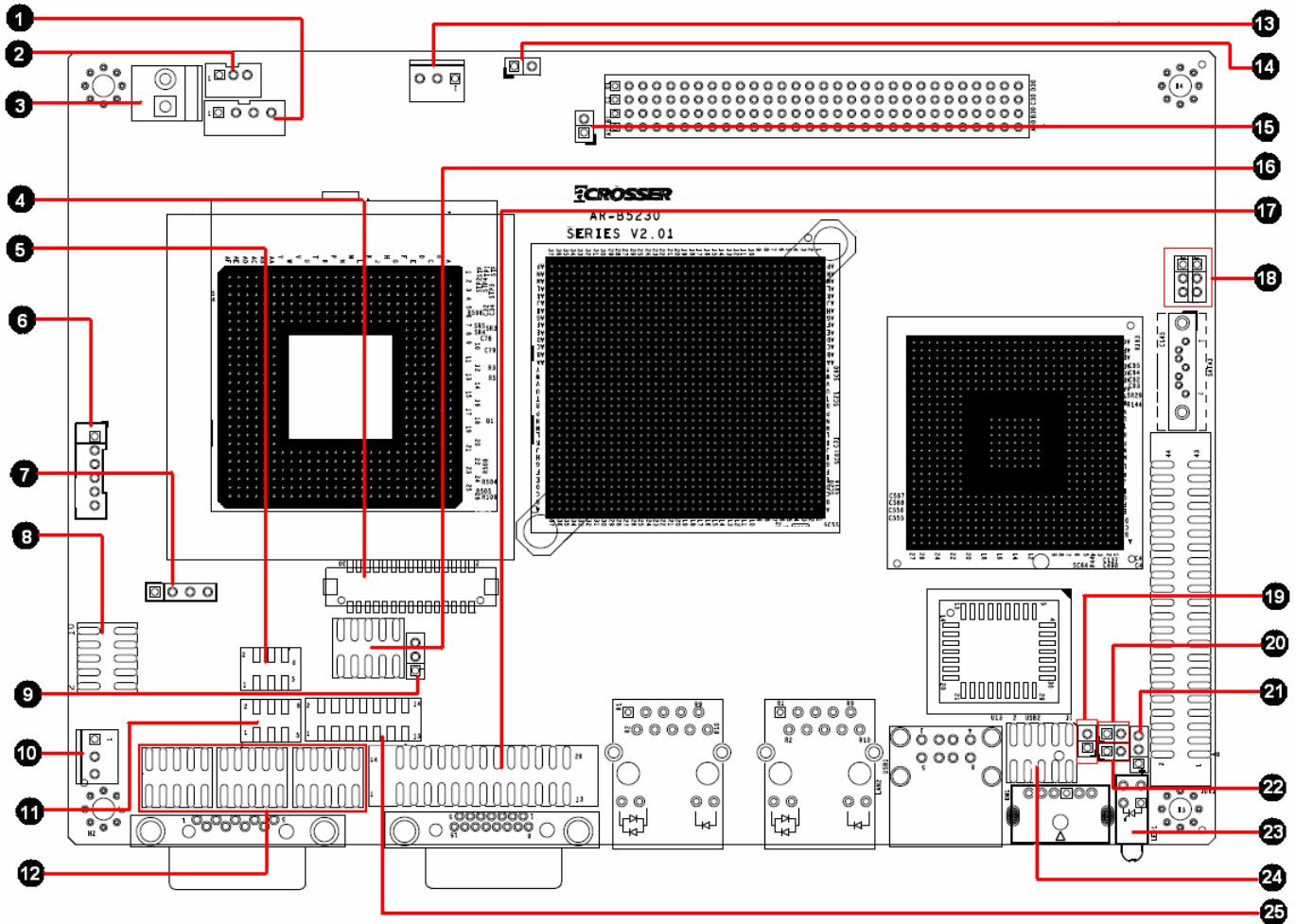
2.1.1 Locations (Top side)



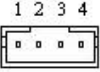


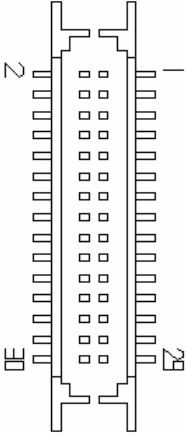

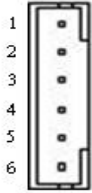
2.1.2 Locations (Bottom Side)

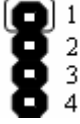
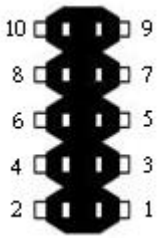
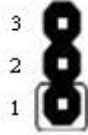
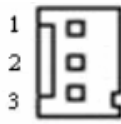


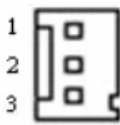




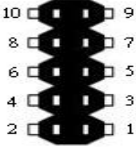




2.2 Connector and Jumper Setting Table



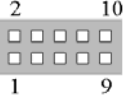
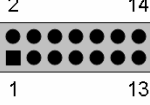


① J5	⑩ FAN2	⑱ JP3
② CN5	⑪ J1	⑳ JP4
③ CN4	⑫ COM2,COM3,COM4	㉑ JP1
④ LVDS1	⑬ FAN1	㉒ J3
⑤ JP2	⑭ JP7	㉓ LED1
⑥ CN3	⑮ JP6	㉔ USB2
⑦ J2	⑯ GPIO1	㉕ TVCON1
⑧ AUDIO1	⑰ DVI1	
⑨ JP5	⑱ JP8/JP9	

1. J5: SATA POWER.		2. CN5: 5V_SUS/PS_ON/GND FOR ATX.																																																																							
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>12V</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>5V</td> </tr> <tr> <td>4</td> <td>5V</td> </tr> </tbody> </table>	PIN	SIGNAL	1	12V	2	GND	3	5V	4	5V		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> </tr> <tr> <td>2</td> <td>PS-ON</td> </tr> <tr> <td>3</td> <td>5V-SUS</td> </tr> </tbody> </table>	PIN	SIGNAL	1	GND	2	PS-ON	3	5V-SUS																																																				
PIN	SIGNAL																																																																								
1	12V																																																																								
2	GND																																																																								
3	5V																																																																								
4	5V																																																																								
PIN	SIGNAL																																																																								
1	GND																																																																								
2	PS-ON																																																																								
3	5V-SUS																																																																								
3. CN4: 12V Power in connector.		4. LVDS: Connector for LVDS signals.																																																																							
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>12V</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> </tbody> </table>	PIN	SIGNAL	1	12V	2	GND		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>VDD</td> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>TXCLK U-</td> <td>4</td> <td>TXCLK U+</td> </tr> <tr> <td>5</td> <td>GND</td> <td>6</td> <td>TXOUT U2-</td> </tr> <tr> <td>7</td> <td>TXOUT U2+</td> <td>8</td> <td>GND</td> </tr> <tr> <td>9</td> <td>TXOUT U1-</td> <td>10</td> <td>TXOUT U1+</td> </tr> <tr> <td>11</td> <td>NC</td> <td>12</td> <td>NC</td> </tr> <tr> <td>13</td> <td>TXOUT U0+</td> <td>14</td> <td>TXOUT U0-</td> </tr> <tr> <td>15</td> <td>GND</td> <td>16</td> <td>TXCLK L+</td> </tr> <tr> <td>17</td> <td>TXCLK L-</td> <td>18</td> <td>CLOCK-</td> </tr> <tr> <td>19</td> <td>TXOUT L2+</td> <td>20</td> <td>TXOUT L2-</td> </tr> <tr> <td>21</td> <td>SMBCLK</td> <td>22</td> <td>TXOUT L1+</td> </tr> <tr> <td>23</td> <td>TXOUT L1-</td> <td>24</td> <td>SMBDATA</td> </tr> <tr> <td>25</td> <td>TXOUT L0+</td> <td>26</td> <td>TXOUT L0-</td> </tr> <tr> <td>27</td> <td>NC</td> <td>28</td> <td>NC</td> </tr> <tr> <td>29</td> <td>VDD</td> <td>30</td> <td>VDD</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	VDD	2	GND	3	TXCLK U-	4	TXCLK U+	5	GND	6	TXOUT U2-	7	TXOUT U2+	8	GND	9	TXOUT U1-	10	TXOUT U1+	11	NC	12	NC	13	TXOUT U0+	14	TXOUT U0-	15	GND	16	TXCLK L+	17	TXCLK L-	18	CLOCK-	19	TXOUT L2+	20	TXOUT L2-	21	SMBCLK	22	TXOUT L1+	23	TXOUT L1-	24	SMBDATA	25	TXOUT L0+	26	TXOUT L0-	27	NC	28	NC	29	VDD	30	VDD
PIN	SIGNAL																																																																								
1	12V																																																																								
2	GND																																																																								
PIN	SIGNAL	PIN	SIGNAL																																																																						
1	VDD	2	GND																																																																						
3	TXCLK U-	4	TXCLK U+																																																																						
5	GND	6	TXOUT U2-																																																																						
7	TXOUT U2+	8	GND																																																																						
9	TXOUT U1-	10	TXOUT U1+																																																																						
11	NC	12	NC																																																																						
13	TXOUT U0+	14	TXOUT U0-																																																																						
15	GND	16	TXCLK L+																																																																						
17	TXCLK L-	18	CLOCK-																																																																						
19	TXOUT L2+	20	TXOUT L2-																																																																						
21	SMBCLK	22	TXOUT L1+																																																																						
23	TXOUT L1-	24	SMBDATA																																																																						
25	TXOUT L0+	26	TXOUT L0-																																																																						
27	NC	28	NC																																																																						
29	VDD	30	VDD																																																																						
5. JP2: Switch for select com2 RS232/422/485.		6. CN3: Connector for Back Light Inverter.																																																																							
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>RS-232</td> </tr> <tr> <td>3-4</td> <td>RS-422</td> </tr> <tr> <td>5-6</td> <td>RS-485</td> </tr> </tbody> </table>	PIN	SIGNAL	1-2	RS-232	3-4	RS-422	5-6	RS-485		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>12V</td> <td>4</td> <td>Back Light On</td> </tr> <tr> <td>2</td> <td>12V</td> <td>5</td> <td>GND</td> </tr> <tr> <td>3</td> <td>GND</td> <td>6</td> <td>Back Light Control</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	12V	4	Back Light On	2	12V	5	GND	3	GND	6	Back Light Control																																														
PIN	SIGNAL																																																																								
1-2	RS-232																																																																								
3-4	RS-422																																																																								
5-6	RS-485																																																																								
PIN	SIGNAL	PIN	SIGNAL																																																																						
1	12V	4	Back Light On																																																																						
2	12V	5	GND																																																																						
3	GND	6	Back Light Control																																																																						

7. J2: Pin header for RS422/485.		8. AUDIO1: MIC & Line-in & Line-out.																																			
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>TX+</td> </tr> <tr> <td>2</td> <td>TX-</td> </tr> <tr> <td>3</td> <td>RX+</td> </tr> <tr> <td>4</td> <td>RX-</td> </tr> </tbody> </table>	PIN	SIGNAL	1	TX+	2	TX-	3	RX+	4	RX-		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>R-OUT</td> <td>2</td> <td>L-OUT</td> </tr> <tr> <td>3</td> <td>GND</td> <td>4</td> <td>GND</td> </tr> <tr> <td>5</td> <td>R-IN</td> <td>6</td> <td>L-IN</td> </tr> <tr> <td>7</td> <td>MIC-IN</td> <td>8</td> <td>GND</td> </tr> <tr> <td>9</td> <td>GND</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	R-OUT	2	L-OUT	3	GND	4	GND	5	R-IN	6	L-IN	7	MIC-IN	8	GND	9	GND	10	GND
PIN	SIGNAL																																				
1	TX+																																				
2	TX-																																				
3	RX+																																				
4	RX-																																				
PIN	SIGNAL	PIN	SIGNAL																																		
1	R-OUT	2	L-OUT																																		
3	GND	4	GND																																		
5	R-IN	6	L-IN																																		
7	MIC-IN	8	GND																																		
9	GND	10	GND																																		
9. JP5: Switch for select 3V or 5V Panel.		10. FAN2: System Fan connector.																																			
	<table border="1"> <thead> <tr> <th>STATUS</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>2-3</td> <td>5V</td> </tr> <tr> <td>1-2</td> <td>3.3V</td> </tr> </tbody> </table>	STATUS	SETTING	2-3	5V	1-2	3.3V		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> </tr> <tr> <td>2</td> <td>12V</td> </tr> <tr> <td>3</td> <td>DATA</td> </tr> </tbody> </table>	PIN	SIGNAL	1	GND	2	12V	3	DATA																				
STATUS	SETTING																																				
2-3	5V																																				
1-2	3.3V																																				
PIN	SIGNAL																																				
1	GND																																				
2	12V																																				
3	DATA																																				
11. J1: Pin header for speaker& reset& power on.		12. COM2&COM3&COM4: Serial Port COM2 & COM3 & COM4.																																			
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>SPEAKER</td> </tr> <tr> <td>3-4</td> <td>RESET</td> </tr> <tr> <td>5-6</td> <td>POWER BUTTON</td> </tr> </tbody> </table>	PIN	SIGNAL	1-2	SPEAKER	3-4	RESET	5-6	POWER BUTTON		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>DCD</td> <td>2</td> <td>DSR</td> </tr> <tr> <td>3</td> <td>RX</td> <td>4</td> <td>RTS</td> </tr> <tr> <td>5</td> <td>TX</td> <td>6</td> <td>CTS</td> </tr> <tr> <td>7</td> <td>DTR</td> <td>8</td> <td>RI</td> </tr> <tr> <td>9</td> <td>GND</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	DCD	2	DSR	3	RX	4	RTS	5	TX	6	CTS	7	DTR	8	RI	9	GND	10	GND		
PIN	SIGNAL																																				
1-2	SPEAKER																																				
3-4	RESET																																				
5-6	POWER BUTTON																																				
PIN	SIGNAL	PIN	SIGNAL																																		
1	DCD	2	DSR																																		
3	RX	4	RTS																																		
5	TX	6	CTS																																		
7	DTR	8	RI																																		
9	GND	10	GND																																		
13. FAN1: CPU Fan connector.		14. JP7: Switch for Dothan or Banines.																																			
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> </tr> <tr> <td>2</td> <td>12V</td> </tr> <tr> <td>3</td> <td>DATA</td> </tr> </tbody> </table>	PIN	SIGNAL	1	GND	2	12V	3	DATA		<table border="1"> <thead> <tr> <th>STATUS</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>OPEN</td> <td>1.8V(BANINES)</td> </tr> <tr> <td>CLOSE</td> <td>1.5V(DOTHAN)</td> </tr> </tbody> </table>	STATUS	SETTING	OPEN	1.8V(BANINES)	CLOSE	1.5V(DOTHAN)																				
PIN	SIGNAL																																				
1	GND																																				
2	12V																																				
3	DATA																																				
STATUS	SETTING																																				
OPEN	1.8V(BANINES)																																				
CLOSE	1.5V(DOTHAN)																																				

15. JP6: Pin header for SERIRQ function.		16. GPIO1: GPIO connector.																																																																		
	<table border="1"> <thead> <tr> <th>STATUS</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>OPEN</td> <td>Disable</td> </tr> <tr> <td>CLOSE</td> <td>Enable (default)</td> </tr> </tbody> </table>	STATUS	SETTING	OPEN	Disable	CLOSE	Enable (default)		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GPIO0</td> <td>2</td> <td>VCC</td> </tr> <tr> <td>3</td> <td>GPIO1</td> <td>4</td> <td>GPIO7</td> </tr> <tr> <td>5</td> <td>GPIO2</td> <td>6</td> <td>GPIO6</td> </tr> <tr> <td>7</td> <td>GPIO3</td> <td>8</td> <td>GPIO5</td> </tr> <tr> <td>9</td> <td>GND</td> <td>10</td> <td>GPIO4</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	GPIO0	2	VCC	3	GPIO1	4	GPIO7	5	GPIO2	6	GPIO6	7	GPIO3	8	GPIO5	9	GND	10	GPIO4																																			
STATUS	SETTING																																																																			
OPEN	Disable																																																																			
CLOSE	Enable (default)																																																																			
PIN	SIGNAL	PIN	SIGNAL																																																																	
1	GPIO0	2	VCC																																																																	
3	GPIO1	4	GPIO7																																																																	
5	GPIO2	6	GPIO6																																																																	
7	GPIO3	8	GPIO5																																																																	
9	GND	10	GPIO4																																																																	
17. DVI1: DVI-D connector.		18. JP8/JP9: FSB select jumper.																																																																		
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> <td>26</td> <td>TD0+</td> </tr> <tr> <td>2</td> <td>TD0-</td> <td>25</td> <td>GND</td> </tr> <tr> <td>3</td> <td>TD1+</td> <td>24</td> <td>TD1-</td> </tr> <tr> <td>4</td> <td>GND</td> <td>23</td> <td>TD2+</td> </tr> <tr> <td>5</td> <td>TD2-</td> <td>22</td> <td>GND</td> </tr> <tr> <td>6</td> <td>TCK+</td> <td>21</td> <td>TCK-</td> </tr> <tr> <td>7</td> <td>HPD</td> <td>20</td> <td>SCL1</td> </tr> <tr> <td>8</td> <td>VCC</td> <td>19</td> <td>SDATA1</td> </tr> <tr> <td>9</td> <td>RED</td> <td>18</td> <td>GND</td> </tr> <tr> <td>10</td> <td>GREEN</td> <td>17</td> <td>GND</td> </tr> <tr> <td>11</td> <td>BLUE</td> <td>16</td> <td>GND</td> </tr> <tr> <td>12</td> <td>VSYNC</td> <td>15</td> <td>SCL2</td> </tr> <tr> <td>13</td> <td>HSYNC</td> <td>14</td> <td>SDATA2</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	GND	26	TD0+	2	TD0-	25	GND	3	TD1+	24	TD1-	4	GND	23	TD2+	5	TD2-	22	GND	6	TCK+	21	TCK-	7	HPD	20	SCL1	8	VCC	19	SDATA1	9	RED	18	GND	10	GREEN	17	GND	11	BLUE	16	GND	12	VSYNC	15	SCL2	13	HSYNC	14	SDATA2		<table border="1"> <thead> <tr> <th>FSB</th> <th>JP8</th> <th>JP9</th> </tr> </thead> <tbody> <tr> <td>100MHz</td> <td>2-3</td> <td>1-2</td> </tr> <tr> <td>133MHz</td> <td>2-3</td> <td>2-3</td> </tr> </tbody> </table>	FSB	JP8	JP9	100MHz	2-3	1-2	133MHz	2-3	2-3
PIN	SIGNAL	PIN	SIGNAL																																																																	
1	GND	26	TD0+																																																																	
2	TD0-	25	GND																																																																	
3	TD1+	24	TD1-																																																																	
4	GND	23	TD2+																																																																	
5	TD2-	22	GND																																																																	
6	TCK+	21	TCK-																																																																	
7	HPD	20	SCL1																																																																	
8	VCC	19	SDATA1																																																																	
9	RED	18	GND																																																																	
10	GREEN	17	GND																																																																	
11	BLUE	16	GND																																																																	
12	VSYNC	15	SCL2																																																																	
13	HSYNC	14	SDATA2																																																																	
FSB	JP8	JP9																																																																		
100MHz	2-3	1-2																																																																		
133MHz	2-3	2-3																																																																		
20. JP4: CF card Master/Slave select jumper.		21. JP1: CMOS clear jumper.																																																																		
	<table border="1"> <thead> <tr> <th>SET</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>SHORT</td> <td>MASTER</td> </tr> <tr> <td>OPEN</td> <td>SLAVE</td> </tr> </tbody> </table>	SET	SIGNAL	SHORT	MASTER	OPEN	SLAVE		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>On-board battery (Default)</td> </tr> <tr> <td>2-3</td> <td>Clear CMOS</td> </tr> </tbody> </table>	PIN	SIGNAL	1-2	On-board battery (Default)	2-3	Clear CMOS																																																					
SET	SIGNAL																																																																			
SHORT	MASTER																																																																			
OPEN	SLAVE																																																																			
PIN	SIGNAL																																																																			
1-2	On-board battery (Default)																																																																			
2-3	Clear CMOS																																																																			

22. J3: Keyboard lock.		23. AUDIO1: MIC & Line-in & Line-out.																																																									
	<table border="1"> <thead> <tr> <th>SET</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>SHORT</td> <td>Unlock (Default)</td> </tr> <tr> <td>OPEN</td> <td>Lock</td> </tr> </tbody> </table>	SET	SIGNAL	SHORT	Unlock (Default)	OPEN	Lock		<p>Green: Power LED. Yellow: HDD LED.</p>																																																		
SET	SIGNAL																																																										
SHORT	Unlock (Default)																																																										
OPEN	Lock																																																										
24. USB2: Internal USB connector.		25. TVCON1: TV-out connector.																																																									
	<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>+5V</td> <td>2</td> <td>+5V</td> </tr> <tr> <td>3</td> <td>USB0-</td> <td>4</td> <td>USB1-</td> </tr> <tr> <td>5</td> <td>USB0+</td> <td>6</td> <td>USB1+</td> </tr> <tr> <td>7</td> <td>GND</td> <td>8</td> <td>GND</td> </tr> <tr> <td>9</td> <td>GND</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	+5V	2	+5V	3	USB0-	4	USB1-	5	USB0+	6	USB1+	7	GND	8	GND	9	GND	10	GND		<table border="1"> <thead> <tr> <th>PIN</th> <th>SIGNAL</th> <th>PIN</th> <th>SIGNAL</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Y-G</td> <td>2</td> <td>N/A</td> </tr> <tr> <td>3</td> <td>GND</td> <td>4</td> <td>N/A</td> </tr> <tr> <td>5</td> <td>CVBS/Pb-G</td> <td>6</td> <td>N/A</td> </tr> <tr> <td>7</td> <td>GND</td> <td>8</td> <td>N/A</td> </tr> <tr> <td>9</td> <td>C/Pr-G</td> <td>10</td> <td>N/A</td> </tr> <tr> <td>11</td> <td>GND</td> <td>12</td> <td>N/A</td> </tr> <tr> <td>13</td> <td>GND</td> <td>14</td> <td>N/A</td> </tr> </tbody> </table>	PIN	SIGNAL	PIN	SIGNAL	1	Y-G	2	N/A	3	GND	4	N/A	5	CVBS/Pb-G	6	N/A	7	GND	8	N/A	9	C/Pr-G	10	N/A	11	GND	12	N/A	13	GND	14	N/A
PIN	SIGNAL	PIN	SIGNAL																																																								
1	+5V	2	+5V																																																								
3	USB0-	4	USB1-																																																								
5	USB0+	6	USB1+																																																								
7	GND	8	GND																																																								
9	GND	10	GND																																																								
PIN	SIGNAL	PIN	SIGNAL																																																								
1	Y-G	2	N/A																																																								
3	GND	4	N/A																																																								
5	CVBS/Pb-G	6	N/A																																																								
7	GND	8	N/A																																																								
9	C/Pr-G	10	N/A																																																								
11	GND	12	N/A																																																								
13	GND	14	N/A																																																								

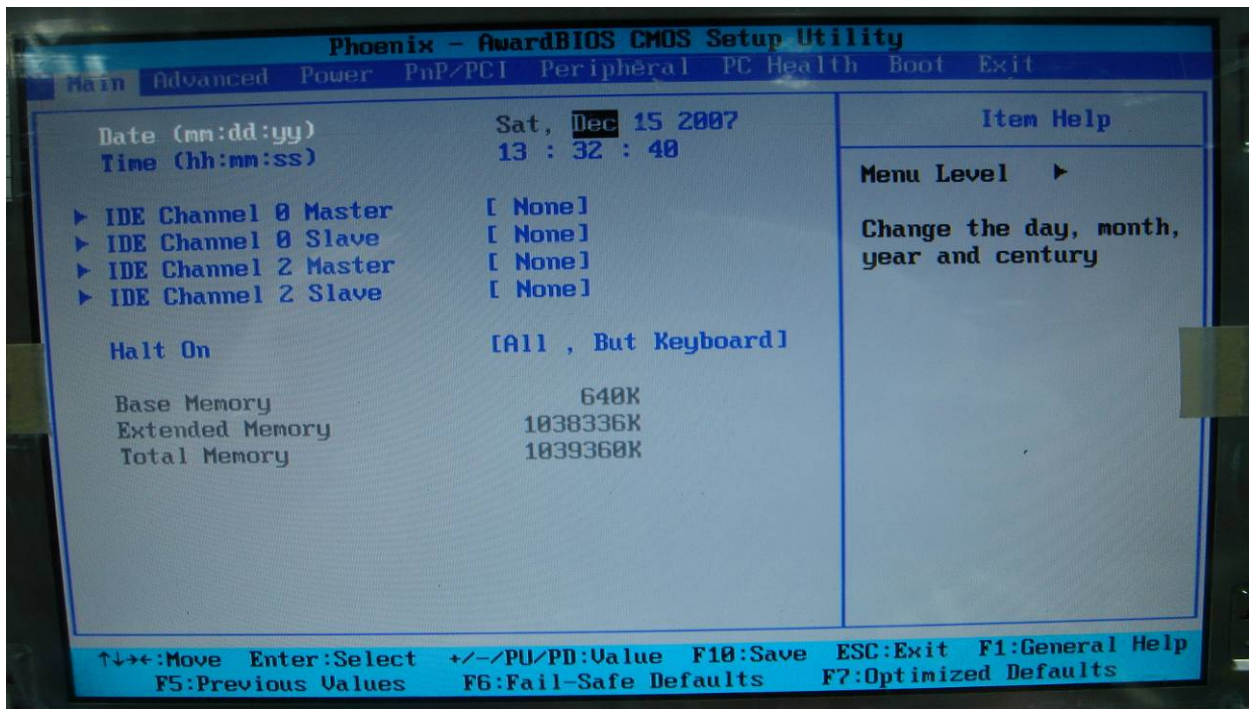
3 BIOS SETTING

This chapter describes the BIOS menu displays and explains how to perform common tasks needed to get up and running. It also gives detailed explanation of the elements found in each of the BIOS menus. The following topics are covered:

- Main Setup
- Advanced Chipset Setup
- Power Setup
- Peripherals Setup
- PnP/PCI Setup
- PC Health Setup
- Boot Setup
- Exit Setup

3.1 MAIN SETUP

Once you enter the AwardBIOS™ CMOS Setup Utility, the Main Menu will appear on the screen. Use the arrow keys to highlight the item and then use the <Pg Up> <Pg Dn> keys to select the value you want in each item.

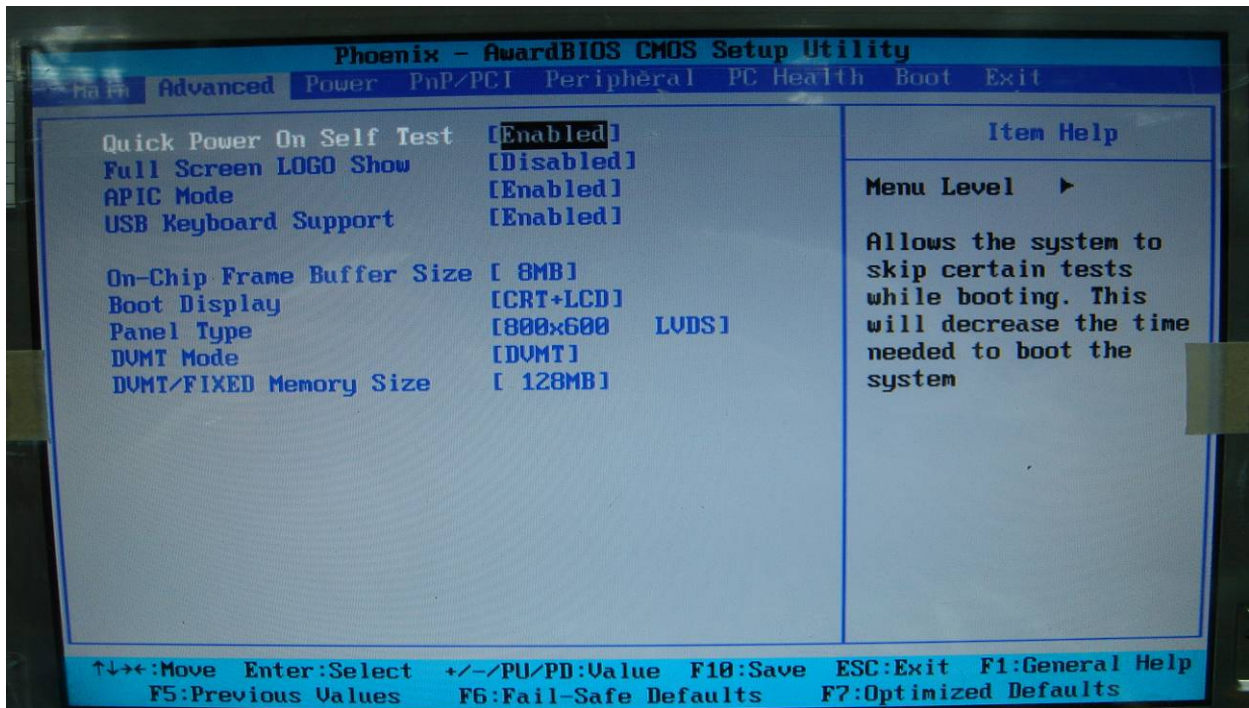


Note : Listed at the bottom of the menu are the control keys. If you need any help with the item fields, you can press the <F1> key, and it will display the relevant information.

Option	Choice	Description
Date Setup	N/A	Set the system date. Note that the 'Day' automatically changes when you set the date
Time Setup	N/A	Set the system time
IDE Channel 0 Master/Slave	N/A	The onboard PCI IDE connectors provide 1 channel for connecting up to 2 IDE hard disks or other devices. The first is the "Master" and the second is "Slave", BIOS will auto-detect the IDE type.
Halt On	All Errors, No Errors,	Select the situation in which you want the BIOS to stop the POST process and notify you.

	All but keyboard.	
--	----------------------	--

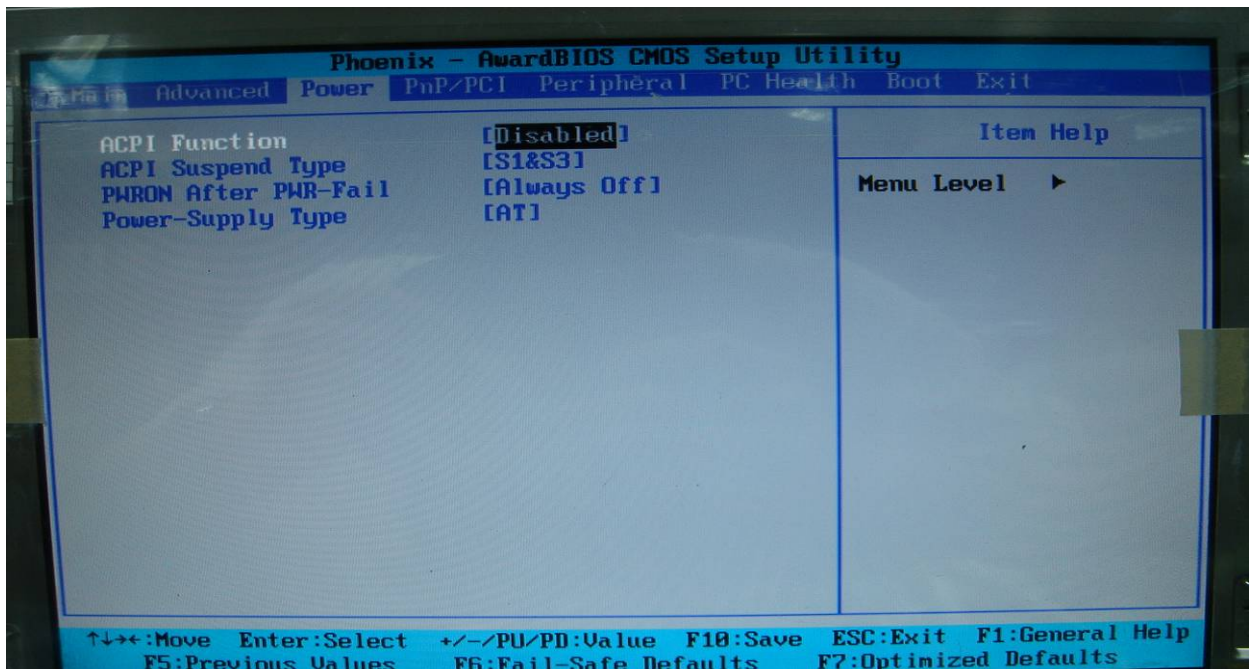
3.2 Advanced Chipset Setup



Option	Choice	Description
Quick Power On Self Test	Enabled Disabled	This category speeds up Power On Self Test (POST) after you have powered up the computer. If it is set to Enable, BIOS will shorten or skip some check items during POST.
Full Screen Logo Show	Enabled Disabled	Select <i>Enabled</i> to show the OEM full screen logo if you have add-in BIOS.
APIC Mode	Enabled Disabled	This item sets APIC(Default Disabled)
USB Keyboard Support	Enabled Disabled	Select <i>Enabled</i> if your system contains a Universal Serial Bus (USB)controller and you have a USB keyboard..
On-Chip Frame Buffer Size	1Mb 8Mb	This Item is for setting the Frame Buffer (Share system memory as display memory).
Boot Display	CRT LCD CRT+LCD TV	This Item is to set display device TV function only support on AR-B5230SD

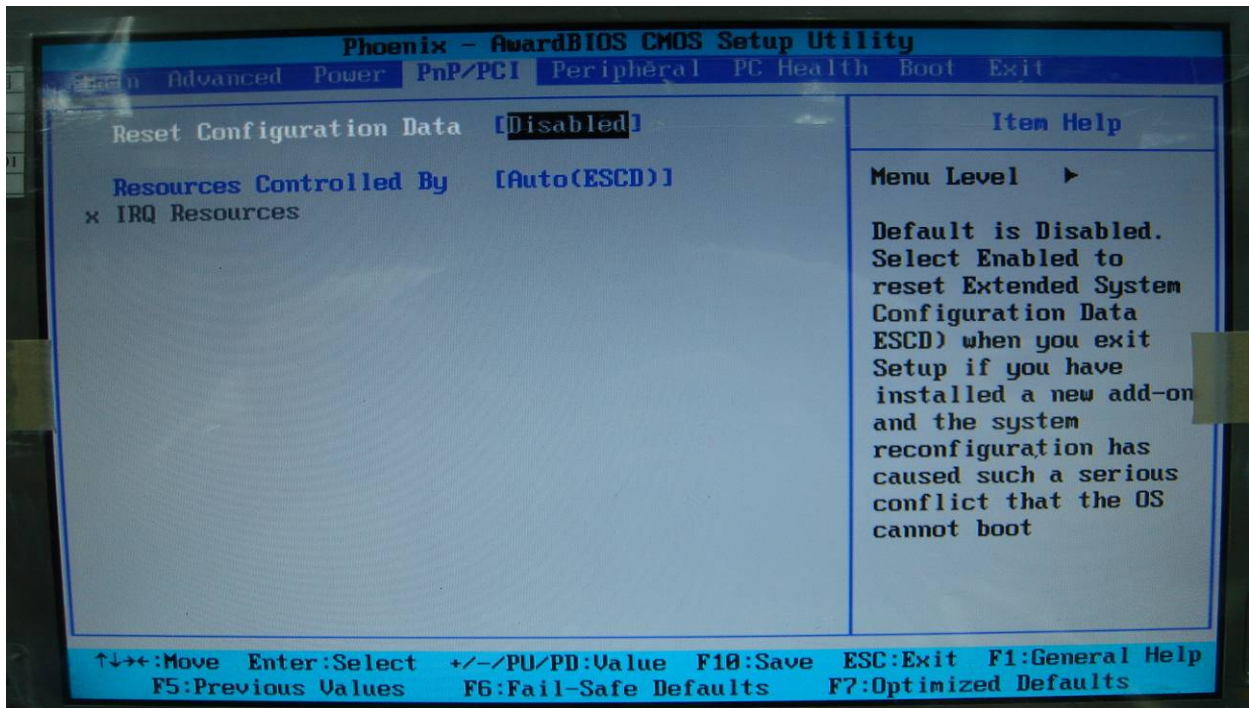
Panel Type	800x600, 1024x768, 1280x1024	This Item can Set the LVDS panel resolution that you want
DVMT mode	FIXED DVMT Both	This item sets the mode for dynamic video memory technology (DVMT).
DVMT/FIXED Memory Size	64Mb 128Mb	This item sets the DVMT size

3.3 Power Setup



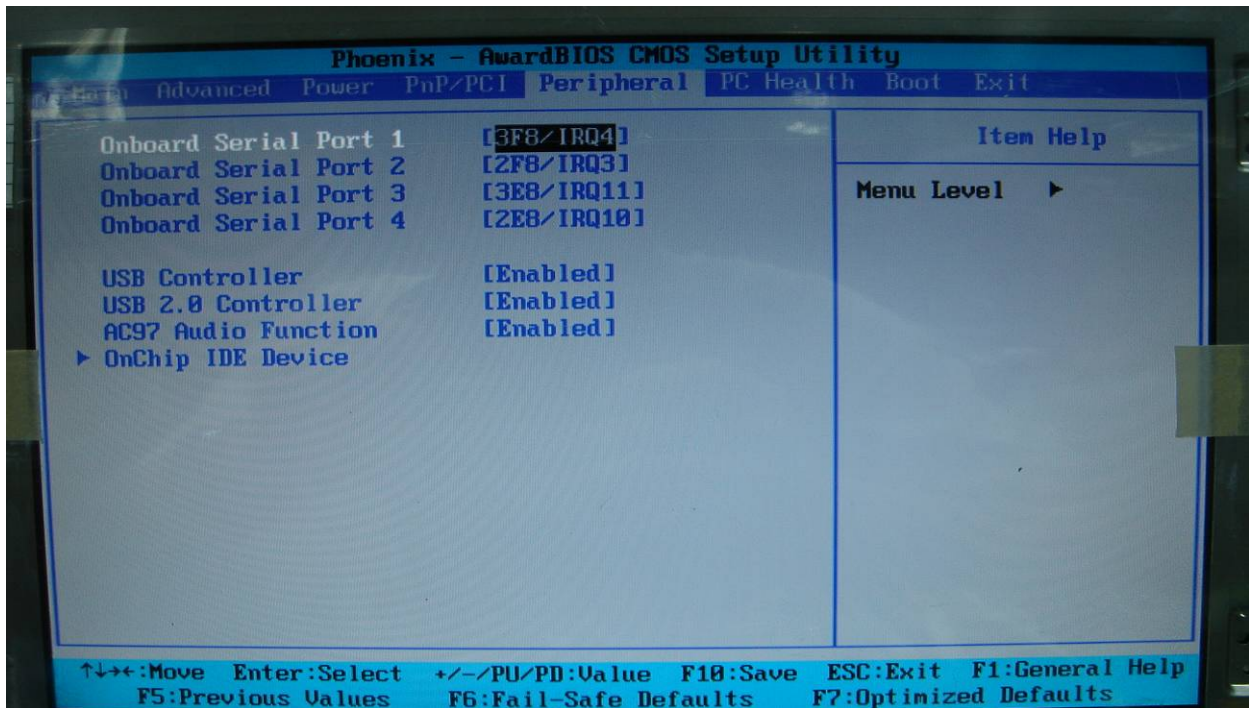
Option	Choice	Description
ACPI Function	Enabled Disabled	This item sets Advanced Configuration and Power Interface. When ATX mode is enabled and AT mode is disabled.
ACPI Suspend Type	S1&S3 S1(POS) S3(STR)	This item sets Advanced Configuration and Power Interface mode. It is asserted ACPI Function is enable.
PWRON After PWR-Fail	Always On Always Off Last State	This item sets the POWERON State (Default Always On).
Power-Supply Type	AT ATX	This item sets the ATX or AT POWER(Default AT).

3.4 PnP/PCI Setup



Option	Choice	Description
Reset Configuration Data	Enabled Disabled	Normally, you leave this field Disabled. Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup. If you have installed a new add-on and the system reconfiguration has caused such a serious conflict, then the operating system can not boot.
Resources Controlled By	Auto(ESCD) Manual	The Award Plug and Play BIOS has the capacity to automatically configure all of the boot and Plug and Play compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows 95. If you set this field to "manual," then you may choose specific resources by going into each of the submenus.
IRQ Resources	N/A	When resources are controlled manually, assign a type to each system interrupt, depending on the type of the device that uses the interrupt

3.5 Peripherals Setup

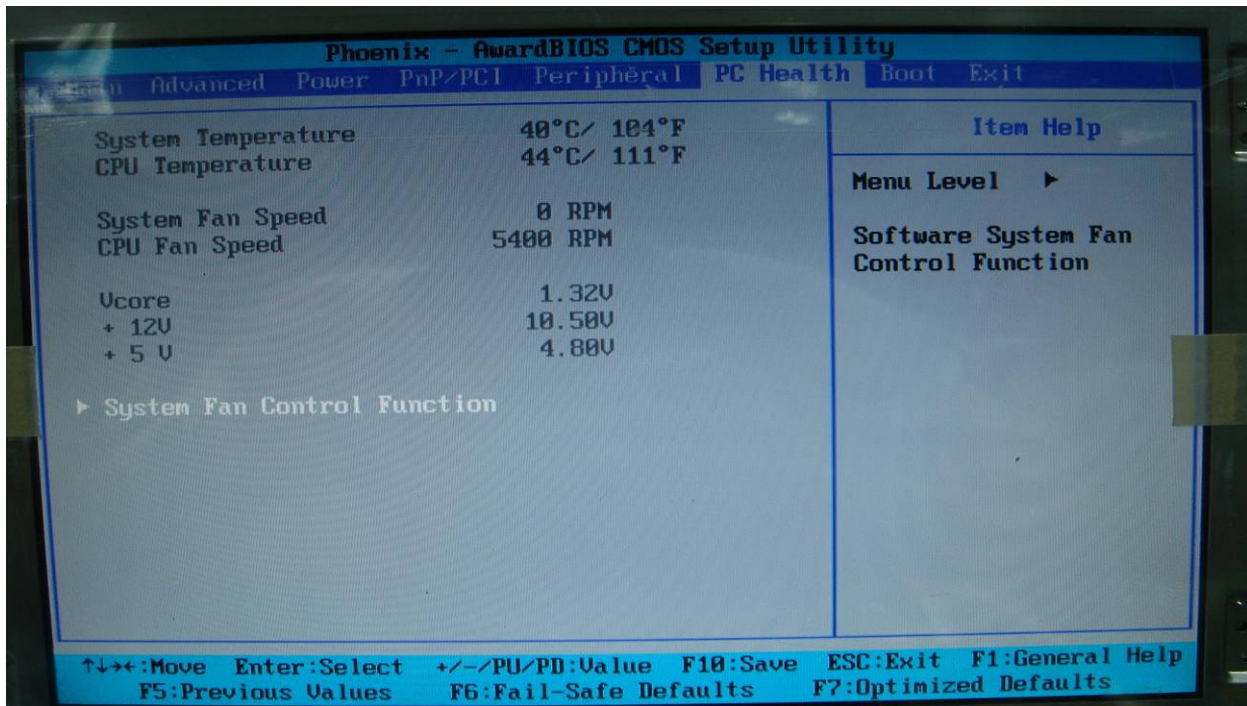


Option	Choice	Description
Onboard Serial Port 1 Onboard Serial Port 2 Onboard Serial Port 3 Onboard Serial Port 4	Serial Port 1: 3F8 / IRQ4 Serial Port 2: 2F8 / IRQ3 Serial Port 3: 3E8 / IRQ11 Serial Port 4: 2E8 / IRQ10	Select an address and the corresponding interrupt for each serial port
USB Controller	Enabled Disabled	Select <i>Enabled</i> if your system contains a Universal Serial Bus (USB) controller and you have USB peripherals
USB 2.0 Controller	Enabled Disabled	Select <i>Enabled</i> if your system contains a Universal Serial Bus (USB) 2.0 controller and you have USB peripherals
AC97 Audio Function	Enabled Disabled Audio/Modem	This item allows you to decide to enable/disable AC97 Audio

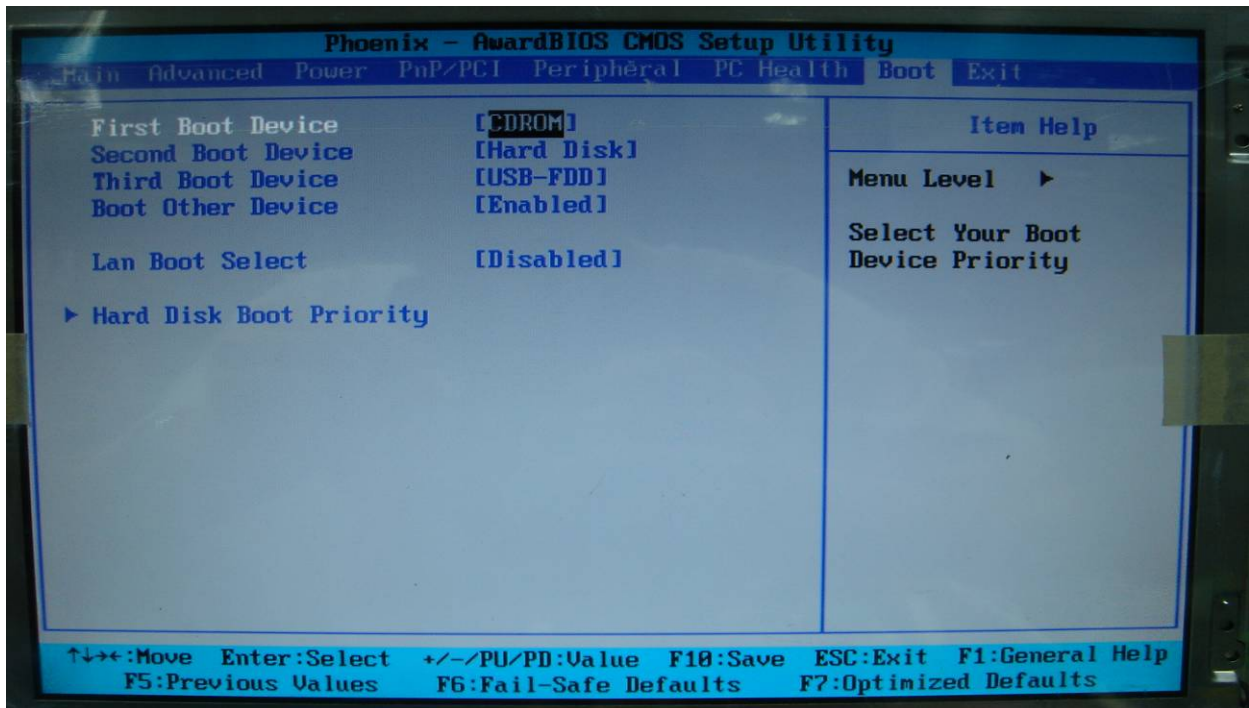
Onchip IDE DEVICE	Enabled Disabled	The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select <i>Enabled</i> to activate each channel separately.
-------------------	---------------------	--

3.6 PC Health Setup

This section shows the parameters in determining the PC Health Status. These parameters include temperatures, fan speeds, and voltages.

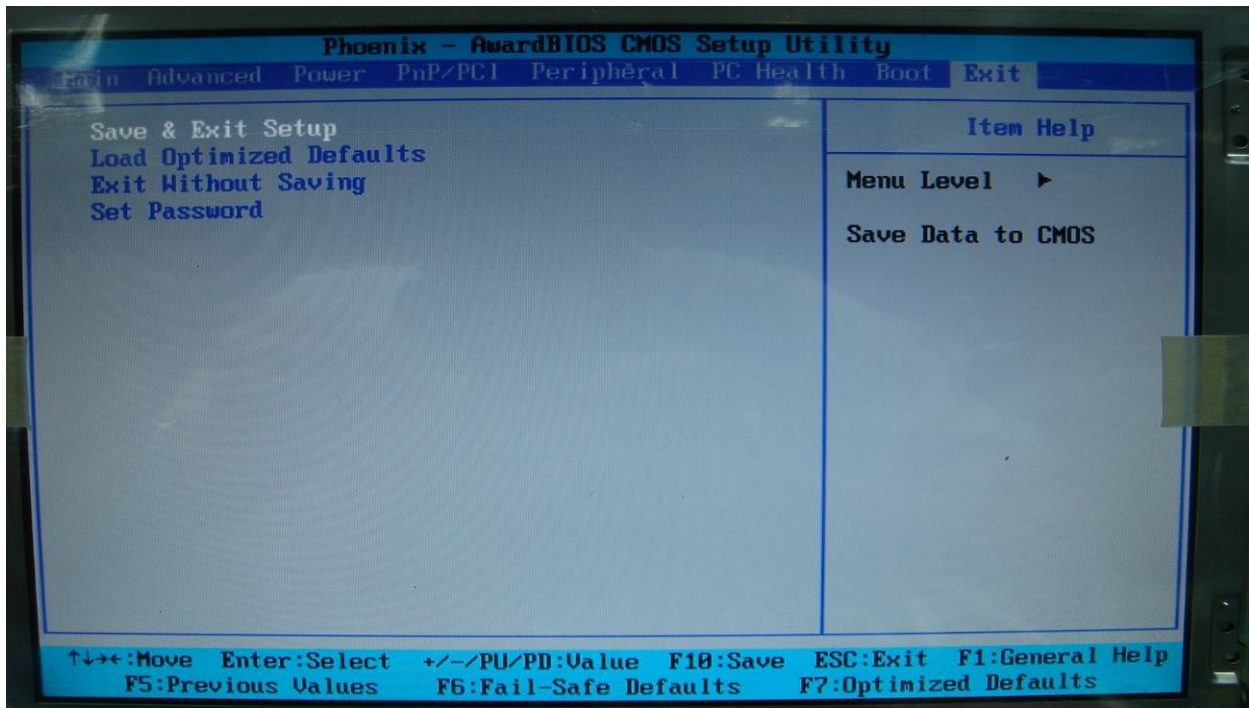


3.7 Boot Setup



Option	Choice	Description
First / Second / Third Boot Device/Other Boot Device	Hard Disk CDROM USB-FDD USB-CDROM LAN Disabled	The BIOS attempts to load the operating system from the devices in the sequence selected in these items.
LAN Boot Select	Enabled Disabled	These fields allow the system to search for an OS from LAN
Hard Disk Boot Priority	N/A	These fields set the Boot Priority for each Hard Disk

3.8 Exit Setup



Option	Choice	Description
Save & Exit Setup	Pressing <Enter> on this item for confirmation: Save to CMOS and EXIT (Y/N)? Y	Press “Y” to store the selections made in the menus in CMOS – a special section of memory that stays on after you turn your system off. The next time you boot your computer, the BIOS configures your system according to the Setup selections stored in CMOS. After saving the values the system is restarted again
Load Optimized Defaults	When you press <Enter> on this item you get a confirmation dialog box with a message like this: Load Optimized Defaults (Y/N) ? N	Press ‘Y’ to load the default values that are factory-set for optimal-performance system operations.

Exit Without Saving	Pressing <Enter> on this item for confirmation: Quit without saving (Y/N)? Y	This allows you to exit Setup without storing any changes in CMOS. The previous selections remain in effect. This shall exit the Setup utility and restart your computer.
Set Password	Pressing <Enter> on this item for confirmation: ENTER PASSWORD:	<p>When a password has been enabled, you will be prompted to enter your password every time you try to enter Setup. This prevents unauthorized persons from changing any part of your system configuration.</p> <p>Type the password, up to eight characters in length, and press <Enter>. The password typed now will clear any previous password from the CMOS memory. You will be asked to confirm the password.</p> <p>Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.</p> <p>To disable a password, just press <Enter> when you are prompted to enter the password. A message will confirm that the password will be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.</p>