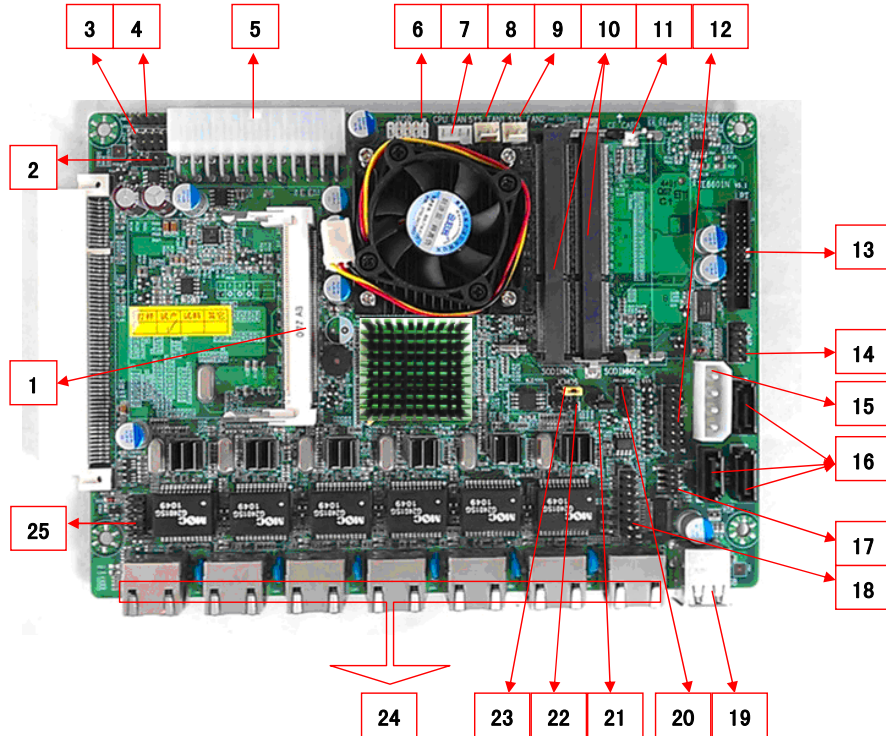
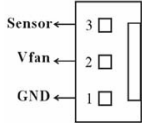
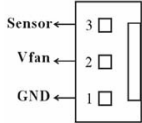
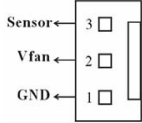


AR-B6006 Quick Manual

Locations of IO ports & Jumper settings definition



Item	Name	Description																								
1	Mini PCI	124 pin mini PCI connector																								
2	J4 (LED Board)	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>3.3V</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>3.3V</td> </tr> <tr> <td>4</td> <td>GND</td> </tr> </tbody> </table>	Pin	Description	1	3.3V	2	GND	3	3.3V	4	GND														
Pin	Description																									
1	3.3V																									
2	GND																									
3	3.3V																									
4	GND																									
3	J1(GPIO)	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>5v</td> <td>2</td> <td>12v</td> </tr> <tr> <td>3</td> <td>GPIO17</td> <td>4</td> <td>GPIO20</td> </tr> <tr> <td>5</td> <td>GPIO18</td> <td>6</td> <td>GPIO37</td> </tr> <tr> <td>7</td> <td>GND</td> <td>8</td> <td>GND</td> </tr> </tbody> </table> <p>More Detail please see Appendix A</p>	Pin	Description	Pin	Description	1	5v	2	12v	3	GPIO17	4	GPIO20	5	GPIO18	6	GPIO37	7	GND	8	GND				
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1	5v	2	12v																							
3	GPIO17	4	GPIO20																							
5	GPIO18	6	GPIO37																							
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4	PWRSW	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Power button +</td> </tr> <tr> <td>2</td> <td>Power button -</td> </tr> <tr> <td>3</td> <td>Reset +</td> </tr> <tr> <td>4</td> <td>Reset -</td> </tr> </tbody> </table>	Pin	Description	1	Power button +	2	Power button -	3	Reset +	4	Reset -														
Pin	Description																									
1	Power button +																									
2	Power button -																									
3	Reset +																									
4	Reset -																									
5	ATX	ATX 20P female connector																								
6	JUSB	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>VCC</td> <td>2</td> <td>VCC</td> </tr> <tr> <td>3</td> <td>-DATA2</td> <td>4</td> <td>-DATA3</td> </tr> <tr> <td>5</td> <td>+DATA2</td> <td>6</td> <td>+DATA3</td> </tr> <tr> <td>7</td> <td>GND</td> <td>8</td> <td>GND</td> </tr> <tr> <td>9</td> <td>NULL</td> <td>10</td> <td>GND</td> </tr> </tbody> </table>	Pin	Description	Pin	Description	1	VCC	2	VCC	3	-DATA2	4	-DATA3	5	+DATA2	6	+DATA3	7	GND	8	GND	9	NULL	10	GND
Pin	Description	Pin	Description																							
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3	-DATA2	4	-DATA3																							
5	+DATA2	6	+DATA3																							
7	GND	8	GND																							
9	NULL	10	GND																							

7	CPU FAN																																					
8	SYS FAN1																																					
9	SYS FAN2																																					
10	So-DIMM	DDR3 SO-DIMM socket																																				
11	CF Card	Compact Flash socket																																				
12	VGA	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>R</td> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>VCC</td> <td>4</td> <td>G</td> </tr> <tr> <td>5</td> <td>GND</td> <td>6</td> <td>DDCDA</td> </tr> <tr> <td>7</td> <td>B</td> <td>8</td> <td>GND</td> </tr> <tr> <td>9</td> <td>HSYNC</td> <td>10</td> <td>VCC</td> </tr> <tr> <td>11</td> <td>VCC</td> <td>12</td> <td>VSYNC</td> </tr> <tr> <td>13</td> <td>GND</td> <td>14</td> <td>GND</td> </tr> <tr> <td>15</td> <td>DDCCK</td> <td>16</td> <td>NC</td> </tr> </tbody> </table>	Pin	Description	Pin	Description	1	R	2	GND	3	VCC	4	G	5	GND	6	DDCDA	7	B	8	GND	9	HSYNC	10	VCC	11	VCC	12	VSYNC	13	GND	14	GND	15	DDCCK	16	NC
Pin	Description	Pin	Description																																			
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7	B	8	GND																																			
9	HSYNC	10	VCC																																			
11	VCC	12	VSYNC																																			
13	GND	14	GND																																			
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13	LPT	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>STB</td> <td>2</td> <td>AFD</td> </tr> <tr> <td>3</td> <td>D0</td> <td>4</td> <td>ERR</td> </tr> <tr> <td>5</td> <td>D1</td> <td>6</td> <td>INIT</td> </tr> <tr> <td>7</td> <td>D2</td> <td>8</td> <td>SLIN</td> </tr> <tr> <td>9</td> <td>D3</td> <td>10</td> <td>GND</td> </tr> <tr> <td>11</td> <td>D4</td> <td>12</td> <td>GND</td> </tr> <tr> <td>13</td> <td>D5</td> <td>14</td> <td>GND</td> </tr> <tr> <td>15</td> <td>D6</td> <td>16</td> <td>GND</td> </tr> <tr> <td>17</td> <td>D7</td> <td>18</td> <td>GND</td> </tr> <tr> <td>19</td> <td>ACK</td> <td>20</td> <td>GND</td> </tr> <tr> <td>21</td> <td>BUSY</td> <td>22</td> <td>GND</td> </tr> <tr> <td>23</td> <td>PE</td> <td>24</td> <td>GND</td> </tr> <tr> <td>25</td> <td>SLCT</td> <td>26</td> <td>GND</td> </tr> </tbody> </table>	Pin	Description	Pin	Description	1	STB	2	AFD	3	D0	4	ERR	5	D1	6	INIT	7	D2	8	SLIN	9	D3	10	GND	11	D4	12	GND	13	D5	14	GND	15	D6	16	GND	17	D7	18	GND	19	ACK	20	GND	21	BUSY	22	GND	23	PE	24	GND	25	SLCT	26	GND
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14	COM 2	<table border="1"> <thead> <tr> <th>Pin</th> <th>Description</th> <th>Pin</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>DCD</td> <td>2</td> <td>RXD</td> </tr> <tr> <td>3</td> <td>TXD</td> <td>4</td> <td>DTR</td> </tr> <tr> <td>5</td> <td>GND</td> <td>6</td> <td>DSR</td> </tr> <tr> <td>7</td> <td>RTS</td> <td>8</td> <td>CTS</td> </tr> <tr> <td>9</td> <td>RI</td> <td>10</td> <td>NULL</td> </tr> </tbody> </table>	Pin	Description	Pin	Description	1	DCD	2	RXD	3	TXD	4	DTR	5	GND	6	DSR	7	RTS	8	CTS	9	RI	10	NULL																																
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		5	GND	6	DSR																																																					
		7	RTS	8	CTS																																																					
9	RI	10	NULL																																																							
15	IDE PWR	4P male power connector																																																								
16	SATA	Standard SATA connector																																																								

17	J5	Pin	Description	Pin	Description
		1	KB_DATA	2	MS_DATA
		3	KB_CLK	4	MS_CLK
		5	GND	6	GND
		7	VCC	8	VCC
18	LAN LED	Pin	Description	Pin	Description
		1	LAN1_LINK/ACTIVE#_LED	2	VCC
		3	LAN1_LINK1000#_LED	4	LAN1_LINK100_LED
		5	LAN2_LINK/ACTIVE#_LED	6	VCC
		7	LAN2_LINK1000#_LED	8	LAN2_LINK100#_LED
		9	LAN3_LINK/ACTIVE#_LED	10	VCC
		11	LAN3_LINK1000#_LED	12	LAN3_LINK100#_LED
		13	LAN4_LINK/ACTIVE#_LED	14	VCC
		15	LAN4_LINK1000#_LED	16	LAN4_LINK100#_LED
19	USB	USB type A connector			
20	PWR/HD LED	Pin	Description		
		1	HDD_LED +		
		2	HDD_LED -		
		3	PWR_LED+		
		4	PWR_LED -		
21	J6	1- 2PIN , Auto Power ON ; 2-3PIN , Manual Power On Default setting : 1-2 Auto Power on			

22	JCMOS	Pin	1-2	2-3	
		Define	Normal	Clear cmos	
Default 1-2 Normal					
23	J3 (WDT Select)	1- 2PIN , Watchdog Control System Reset ;			
		2- 2-3PIN , Watchdog Control BYPASS			
Default :2-3 Watchdog Control Bypass					
24	LAN	RJ45 connector			
25	Bypass LED	Pin	Description	Pin	Description
		1	VCC	2	BYPASS1-N
		3	VCC	4	BYPASS1
		5	VCC	6	BYPASS2-N
		7	VCC	8	BYPASS2
		9	NULL	10	NC

Note. It can not use USB Hub with power adaptor that connects to USB port.

Appendix A GPIO Mapping table

GPIO17 -----Output Default Low

GPIO18 ----- Output Default Low

GPIO20 ----- Output Default Low

GPIO37 ----- Output Default Low

GPIO Base Address: 0x1180

GPIO17: Enable GPIO17: 0x1182 bit1 1: Enable

GPIO17 Input/output Select: 0x1186 bit1 0: Output 1: Input

GPIO17 Level: 0x118E bit1

GPIO18: Enable GPIO18: 0x1182 bit2 1: Enable

GPIO18 Input/output Select: 0x1186 bit2 0: Output 1: Input

GPIO18 Level: 0x118E bit2

GPIO20: Enable GPIO20: 0x1182 bit4 1: Enable

GPIO20 Input/output Select: 0x1186 bit4 0: Output 1: Input

GPIO20 Level: 0x118E bit4

GPIO37: Enable GPIO37: 0x11B0 bit5 1: Enable

GPIO37 Input/output Select: 0x11B4 bit5 0: Output 1: Input

GPIO37 Level: 0x11B8 bit5