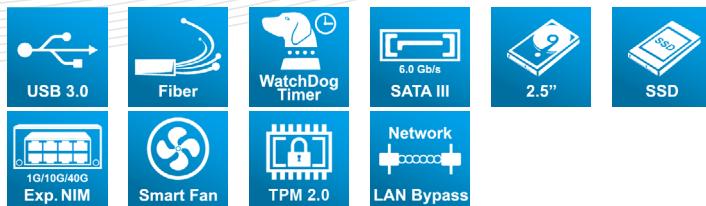


# ANR-ICEDA1

## 1U Rackmount Network Appliance

- *Intel® Xeon® D-1713NT Processor  
(ICE-Lake-D LCC Platform)*



## User Manual

Acrosser Technology Co., Ltd.  
[www.acrosser.com](http://www.acrosser.com)

## Disclaimer

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## Purpose

This document is intended to provide the information about the features and use of the product.

## Audience

The intended audiences are technical personnel, not for general audiences.

## WARNING

Danger of explosion if batteries are incorrectly replaced. Always replace the battery with the same specifications. Dispose of used batteries according to the manufacturer's instructions.

Before running the system, make sure the power cord is firmly plugged into the socket.

## CAUTION



IEC 60417-6042 (2010-11)



IEC 60417-6172 (2012-09)

All power cords must be disconnected during product repair.

Ver: 100

Date: Feb. 13, 2023

To read this User Manual on your smart phone, you will have to install an APP that can read PDF file format first. Please find the APP you prefer from the APP Market.

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# 1. Product Specifications

The ANR-ICEDA1 is an 1U rackmount network appliance product. Powered by Intel® Xeon® D-1713NT Processor (Intel code name “Ice Lake”) and runs on Intel® SoC with integrated Ethernet in high-density Ball-Grid Array packages, it delivers server-class computing, hardware-based security, and high-bandwidth I/Os for networking applications at the edge.

## 1.1. Specifications

(Specifications are subject to change without notice.)

### Platform

|                      |   |
|----------------------|---|
| <b>Form Factor</b>   | • 1U Rackmount Network Platform   |
| <b>Processor</b>     | • Intel® Xeon® D-1713NT Processor, 4C/8T, Base Frequency: 2.20 GHz, 45W |
| <b>Chipset</b>       | • SoC   |
| <b>System Memory</b> | • 2x DDR4 288 Pin SO-DIMM ECC DIMM slots                                |

### Front I/O

|                 |  |
|-----------------|--|
| <b>Ethernet</b> | • 12x 1GbE RJ45 (I350 AM4 x 3) + 4x 10GbE SFP+           |
| <b>Bypass</b>   | • 2 Pairs LAN Bypass                                     |
| <b>USB</b>      | • 2x Type A USB 3.2 Gen1                                 |
| <b>Console</b>  | • 1x RJ-45 Console                                       |
| <b>LCM</b>      | • 1x LCM with Keypad                                     |
| <b>LED</b>      | • 1x Power LED<br>• 1x Status LED<br>• 1x HDD Active LED |
| <b>Others</b>   | • 1x Software Button                                     |

### Rear I/O

|                     |                          |
|---------------------|--------------------------|
| <b>Fan</b>          | • 1x System Fan          |
| <b>Power Button</b> | • 1x Power Switch        |
| <b>DC-in</b>        | • 1x 220W AC Power Input |

### Storage

|             |   |
|-------------|---|
| <b>SATA</b> | • 2x 2.5" Storage Devices w/ SATA III (6Gbps) |
|-------------|---|

## Expansion

|                 |   |
|-----------------|---|
| <b>M.2</b>      | <ul style="list-style-type: none"><li>• 1x M.2 B key(3052) with SIM</li><li>• 1x M.2 E key(2230) (PCIe)</li></ul> |
| <b>NIM Slot</b> | <ul style="list-style-type: none"><li>• 1x NIM Slot (Optional)</li></ul>  |

## Other Features

|                       |  |
|-----------------------|--|
| <b>RTC</b>            | <ul style="list-style-type: none"><li>• Internal RTC</li></ul>                         |
| <b>Watchdog Timer</b> | <ul style="list-style-type: none"><li>• 1~255 steps by software programmable</li></ul> |
| <b>TPM</b>            | <ul style="list-style-type: none"><li>• TPM2.0</li></ul>                               |

## Software

|                   |   |
|-------------------|---|
| <b>OS support</b> | <ul style="list-style-type: none"><li>• Ubuntu 20.04.2 or above, Cent OS 7 or above</li></ul> |
|-------------------|---|

## Mechanical & Environment

|                              |   |
|------------------------------|---|
| <b>Chassis</b>               | <ul style="list-style-type: none"><li>• Metal chassis</li></ul>   |
| <b>Dimension</b>             | <ul style="list-style-type: none"><li>• 16.93" x 7.87" x1.73" (430mm x 200mm x 44mm)</li></ul>  |
| <b>Operation Temperature</b> | <ul style="list-style-type: none"><li>• 32°F ~ 104°F (0°C ~ 40°C)</li></ul>   |
| <b>Storage Temperature</b>   | <ul style="list-style-type: none"><li>• -4°F ~ 140°F (-20°C ~ 60°C)</li></ul>   |
| <b>Operating Humidity</b>    | <ul style="list-style-type: none"><li>• 10%~80% relative humidity, non-condensing</li></ul>   |
| <b>Vibration</b>             | <ul style="list-style-type: none"><li>• 0.5 g rms/ 5 ~ 500Hz / operation (2.5" Hard Disk Drive)</li><li>• 1.5 g rms/ 5 ~ 500Hz / non operation</li></ul>                      |
| <b>Shock</b>                 | <ul style="list-style-type: none"><li>• 10 G peak acceleration (11 m sec. duration), operation</li><li>• 20 G peak acceleration (11 m sec. duration), non-operation</li></ul> |
| <b>Certification</b>         | <ul style="list-style-type: none"><li>• CE / FCC class A</li></ul>  |

## 1.2. Package Contents

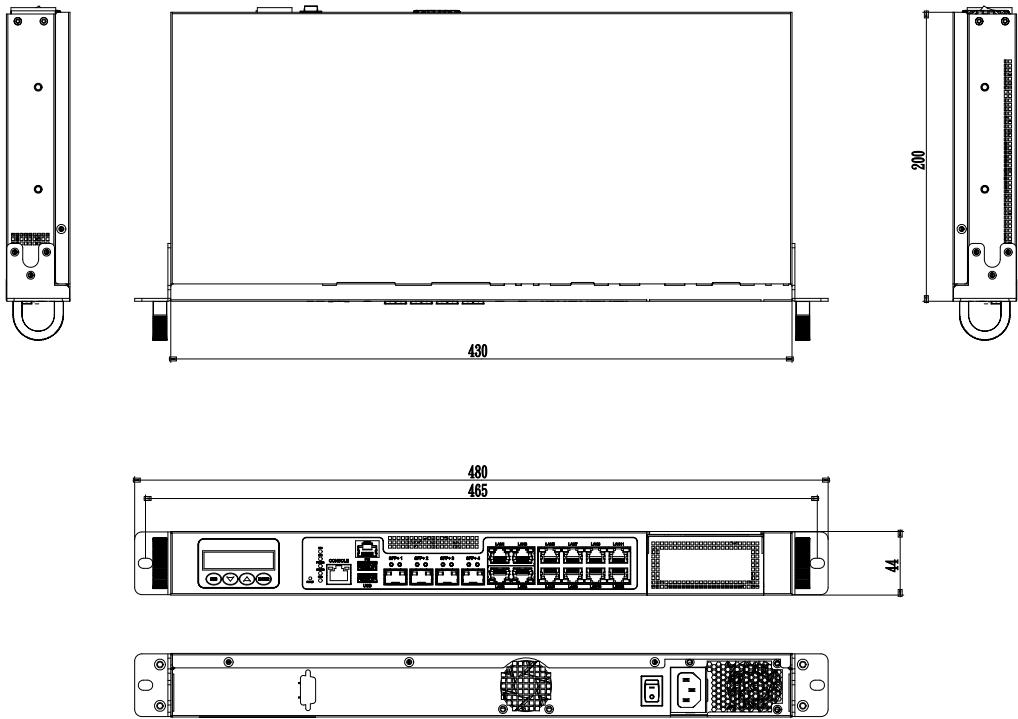
Check if the following items are included in the package.

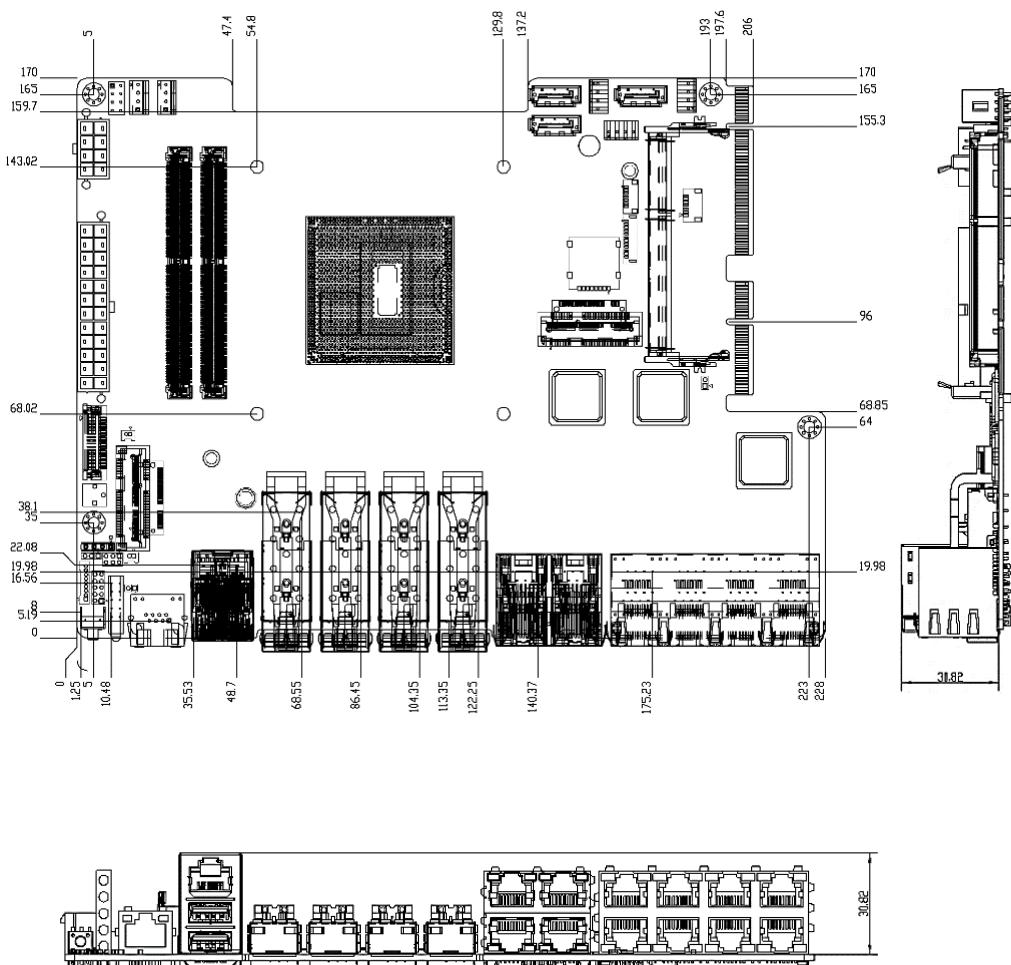
| Item  | Q'ty | Remark |
|---|------|--------|
| <input type="checkbox"/> ANR-ICEDE0         | 1    |        |
| <input type="checkbox"/> Console cable      | 1    |        |
| <input type="checkbox"/> Ear bracket (pair) | 1    |        |
| <input type="checkbox"/> SATA cable         | 2    |        |
| <input type="checkbox"/> SATA power cable   | 2    |        |
| <input type="checkbox"/> HDD kit            | 2    |        |

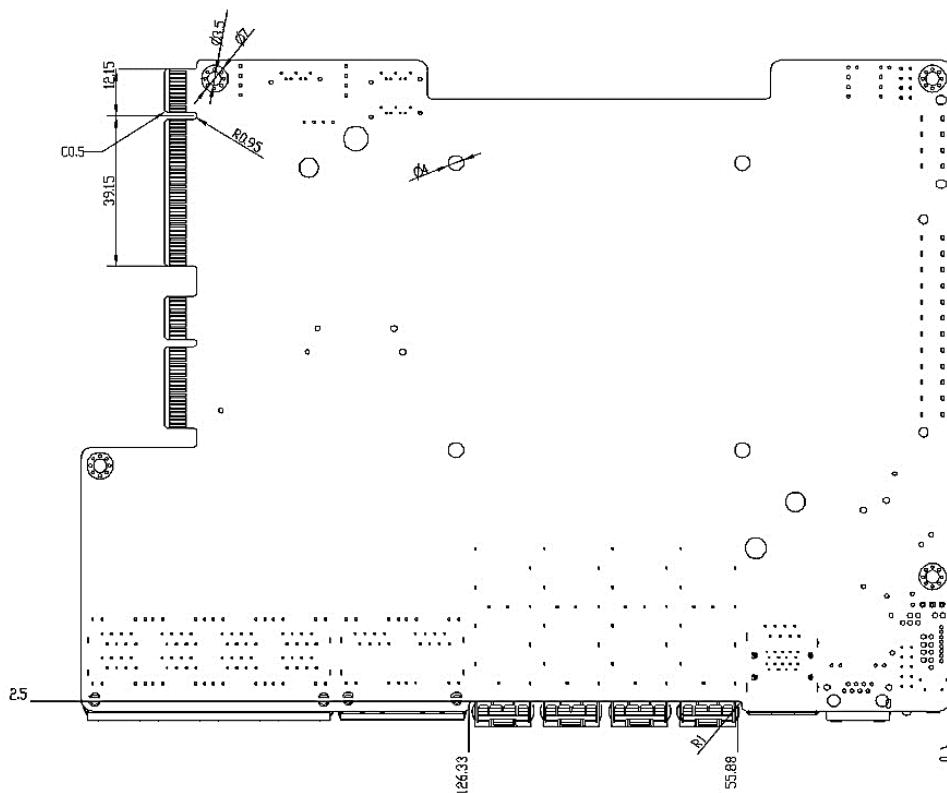
## 2. Hardware Information

### 2.1. Dimensions

(Unit: mm)

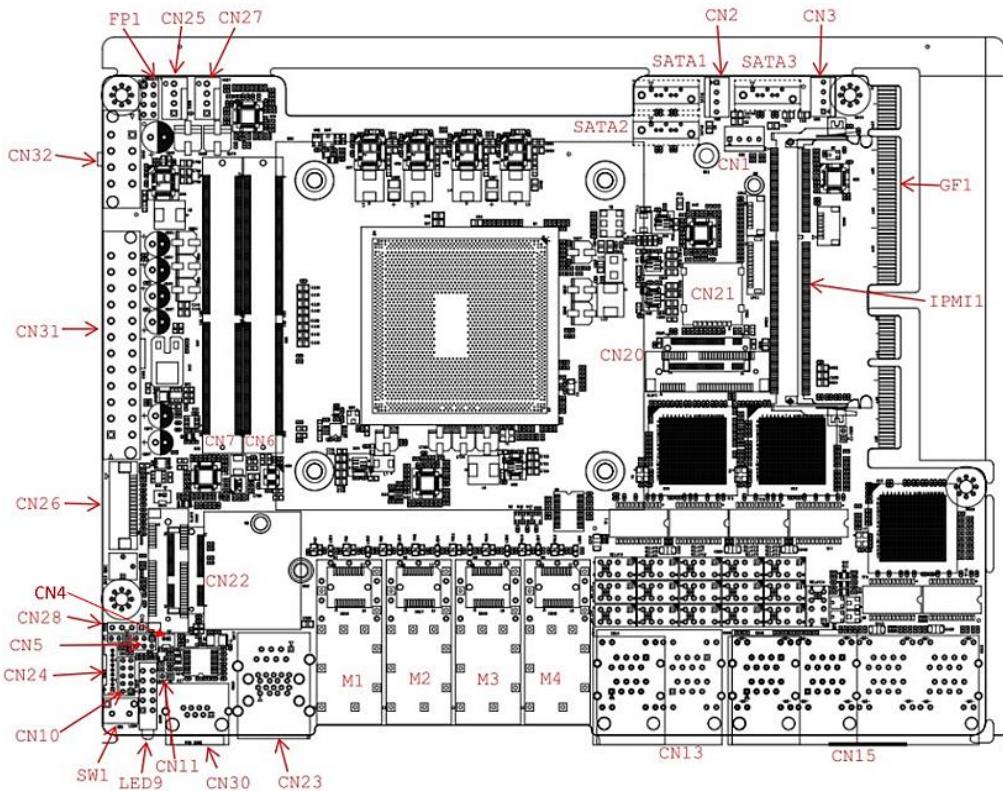
**System:**

**Component Side:**

**Solder Side:**

## 2.2. Jumpers and Connectors

Component Side:



## 2.3. List of Jumpers

This system board is configured with a number of jumpers which can be configured for your application. This section details those jumpers and their settings.

| Label | Function   |
|-------|------------|
| CN5   | Clear CMOS |

### 2.3.1. Clear CMOS (CN5)

| Setting          | Configuration |
|------------------|---------------|
| Normal (Default) | 1-3, 2-4      |
| Clear CMOS       | 3-5, 4-6      |

## 2.4. List of Connectors

This system board is configured with a number of connectors which can be used for configuring your system and connecting with external modules. This section details those connectors and settings.

| Label         | Function                                |
|---------------|---|
| LED9          | Status LED                              |
| CN1, CN2, CN3 | SATA Power CN4                          |
| CN4           | Battery Header                          |
| CN6, CN7      | DDR4 SO-DIMM*2                          |
| CN10          | DIO Header                              |
| CN11          | Case Open                               |
| CN13          | 1Gb RJ45 Port                           |
| CN15          | 1Gb RJ45 Port                           |
| CN20          | M.2 B-Key slot                          |
| CN21          | Micro SIM                               |
| CN22          | M.2 E-Key slot                          |
| CN23          | USB3.1 + RJ45 Connector (IPMI Ethernet) |
| CN24          | Serial Port                             |
| CN25, CN27    | FAN1/FAN2                               |
| CN26, CN28    | LCM                                     |
| CN30          | Console                                 |
| CN31, CN32    | ATX Power Connector                     |

| Label               | Function                     |
|---------------------|------------------------------|
| M1, M2, M3, M4      | 10Gb SFP+                    |
| M1, M2, M3, M4      | 10Gb SFP+                    |
| SATA1, SATA2, SATA3 | SATA Connector               |
| SATA1, SATA2, SATA3 | SATA Connector               |
| IPMI1               | IPMI Slot                    |
| SW1                 | Software Programmable Button |

Note 1: *Bypass Function on CN13.*

Note 2: *PCIe\*8 on GF1 is for NIM riser card PER-R40X only, not for standard PCIe signal.*

#### 2.4.1. Battery Holder (CN4)

| Pin # | Signal | Pin # | Signal |
|-------|--------|-------|--------|
| 1     | +3.3V  | 2     | Ground |

#### 2.4.2. Digital I/O (CN10)

| Pin # | Signal           | Pin # | Signal           |
|-------|------------------|-------|------------------|
| 1     | Digital I/O bit1 | 2     | Digital I/O bit2 |
| 3     | Digital I/O bit3 | 4     | Digital I/O bit4 |
| 5     | Digital I/O bit5 | 6     | Digital I/O bit6 |
| 7     | Digital I/O bit7 | 8     | Digital I/O bit8 |
| 9     | +5V              | 10    | GND              |

#### 2.4.3. Front Panel Pin Header (FP1)

| Pin # | Signal             | Pin # | Signal   |
|-------|--------------------|-------|----------|
| 1     | Power Button SW+   | 2     | Ground   |
| 3     | Hardware Reset SW+ | 4     | Ground   |
| 5     | PWRLED             | 6     | Ground   |
| 7     | HDDACT             | 8     | HDD LED- |

#### 2.4.4. Case Open Holder (CN11)

| Pin # | Signal | Pin # | Signal    |
|-------|--------|-------|-----------|
| 1     | Ground | 2     | Case Open |

## 2.4.5. M.2 B-Key Slot (CN20)

| Pin # | Signal    | Pin # | Signal     |
|-------|-----------|-------|------------|
| 1     | CFG3      | 2     | +3.3V      |
| 3     | GND       | 4     | +3.3V      |
| 5     | GND       | 6     | PWR_OFF    |
| 7     | USB2DN    | 8     | W_DISABLE  |
| 9     | USB2DP    | 10    | NC         |
| 11    | GND       | -     | -          |
| -     | -         | 20    | NC         |
| 21    | CFG0      | 22    | NC         |
| 23    | NC        | 24    | NC         |
| 25    | NC        | 26    | NC         |
| 27    | GND       | 28    | NC         |
| 29    | PCIE1RXP  | 30    | UIMRST     |
| 31    | PCIE1RXN  | 32    | UIMCLK     |
| 33    | GND       | 34    | UIMDAT     |
| 35    | PCIE1TXN  | 36    | UIMPWR     |
| 37    | PCIE1TYP  | 38    | DEVSLP     |
| 39    | GND       | 40    | NC         |
| 41    | PCIE0RXP  | 42    | NC         |
| 43    | PCIE0RXN  | 44    | NC         |
| 45    | GND       | 46    | NC         |
| 47    | PCIE0TXN  | 48    | NC         |
| 49    | PCIE0TYP  | 50    | PLTRST#    |
| 51    | GND       | 52    | NC         |
| 53    | PCIECLKDN | 54    | WAKE#      |
| 55    | PCIECLKDP | 56    | NC         |
| 57    | GND       | 58    | NC         |
| 59    | NC        | 60    | NC         |
| 61    | NC        | 62    | NC         |
| 63    | NC        | 64    | NC         |
| 65    | NC        | 66    | SIMDET     |
| 67    | NC        | 68    | 32K_SUSCLK |
| 69    | CFG1      | 70    | +3.3V      |

| Pin # | Signal | Pin # | Signal |
|-------|--------|-------|--------|
| 71    | GND    | 72    | +3.3V  |
| 73    | GND    | 74    | +3.3V  |
| 75    | CFG2   | -     | -      |

## 2.4.6. M.2 E-Key Slot (CN22)

| Pin # | Signal     | Pin # | Signal     |
|-------|------------|-------|------------|
| 1     | GND        | 2     | +3.3V      |
| 3     | NC         | 4     | +3.3V      |
| 5     | NC         | 6     | NC         |
| 7     | GND        | 8     | NC         |
| 9     | NC         | 10    | NC         |
| 11    | NC         | 12    | NC         |
| 13    | NC         | 14    | NC         |
| 15    | NC         | 16    | NC         |
| 17    | NC         | 18    | GND        |
| 19    | NC         | 20    | NC         |
| 21    | NC         | 22    | NC         |
| 23    | NC         | 32    | NC         |
| 33    | GND        | 34    | NC         |
| 35    | PCIE0TXP   | 36    | NC         |
| 37    | PCIE0TXN   | 38    | NC         |
| 39    | GND        | 40    | NC         |
| 41    | PCIE0RXP   | 42    | NC         |
| 43    | PCIE0RXN   | 44    | NC         |
| 45    | GND        | 46    | NC         |
| 47    | PCIECLK0DP | 48    | NC         |
| 49    | PCIECLK0DN | 50    | 32K_SUSCLK |
| 51    | GND        | 52    | PLTRST#    |
| 53    | CLKREQ#    | 54    | DIS2#      |
| 55    | WAKE#      | 56    | DIS1#      |
| 57    | GND        | 58    | NC         |
| 59    | PCIE1TXP   | 60    | NC         |
| 61    | PCIE1TXN   | 62    | NC         |

| Pin # | Signal     | Pin # | Signal |
|-------|------------|-------|--------|
| 63    | GND        | 64    | NC     |
| 65    | PCIE0RXP   | 66    | NC     |
| 67    | PCIE0RXN   | 68    | NC     |
| 69    | GND        | 70    | NC     |
| 71    | PCIECLK1DP | 72    | +3.3V  |
| 73    | PCIECLK1DN | 74    | +3.3V  |
| 75    | GND        | -     | -      |

#### 2.4.7. Serial Port (CN24)

| Pin # | Signal | Pin # | Signal |
|-------|--------|-------|--------|
| 1     | DCD2   | 2     | DSR2   |
| 3     | RXD2   | 4     | RTS2   |
| 5     | TXD2   | 6     | CTS2   |
| 7     | DTR2   | 8     | RI2    |
| 9     | GND    | -     | -      |

## 2.5. Hardware Installation

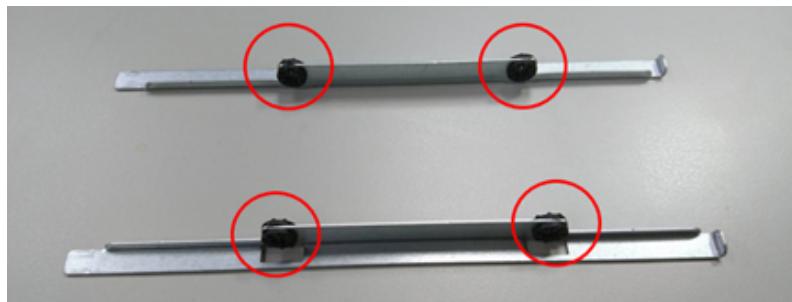
This section details the hardware assembly steps for the ANR-ICEDA1. Please read this section thoroughly before beginning installation and ensure you have all necessary components ready. A Phillips head screwdriver is required.

### 2.5.1. 2.5" Hard Disk Drive Installation

Step 1: Unscrew the upper lid.



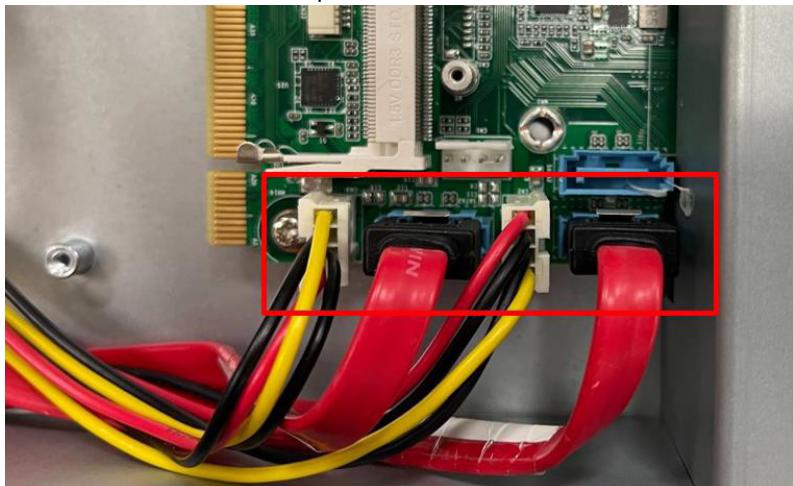
Step 2: Place assembled cushions on the hard disk driver bracket.



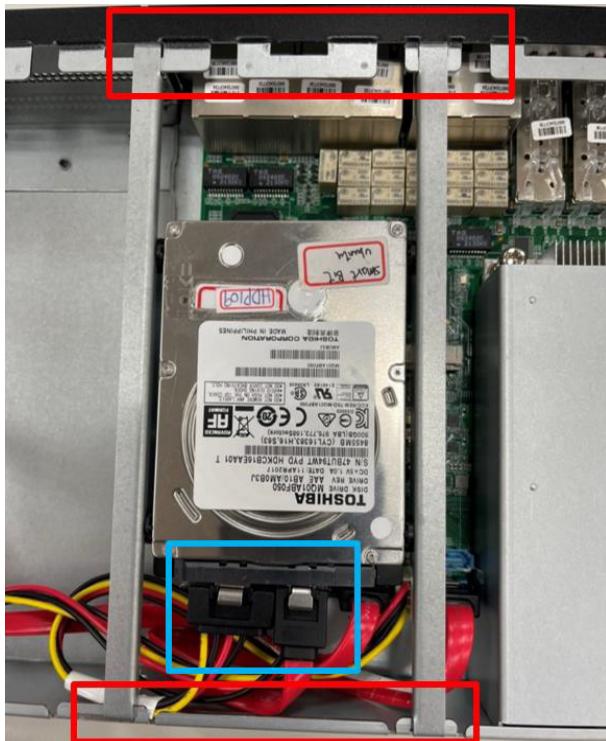
Step 3: Lock HDD on the bottom cushions with four screws.



Step 4: Connect the SATA cable and power cable to the main board.



Step 5: Connect the SATA cable and power cable into the Hard Disk and put hard drive bracket on the chassis.



Step 6: Connect the SATA cable and power cable into the Hard Disk and put hard drive bracket on the chassis.

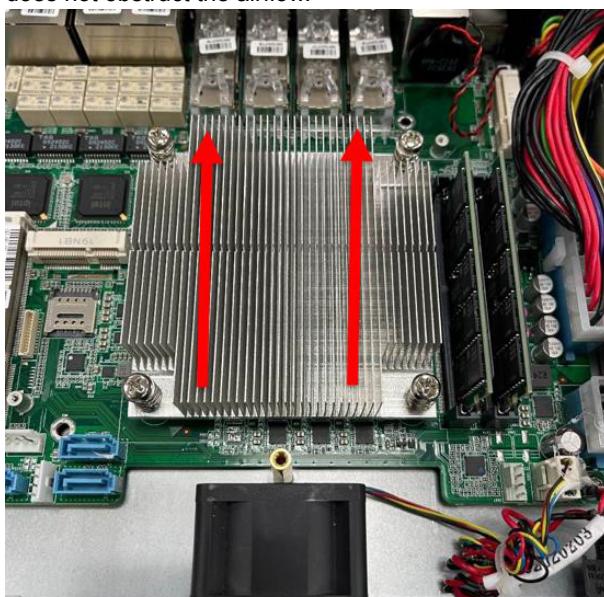


## 2.5.2. Heat Sink Installation

Step 1: Loosen the screw and remove the fan duct.



Step 2: Cover the Heatsink on the CPU and ensure the direction of the Heatsink does not obstruct the airflow.

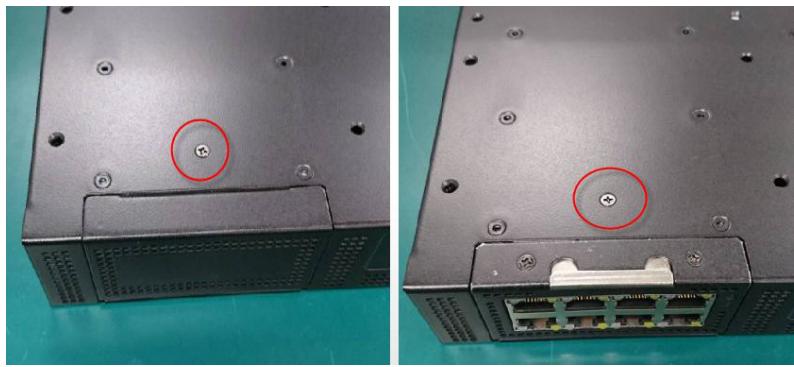


Step 3: Fasten the screw to lock the air duct.

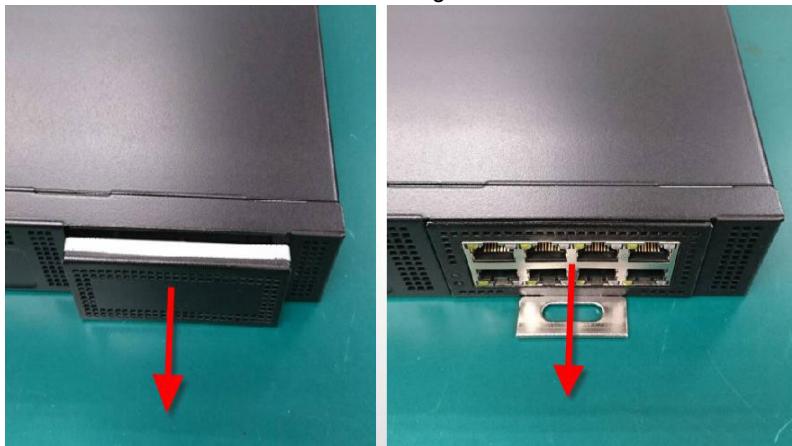


### 2.5.3. NIM Installation

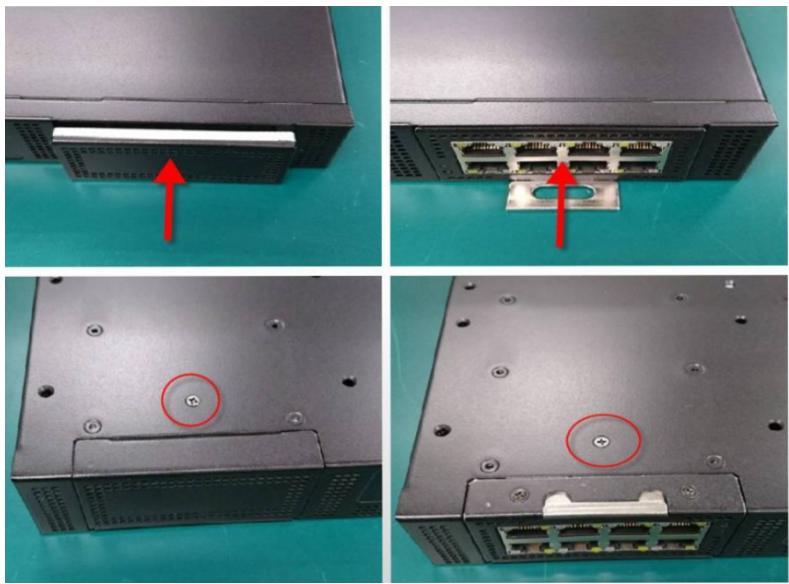
Step 1: Loosen the screws on the bottom of chassis.



Step 2: Remove the null Module cover or existing LAN module.



Step 3: Insert the LAN Module and fasten the screws.



## 3. AMI BIOS Setup

### 3.1. System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors.

The system configuration verification routines check the current system configuration stored in the CMOS memory and BIOS NVRAM. If system configuration is not found or system configuration data error is detected, system will load optimized default and re-boot with this default system configuration automatically.

There are four situations in which you will need to setup system configuration:

- Starting the system for the first time
- The system hardware has been changed
- The system configuration is reset by Clear-CMOS jumper.
- The CMOS memory has lost power and the configuration information is erased

The system's CMOS memory uses a backup battery for data retention. The battery must be replaced when it runs down.

### 3.2. AMI BIOS Setup

The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off.

To enter BIOS Setup, press **<Del>** or **<ESC>** immediately while your computer is powering up.

The function for each interface can be found below.

**Main** – Date and time can be set here. Press **<Tab>** to switch between date elements

**Advanced** – Enable/ Disable boot option for legacy network devices

**Chipset** – Host bridge parameters.

**Boot** – Enable/ Disable quiet Boot Option

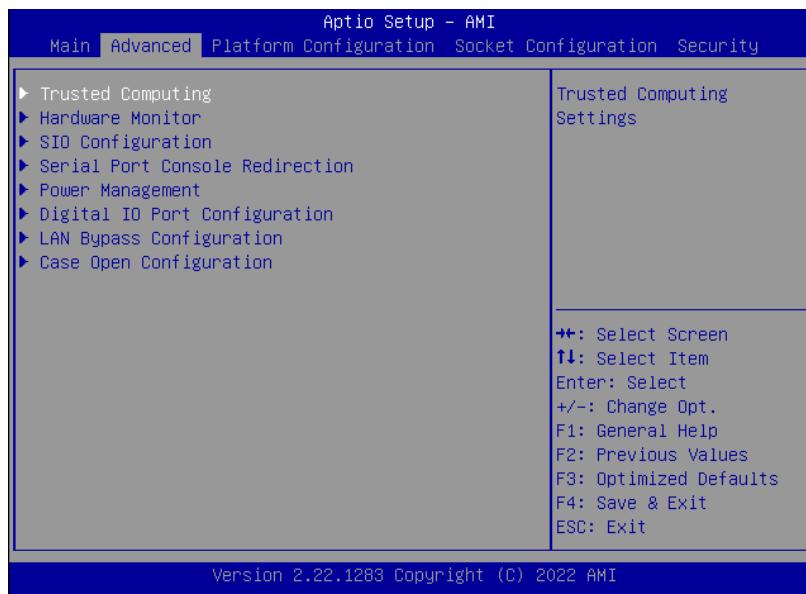
**Security** – The setup administrator password can be set here

**Save & Exit** – Save your changes and exit the program

### 3.3. Setup Submenu: Main



### 3.4. Setup Submenu: Advanced



### 3.4.1. Trusted Computing



- **Security Device Support**

Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

- **SHA-1 PCR Bank**

Enable or Disable SHA-1 PCR Bank

- **SHA256 PCR Bank**

Enable or Disable SHA256 PCR Bank.

- **SHA384 PCR Bank**

Enable or Disable SHA384 PCR Bank.

- **Pending operation**

Schedule an Operation for the Security Device. NOTE: Your Computer will reboot during restart in order to change State of Security Device.

- **Platform Hierarchy**

Enable or Disable Platform Hierarchy.

- **Storage Hierarchy**

Enable or Disable Storage Hierarchy.

- **Endorsement Hierarchy**

Enable or Disable Endorsement Hierarchy.

- **TPM 2.0 UEFI Spec Version**

Select the TCH2 Spec Version Support.

TCG\_1\_2: The Compatible mode for Win8/Win10

TCG\_2: Support new TCG2 protocol and event format for Win10 or later.

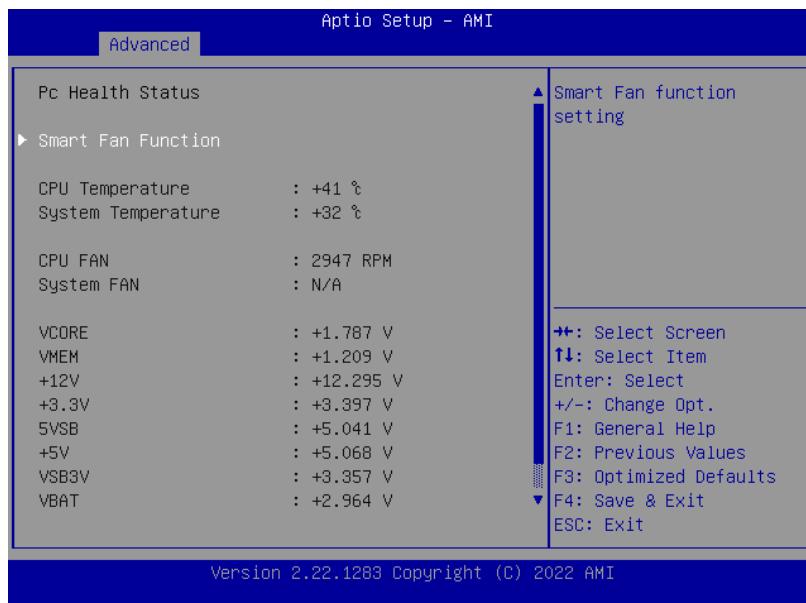
- **Physical Presence Spec Version**

Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3

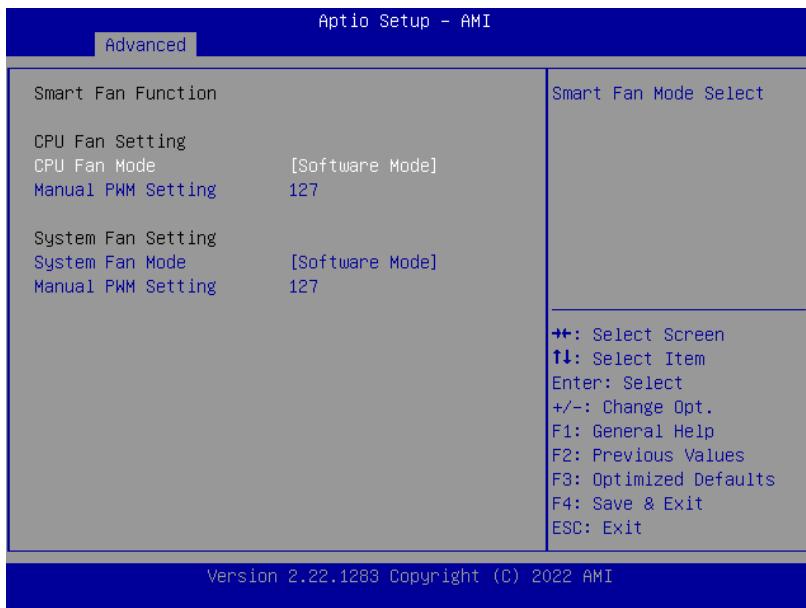
- **Device Select**

TPM 1.2 will restrict support to TPM 1.2 devices, TPM 2.0 will restrict support to TPM 2.0 devices, Auto will support both with the default set to TPM 2.0 devices if not found, TPM 1.2 devices will be enumerated.

### 3.4.2. Hardware Monitor

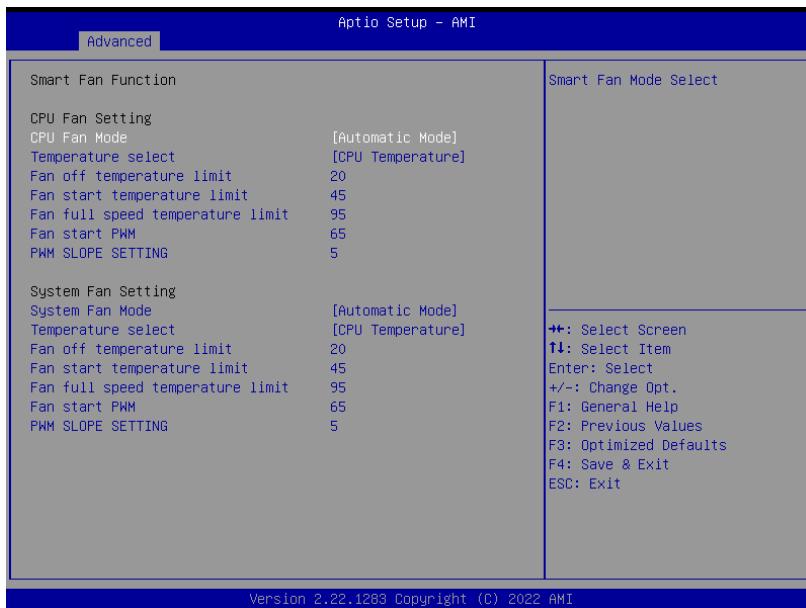


### 3.4.3. System Fan Setting



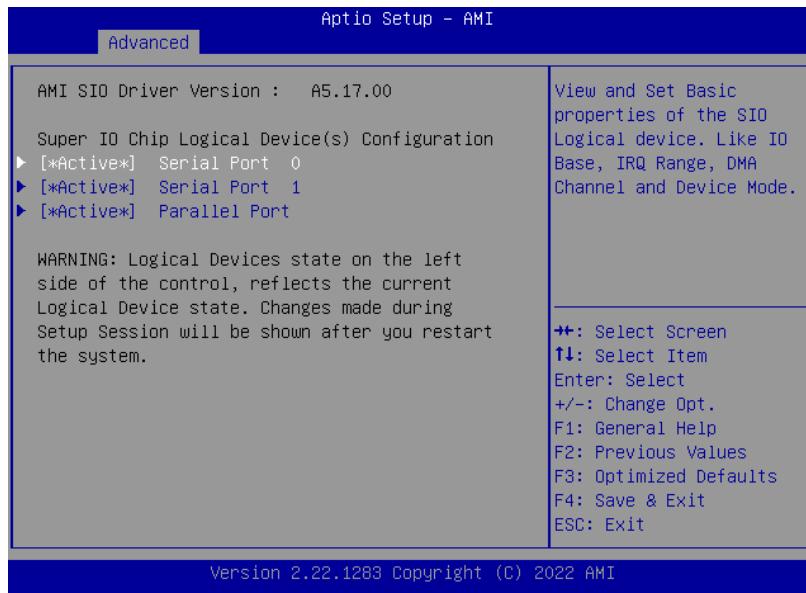
- **Manual PWM Setting**

Fan will work with this Manual PWM Value.



- **Smart Fan 1 Mode**  
Smart Fan Mode Select.
- **Fan off temperature limit**  
Fan will off when temperature lower than this limit.
- **Fan start temperature limit**  
Fan will work when temperature higher than this limit.
- **Fan full speed temperature limit**  
Fan will full speed when temperature higher than this limit.
- **Fan start PWM**  
Fan will full start with this PWM value.
- **PWM SLOPE SETTING**  
PWM SLOPE Selection.  
Slope = PWM value / °C

### 3.4.4. SIO Configuration



### 3.4.5. Serial Port 0 Configuration



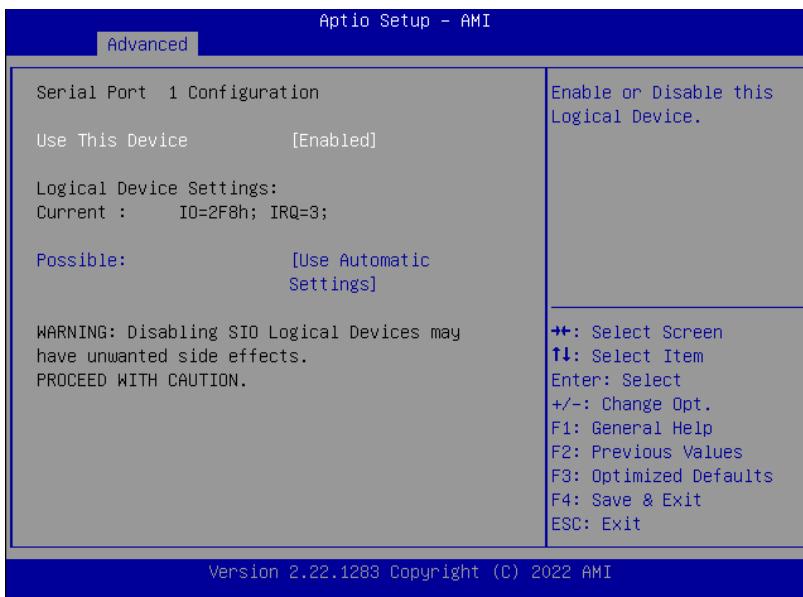
- **Use This Device**

Enable/Disable this Logical Device.

- **Possible**

Allow user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts.

### 3.4.6. Serial Port 1 Configuration



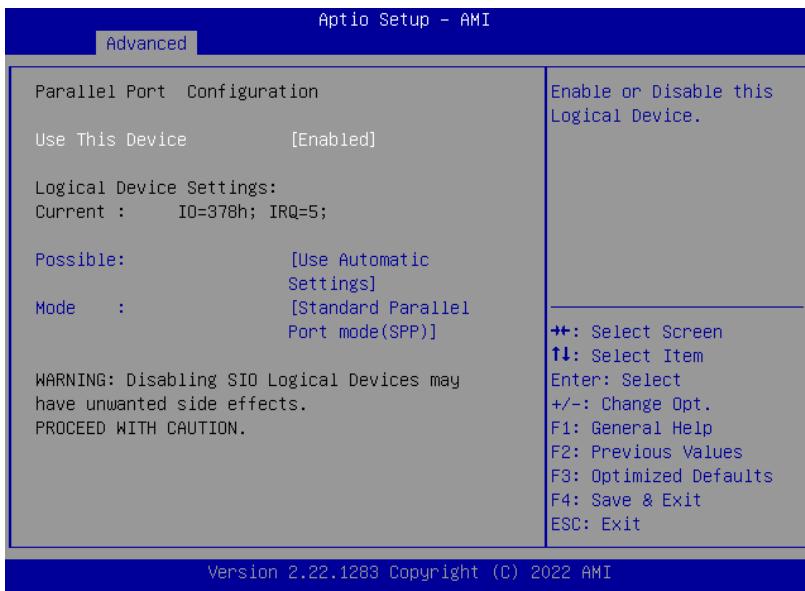
- **Use This Device**

Enable/Disable this Logical Device.

- **Possible**

Allow user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts.

### 3.4.7. Parallel Port Configuration



- **Use This Device**

Enable/Disable this Logical Device

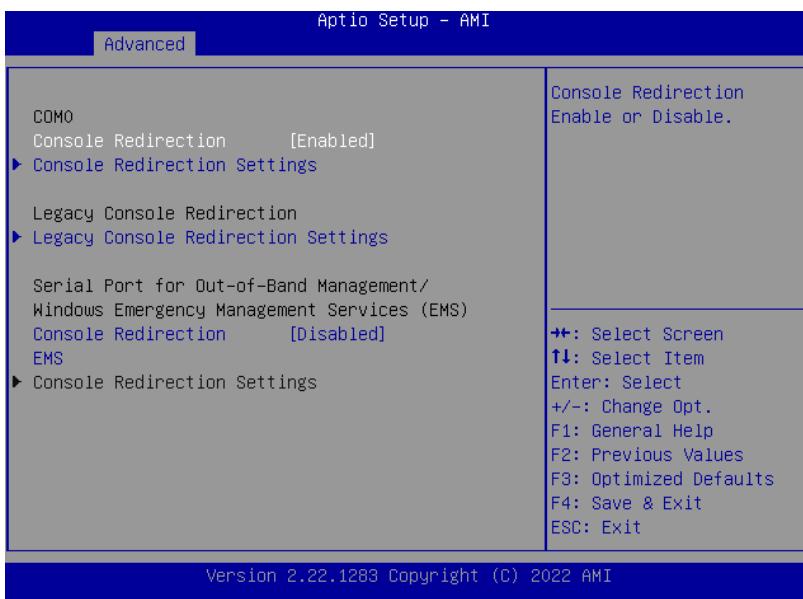
- **Possible**

Allow user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts.

- **Mode**

Change Parallel Port mode. Some of the Modes required a DMA resource. After Mode changing, Reset the System to reflect actual device settings.

### 3.4.8. Serial Port Console Redirection



- **Console Redirection**  
Console Redirection Enable or Disable.
- **Console Redirection EMS**  
Console Redirection Enable or Disable.

### 3.4.9. Console Redirection Settings



- **Terminal Type**

Emulation:

ANSI: Extended ASCII char set.

VT100: ASCII char set.

VT100+: Extends VT100 to support color, function keys, etc.

VT-UTF8: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.

- **Bits per second**

Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.

- **Data Bits**

Data Bits

- **Parity**

A Parity bit can be sent with the data bits to detect some transmission errors.

Even: Parity bit is 0 if the number of 1's in the data bits is even.

Odd: Parity bit is 0 if the number of 1's in the data bits is odd.

Mark: Parity bit is always 1.

Space: Parity bit is always 0. Mark and Space Parity do not allow for error detection.

They can be used as an additional data bit.

- **Stop Bits**

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.

- **Flow control**

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a 'stop' signal can be sent to stop the data flow. Once the buffers are empty, a 'start' signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

- **VT-UTF8 Combo Key Support**

Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.

- **Recorder Mode**

With this mode enabled only text will be sent. This is to capture Terminal data.

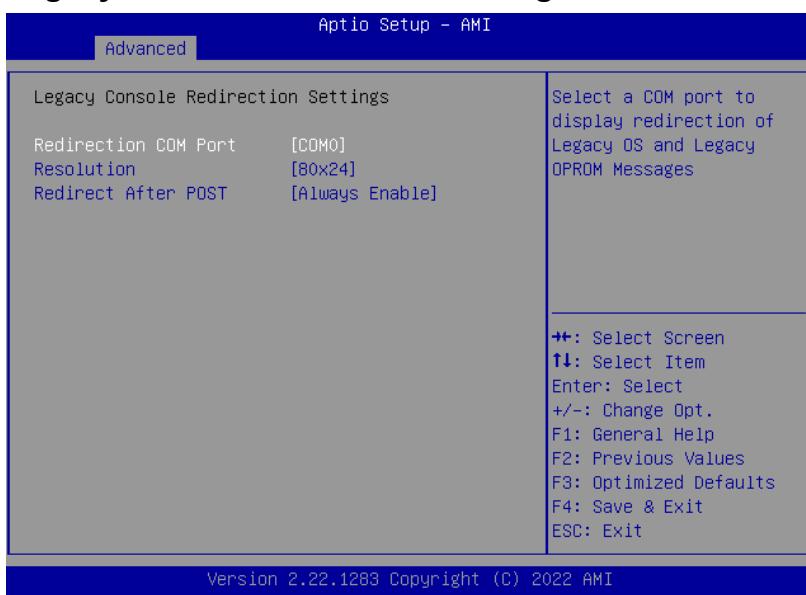
- **Resolution 100x31**

Enables or disables extended terminal resolution.

- **Putty KeyPad**

Select FunctionKey and KeyPad on Putty.

### 3.4.10. Legacy Console Redirection Settings



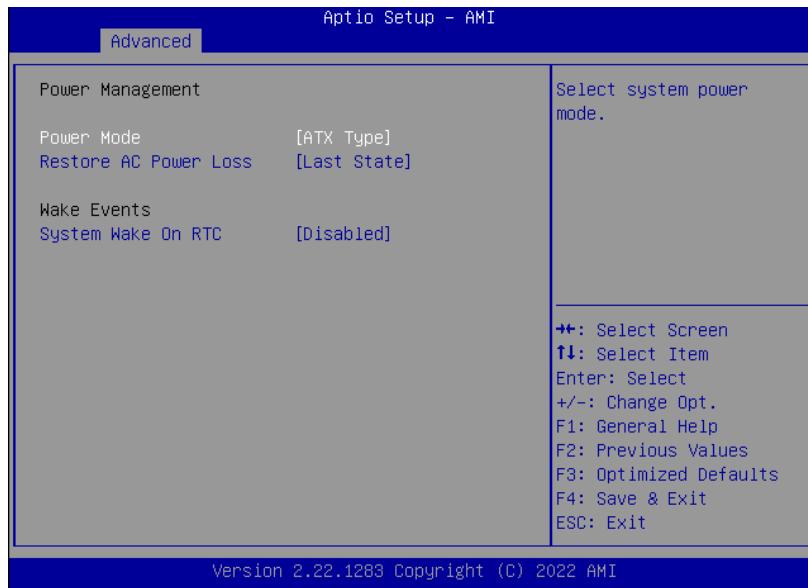
- **Redirection COM Port**

Select a COM port to display redirection of Legacy OS and Legacy OPROM Messages.

- **Resolution**

On Legacy OS, the Number of Rows and Columns supported redirection.

### 3.4.11. Power Management



- **Power Mode**

Select power supply mode.

- **Restore AC Power Loss**

Select power state when power is re-applied after a power failure.

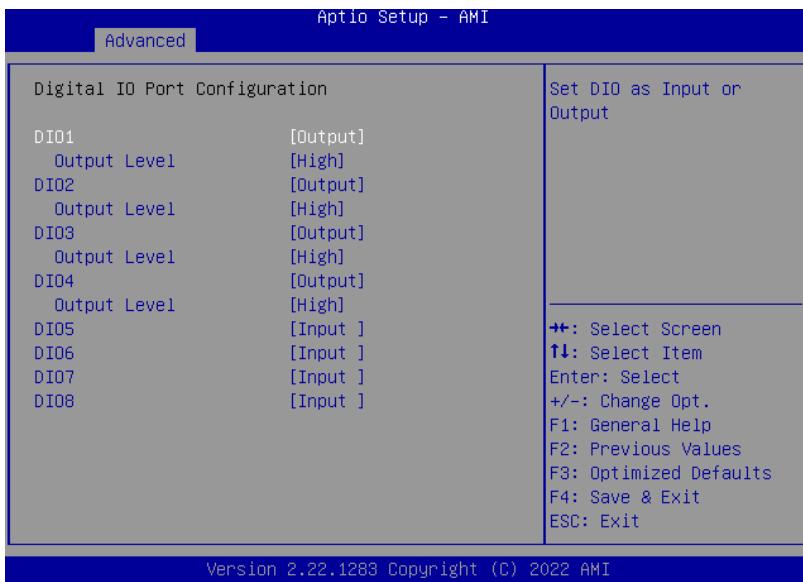
- **System Wake On RTC**

By Date: System will wake on the day with hr::min::sec specified./n

By Weekday: System will wake on the enabled weekday with hr ::min::sec specified./n

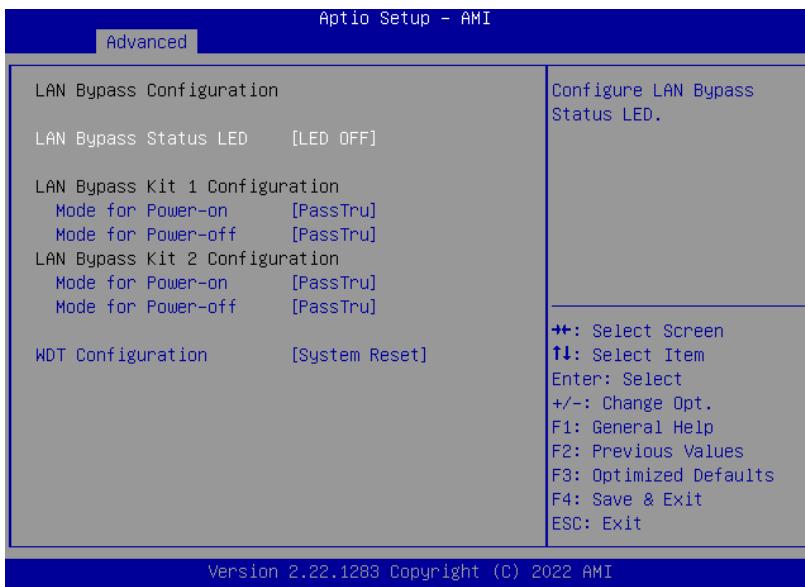
Bypass: BIOS will not control RTC wake function.

### 3.4.12. Digital IO Port Configuration



- **DIO**  
Set DIO as Input or Output.
- **Output Level**  
Set output level when DIO pin is output.

### 3.4.13. LAN Bypass Configuration



- **Lan Bypass Status LED**

Configure LAN Bypass status LED.

- **Mode for Power-on**

Configure LAN kit behavior when system is in power-on state. (Bypass/Pass Through)

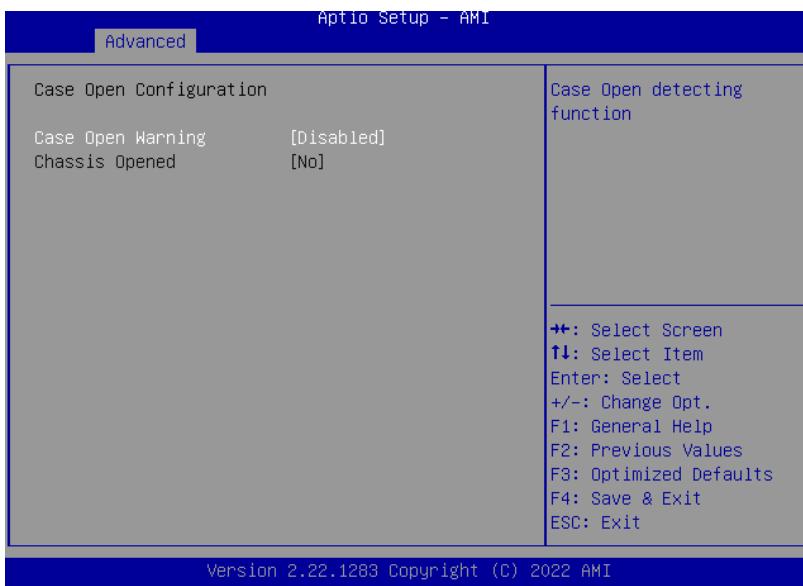
- **Mode for Power-off**

Configure LAN kit behavior when system is in power-off state. (Bypass/Pass Through)

- **WDT Configuration**

Configure WDT behavior, System Reset, Force Bypass.

### 3.4.14. Case Open Configuration

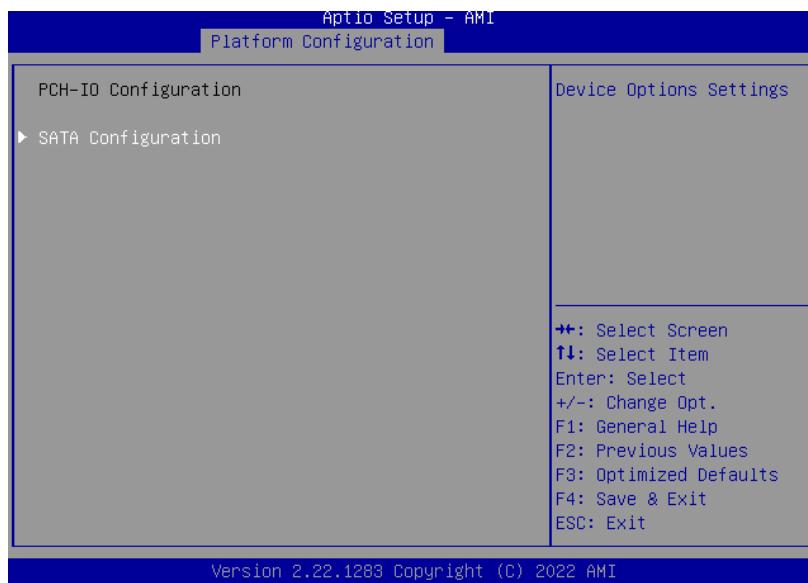


- **Case Open Warning**  
Case Open detecting function.

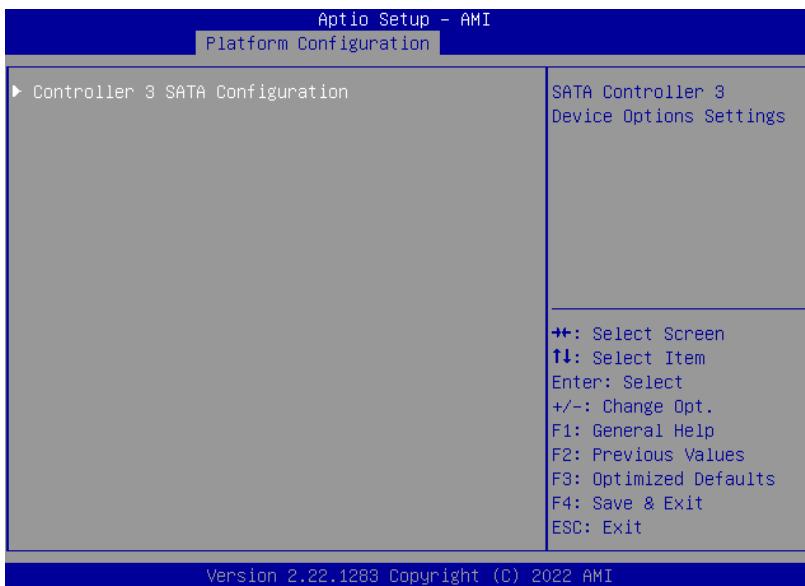
## 3.5. Setup submenu: Chipset



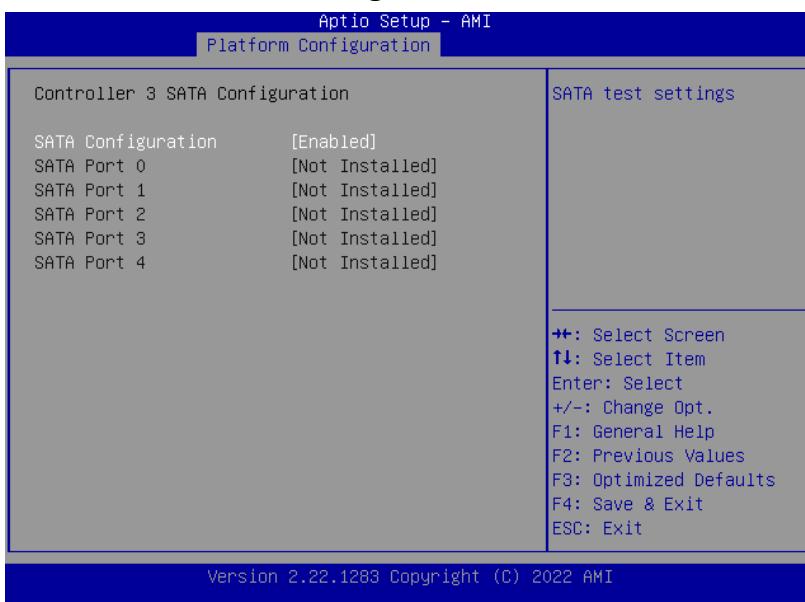
### 3.5.1. PCH-IO Configuration



### 3.5.2. SATA Configuration

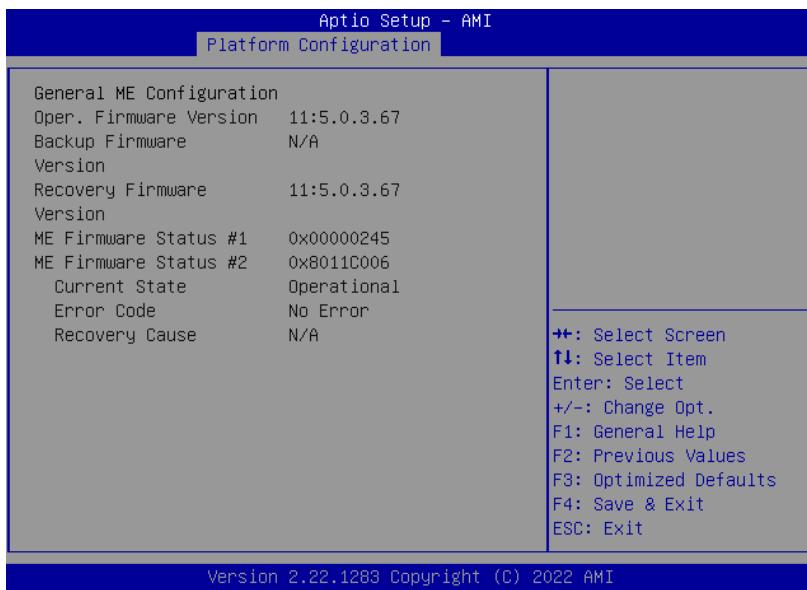


### 3.5.3. Controller 3 SATA Configuration

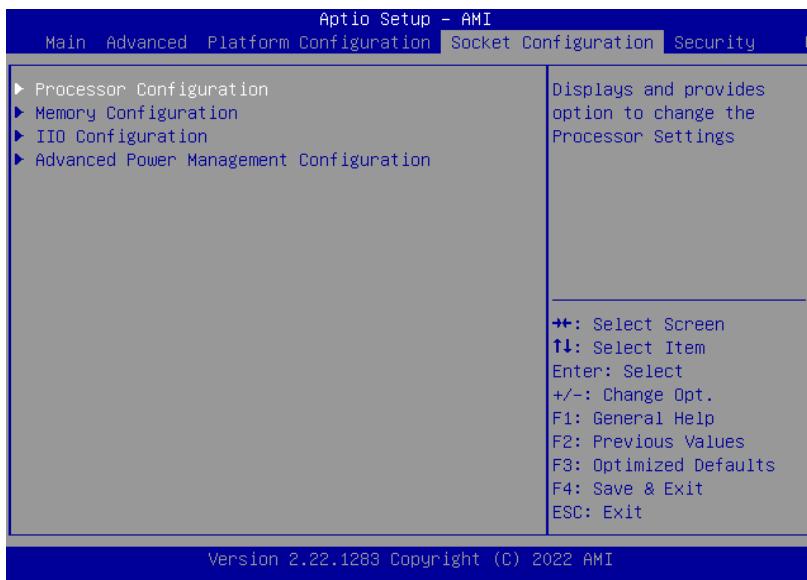


- **SATA Configuration**  
SATA test setting

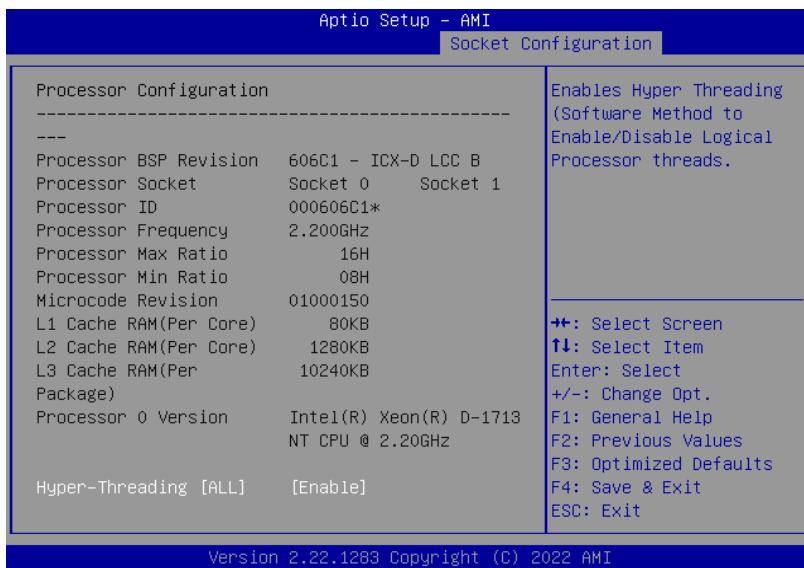
### 3.5.4. General ME Configuration



### 3.5.5. Socket Configuration



### 3.5.6. Processor Configuration



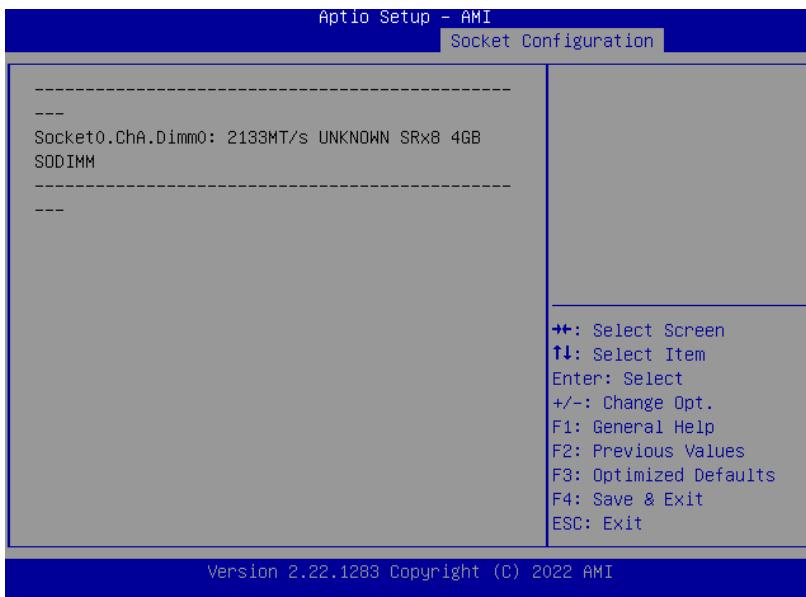
- **Hyper-Threading [ALL]**

Enables Hyper Threading (Software Method to Enable/Disable Logical Processor threads).

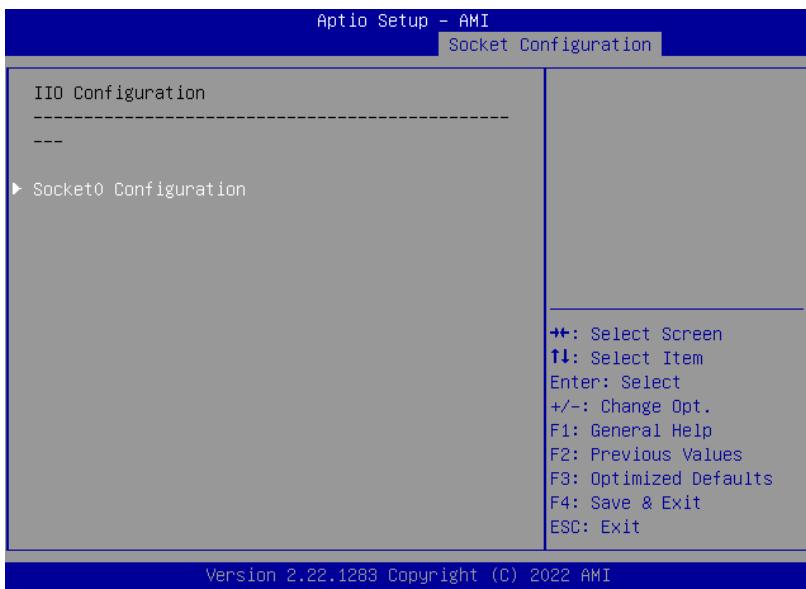
### 3.5.7. Memory Configuration



### 3.5.8. Memory Topology



### 3.5.9. IIO Configuration



### 3.5.10. Socket0 Configuration



- **IOU0 (IIO PCIe Port 1)**  
Selects PCIe port Bifurcation for selected slot(s).

### 3.5.11. Advanced Power Management Configuration



### 3.5.12. Hardware PM State Control



- **Hardware P-States**

Disable: Hardware chooses a P-state based on OS Request (Legacy P-States).

Native Mode: Hardware chooses a P-state based on OS guidance.

Out of Band Mode: Hardware autonomously chooses a P-state (no OS guidance).

### 3.6. Setup submenu: Security



- **Change User/Administrator Password**

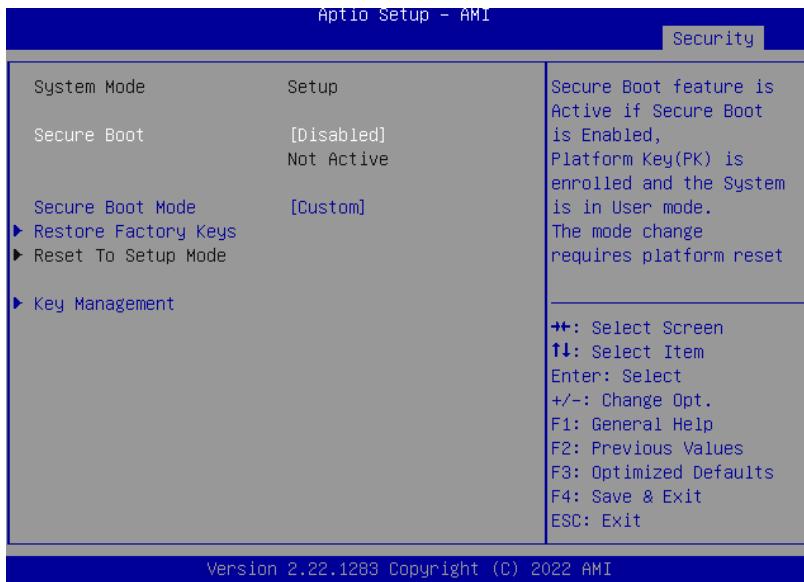
You can set an Administrator Password or User Password. An Administrator Password must be set before you can set a User Password. The password will be required during boot up, or when the user enters the Setup utility. A User Password does not provide access to many of the features in the Setup utility.

Select the password you wish to set, and press Enter. In the dialog box, enter your password (must be between 3 and 20 letters or numbers). Press Enter and retype your password to confirm. Press Enter again to set the password.

- **Removing the Password**

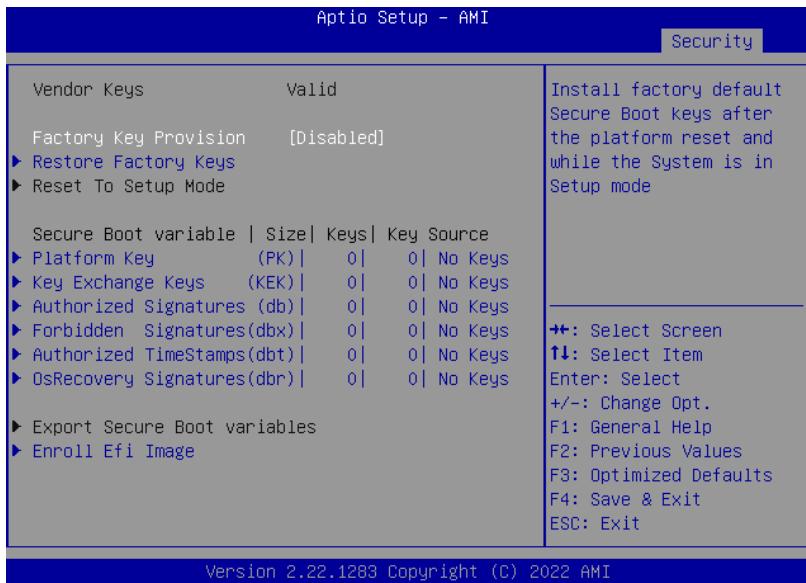
Select the password you want to remove and enter the current password. At the next dialog box press Enter to disable password protection.

### 3.6.1. Secure Boot



- **Secure Boot**  
Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled, and the System is in User mode. The mode change requires platform reset.
- **Secure Boot Mode**  
In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.
- **Restore Factory Keys**  
Force System to User Mode.  
Install factory default Secure Boot key databases.
- **Reset To Setup Mode**  
Delete all Secure Boot key databases from NVRAM.

### 3.6.1.1. Key Management



- **Factory Key Provision**

Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode.

- **Restore Factory Keys**

Force System to User Mode.

Install factory default Secure Boot key databases.

- **Reset To Setup Mode**

Delete all Secure Boot key databases from NVRAM.

- **Export Secure Boot variables**

Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device.

- **Enroll Efi Image**

Allow the image to run in Secure Boot mode.

Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db)

- **Secure Boot Variables**

Enroll Factory Defaults or load certificates from a file:

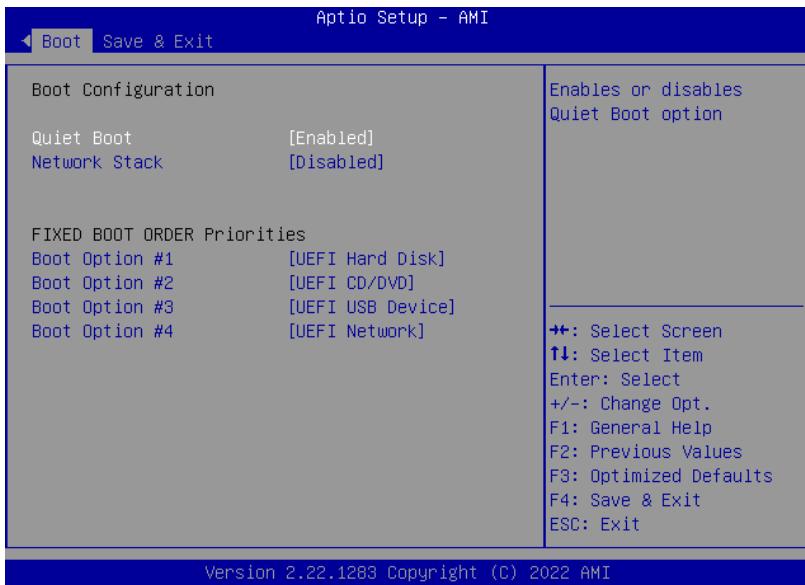
1. Public Key Certificate in:

- a) EFI\_SIGNATURE\_LIST

- b) EFI\_CERT\_X509 (DER encoded)

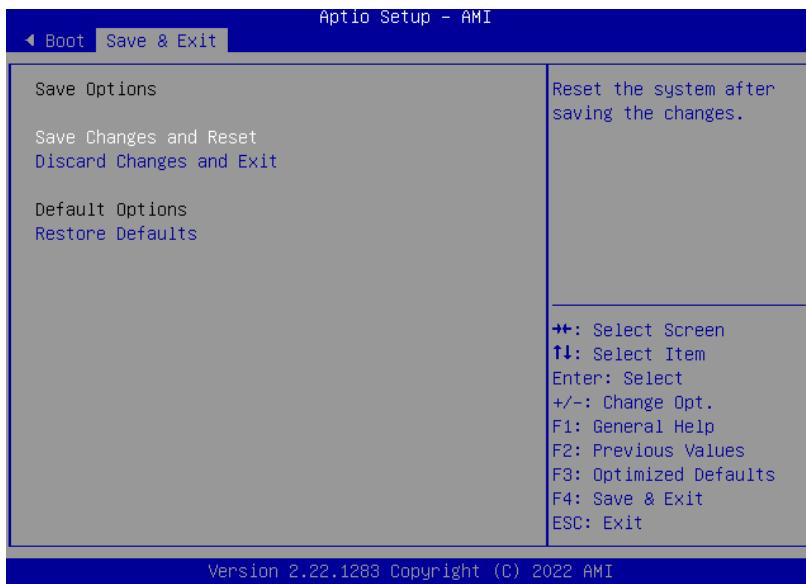
- c) EFI\_CERT\_RSA2048 (bin)
  - d) EFI\_CERT\_SHAXXX
2. Authenticated UEFI Variable
3. EFI PE/COFF Image (SHA256)
- Key Source: Default, External, Mixed

### 3.7. Setup submenu: Boot



- **Quiet Boot**  
Enable / Disable Quiet Boot option.
- **Network Stack**  
Enable/Disable UEFI Network Stack.

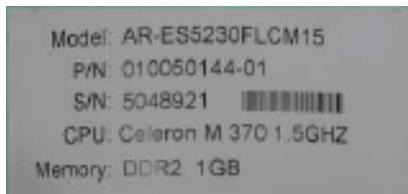
### 3.8. Setup submenu: Save & Exit



## 4. FAQ

### **Q 1. Where is the serial number located on my system?**

- The serial number (S/N) is an alpha-numeric character located on the bottom or side chassis.



(for reference only)

## Technical Support Form

We deeply appreciate your purchase of Acrosser products. Please find the “**tech\_form.doc**” file in our utility CD. If you have any questions or problems about Acrosser products, please fill in the following information. We will answer your questions in the shortest time possible.

### Describe Your Info and Acrosser System Info

- Your Company Name: \_\_\_\_\_
- Your Contact Info: \_\_\_\_\_ Phone Number: \_\_\_\_\_
- Your E-Mail Address: \_\_\_\_\_
- Your Company Address: \_\_\_\_\_  
\_\_\_\_\_
- Acrosser Model Name: \_\_\_\_\_
- Acrosser Serial Number: \_\_\_\_\_

### Describe System Configuration

- CPU Type: \_\_\_\_\_
- Memory Size: \_\_\_\_\_
- Storage Device (e.g. HDD, CF, or SSD): \_\_\_\_\_
- Additional Peripherals (e.g. Graphic Card): \_\_\_\_\_
- Operating System & Version (e.g. Windows 7 Embedded): \_\_\_\_\_
- Special API or Driver: \_\_\_\_\_  
(If yes, please provide it for debug.)
- Running Applications: \_\_\_\_\_
- Others: \_\_\_\_\_

### Describe Your Problems or Questions:

### Send the above information to one of the following Acrosser contacts:

- Acrosser Local Sales Representative
- Acrosser Authorized Sales Channels
- Acrosser Inquiry --- <http://www.acrosser.com/inquiry.html>
- Acrosser FAX Number --- 886-2-29992887

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