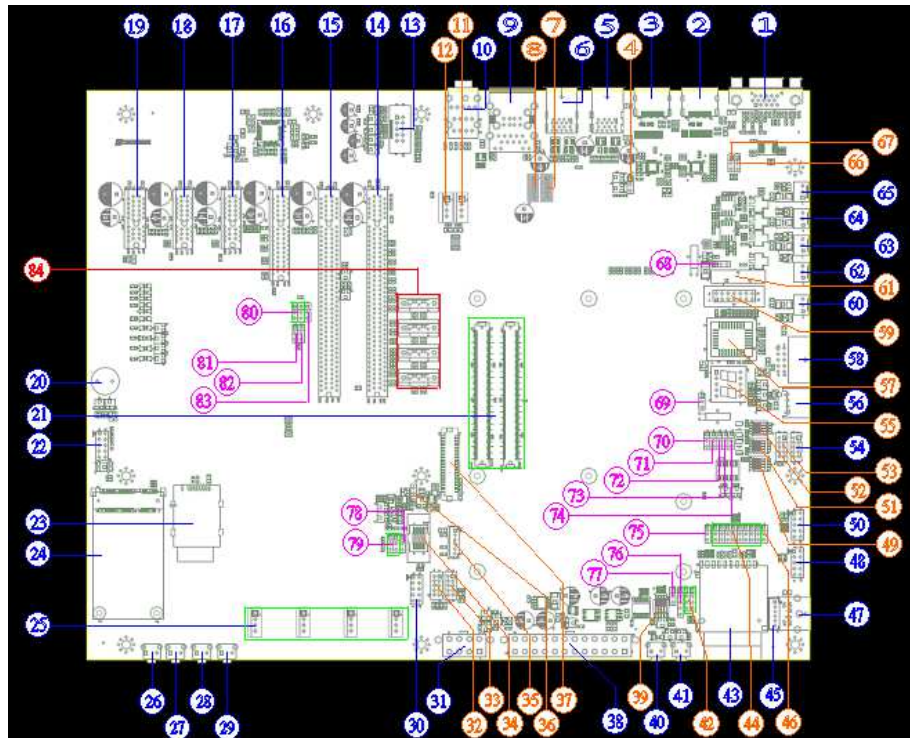


# ACM-B4080 Quick Manual

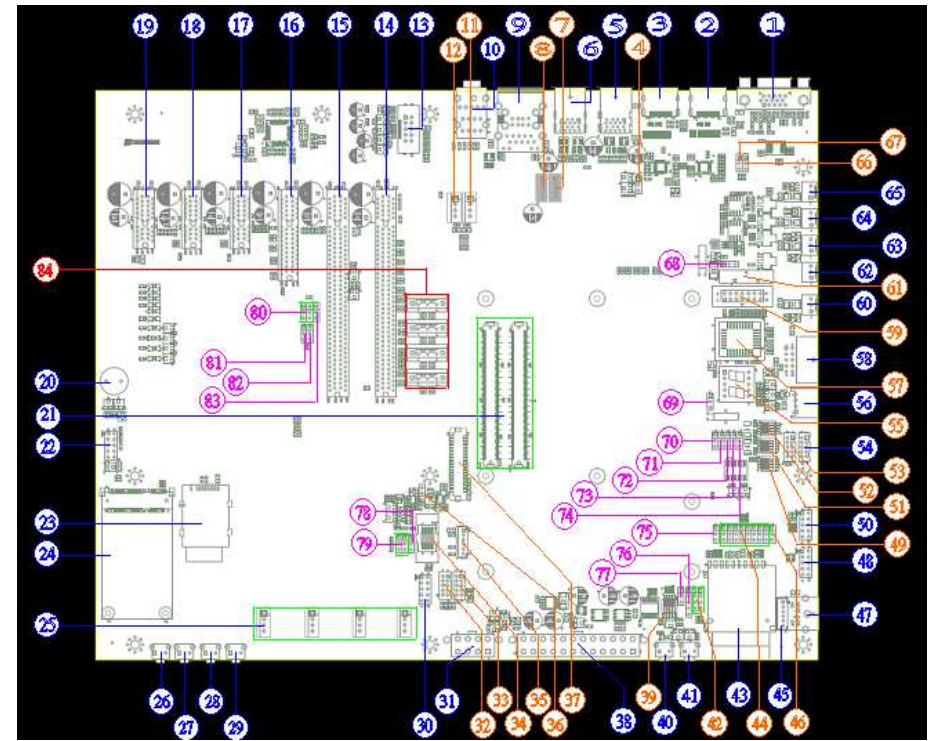
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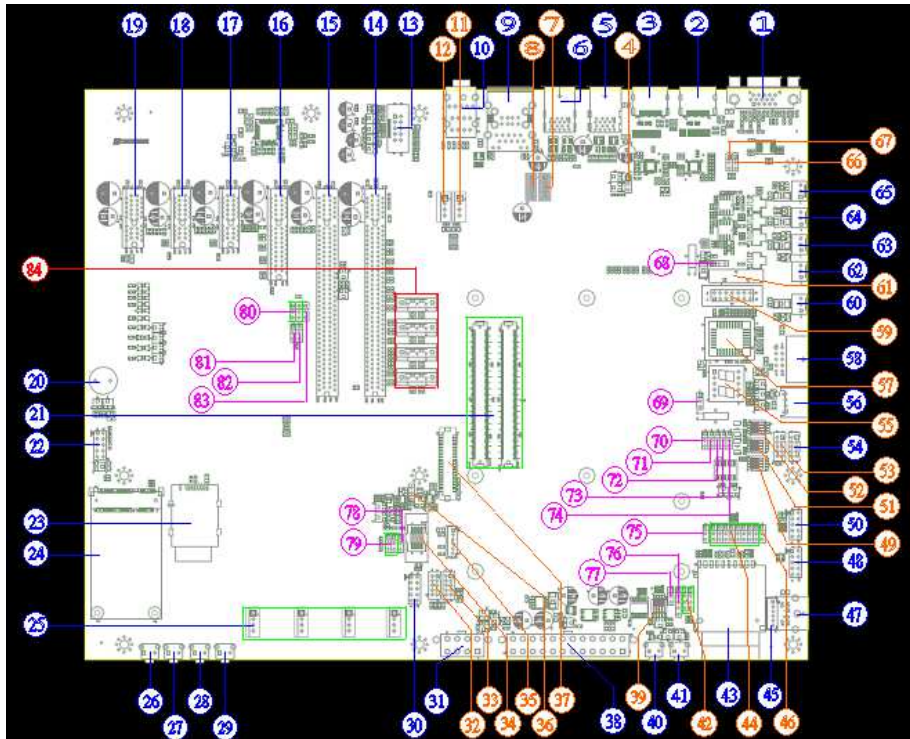
## 1. Main board illustration (Top Side)



|              |                      |            |
|--------------|----------------------|------------|
| ① CN_VGA1    | ② CN_HDMI2           | ③ CN_HDMI1 |
| ④ JP_USBVCC1 | ⑤ CN_2USB2           | ⑥ CN_2USB1 |
| ⑦ CN_USB2    | ⑧ CN_USB1            | ⑨ CN_USBL1 |
| ⑩ CN_AUDIO1  | ⑪ CN_I2C1 (Optional) | ⑫ CN_SMB1  |

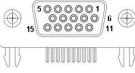
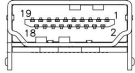
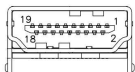


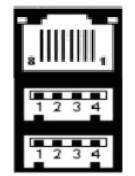



|                       |                       |                              |
|-----------------------|-----------------------|------------------------------|
| ⑬ FP-AUDIO1           | ⑭ PCIEX16P1           | ⑮ SDVO1                      |
| ⑯ PCIEX4P1            | ⑰ PCIEX1              | ⑱ PCIEX2                     |
| ⑲ PCIEX3              | ⑳ BZ1                 | ㉑ COM Express Type 6         |
| ㉒ FPIO1               | ㉓ CN_SIM1 (Optional)  | ㉔ CN_MPCIE1 (Optional)       |
| ㉕ SATA_PWR1~4         | ㉖ BTN_SLEEP1          | ㉗ BTN_BATLOW1 (Optional)     |
| ㉘ BTN_LID1 (Optional) | ㉙ BTN_ALERT1          | ㉚ SPI_CN1                    |
| ㉛ CN_ATX12V1          | ㉜ CN_COM1             | ㉝ CN_COM2                    |
| ㉞ SPI_SOCKET1         | ㉟ CN_INVERTER1        | ㊱ JP_LVDSVCC1                |
| ㊲ CN_LVDS1            | ㊳ CN_ATXPWR1          | ㊴ SW4                        |
| ㊵ BTN_RST1            | ㊶ BTN_PWR1            | ㊷ JP_COMRX1 / TX1 (Optional) |
| ㊸ CN_SD1 (Optional)   | ㊹ JP_GPIO1~8          | ㊺ KB_MS1 (Optional)          |
| ㊻ JP_GPVCC1           | ㊼ CN_KM1 (Optional)   | ㊽ FP_GPIO2                   |
| ㊾ SW3 (Optional)      | ㊿ FP_GPIO1 (Optional) | ① SW2 (Optional)             |

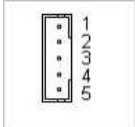
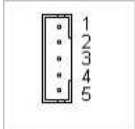
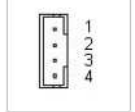
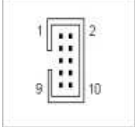









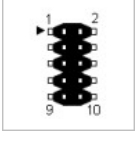
|                             |   |                           |
|-----------------------------|---|---------------------------|
| 52 SW1 (Optional)           | 53 CN_422H_485L1 (Optional)                                     | 54 CN_COMS1 (Optional)    |
| 55 LED_77S1                 | 56 CN_CCTALK2 (Optional)  | 57 FWH_SOCKET1 (Optional) |
| 58 CN_CCTALK1 (Optional)    | 59 LPC_CN1 (Optional)   | 60 FANSYS4                |
| 61 CN_BATTERY1              | 62 FANSYS3 (Optional)   | 63 FANSYS2 (Optional)     |
| 64 FANSYS1 (Optional)       | 65 FANCPU1 (Optional)   | 66 JP_DDC3 (Optional)     |
| 67 JP_DDC2 (Optional)       | 68 JP_CCMOS1<br><b>(Warning: MUST AC OFF before clear CMOS)</b> | 69 JP_TPM1 (Optional)     |
| 70 JP_LPC_DRQ1 (Optional)   | 71 JP_LPC_RST1 (Optional)                                       | 72 JP_LPC_ADD1 (Optional) |
| 73 JP_LPC_PWRSW1 (Optional) | 74 JP_LPC_PSON1 (Optional)                                      | 75 JP_CASE1 (Optional)    |
| 76 PIC_CN1                  | 77 JP_PSON1   | 78 JP_SPIVCC1             |
| 79 JP_BIOS1-2               | 80 JP_SDVO1-2 (Optional)  | 81 JP_EXCD1 (Optional)    |
| 82 JP_EXCD2 (Optional)      | 83 JP_DDC1 (Optional)   | 84 SATA1-4                |

## 2. ATX Back panel I/O Connectors

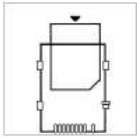
| 1. CN_VGA1    |    | DB15 VGA connector  |       |         |      |         |       |           |      |        |   |     |
|---------------|---|---|-------|---------|------|---------|-------|-----------|------|--------|---|-----|
| 2. CN_HDMI2   |    | HDMI connector  |       |         |      |         |       |           |      |        |   |     |
| 3. CN_HDMI1   |    | HDMI connector  |       |         |      |         |       |           |      |        |   |     |
| 5. CN_2USB2   |    | USB 3.0 connector   |       |         |      |         |       |           |      |        |   |     |
| 6. CN_2USB1   |    | USB 3.0 connector   |       |         |      |         |       |           |      |        |   |     |
| 9. CN_USBL1   |   | <table border="1"> <thead> <tr> <th>USB</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>5V</td> </tr> <tr> <td>2</td> <td>USB_N</td> </tr> <tr> <td>3</td> <td>USB_P</td> </tr> <tr> <td>4</td> <td>GND</td> </tr> </tbody> </table><br>USB 2.0 + RJ45 connector | USB   | SETTING | 1    | 5V      | 2     | USB_N     | 3    | USB_P  | 4 | GND |
| USB           | SETTING   |   |       |         |      |         |       |           |      |        |   |     |
| 1             | 5V  |   |       |         |      |         |       |           |      |        |   |     |
| 2             | USB_N   |   |       |         |      |         |       |           |      |        |   |     |
| 3             | USB_P   |   |       |         |      |         |       |           |      |        |   |     |
| 4             | GND   |   |       |         |      |         |       |           |      |        |   |     |
| 10. CN_AUDIO1 |  | <table border="1"> <thead> <tr> <th>color</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>Blue</td> <td>Line in</td> </tr> <tr> <td>Green</td> <td>SPEAK OUT</td> </tr> <tr> <td>Pink</td> <td>Mic in</td> </tr> </tbody> </table>  | color | SETTING | Blue | Line in | Green | SPEAK OUT | Pink | Mic in |   |     |
| color         | SETTING   |   |       |         |      |         |       |           |      |        |   |     |
| Blue          | Line in   |   |       |         |      |         |       |           |      |        |   |     |
| Green         | SPEAK OUT   |   |       |         |      |         |       |           |      |        |   |     |
| Pink          | Mic in  |   |       |         |      |         |       |           |      |        |   |     |

### 3. Onboard Connectors

| <b>7. CN_USB2</b>   |  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
|---|--|-----|----------|-----|---------|---|---------|---|---------|---|--------|-----------------------|------|---------------------|---------|---|---------|---|---------|---|----|---|---------|----|----------|------------------------|--|
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>5V</td> </tr> <tr> <td>2</td> <td>USB_N</td> </tr> <tr> <td>3</td> <td>USB_P</td> </tr> <tr> <td>4</td> <td>GND</td> </tr> <tr> <td>5</td> <td>3.3V</td> </tr> </tbody> </table>  | PIN | SETTING  | 1   | 5V      | 2 | USB_N   | 3 | USB_P   | 4 | GND    | 5                     | 3.3V | <b>JST_1X5P_2mm</b> |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| PIN   | SETTING  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 1   | 5V   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 2   | USB_N  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 3   | USB_P  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 4   | GND  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 5   | 3.3V   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| <b>8. CN_USB1</b>   |  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>5V</td> </tr> <tr> <td>2</td> <td>USB_N</td> </tr> <tr> <td>3</td> <td>USB_P</td> </tr> <tr> <td>4</td> <td>GND</td> </tr> <tr> <td>5</td> <td>3.3V</td> </tr> </tbody> </table>  | PIN | SETTING  | 1   | 5V      | 2 | USB_N   | 3 | USB_P   | 4 | GND    | 5                     | 3.3V | <b>JST_1X5P_2mm</b> |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| PIN   | SETTING  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 1   | 5V   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 2   | USB_N  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 3   | USB_P  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 4   | GND  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 5   | 3.3V   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| <b>11. CN_I2C1 (Optional)</b>   |  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
|    | <table border="1"> <thead> <tr> <th>Pin</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> </tr> <tr> <td>2</td> <td>I2C SDA</td> </tr> <tr> <td>3</td> <td>I2C SCL</td> </tr> <tr> <td>4</td> <td>3.3V</td> </tr> </tbody> </table>  | Pin | SETTING  | 1   | GND     | 2 | I2C SDA | 3 | I2C SCL | 4 | 3.3V   | <b>JST_1X4P_2.5mm</b> |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| Pin   | SETTING  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 1   | GND  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 2   | I2C SDA  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 3   | I2C SCL  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 4   | 3.3V   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| <b>12. CN_SMB1</b>  |  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
|    | <table border="1"> <thead> <tr> <th>Pin</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> </tr> <tr> <td>2</td> <td>SMB SDA</td> </tr> <tr> <td>3</td> <td>SMD SCL</td> </tr> <tr> <td>4</td> <td>3.3V</td> </tr> </tbody> </table>  | Pin | SETTING  | 1   | GND     | 2 | SMB SDA | 3 | SMD SCL | 4 | 3.3V   | <b>JST_1X4P_2.5mm</b> |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| Pin   | SETTING  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 1   | GND  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 2   | SMB SDA  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 3   | SMD SCL  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 4   | 3.3V   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| <b>13. FP-AUDIO1</b>  |  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Mic2 L</td> <td>2</td> <td>AGND</td> </tr> <tr> <td>3</td> <td>Mic2 R</td> <td>4</td> <td>NC</td> </tr> <tr> <td>5</td> <td>FPOUT L</td> <td>6</td> <td>JD Mic2</td> </tr> <tr> <td>7</td> <td>Sense B</td> <td>8</td> <td>NC</td> </tr> <tr> <td>9</td> <td>FPOUT R</td> <td>10</td> <td>JD FPOUT</td> </tr> </tbody> </table> | PIN | SETTING  | PIN | SETTING | 1 | Mic2 L  | 2 | AGND    | 3 | Mic2 R | 4                     | NC   | 5                   | FPOUT L | 6 | JD Mic2 | 7 | Sense B | 8 | NC | 9 | FPOUT R | 10 | JD FPOUT | <b>BOX_2x5P_2.54mm</b> |  |
| PIN   | SETTING  | PIN | SETTING  |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 1   | Mic2 L   | 2   | AGND     |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 3   | Mic2 R   | 4   | NC       |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 5   | FPOUT L  | 6   | JD Mic2  |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 7   | Sense B  | 8   | NC       |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| 9   | FPOUT R  | 10  | JD FPOUT |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
| <b>14. PCIEX16P1</b>  |  |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |
|  | <b>Standard PCI Express 16x slot</b>   |     |          |     |         |   |         |   |         |   |        |                       |      |                     |         |   |         |   |         |   |    |   |         |    |          |                        |  |

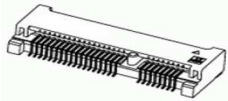
| <b>15. SDVO1</b>  |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|---|--|-----|----------|-----|---------|---|----------|---|----------|---|----------|---|----------|---|---------|---|---------|---|---------|---|---------|---|---------|----|---------|--|
|    | <b>Intel SDVO (Serialized Digital Video Output) interface slot (Use PCI-E 16x slot)</b>  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| <b>16. PCIEX4P1</b>   |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|    | <b>Standard PCI Express 4x slot</b>  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| <b>17. PCIE1</b>  |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|    | <b>Standard PCI Express 1x slot</b>  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| <b>18. PCIE2</b>  |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|    | <b>Standard PCI Express 1x slot</b>  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| <b>19. PCIE3</b>  |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|    | <b>Standard PCI Express 1x slot</b>  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| <b>21. COM Express Type 6</b>   |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|   | <b>Standard COM Express Type6 carrier board connector</b>  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| <b>22. FPIO1</b>  |  |     |          |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>PWR LED-</td> <td>2</td> <td>PWR LED+</td> </tr> <tr> <td>3</td> <td>HDD LED-</td> <td>4</td> <td>HDD LED+</td> </tr> <tr> <td>5</td> <td>RST GND</td> <td>6</td> <td>RST SW+</td> </tr> <tr> <td>7</td> <td>PWR GND</td> <td>8</td> <td>PWR SW+</td> </tr> <tr> <td>9</td> <td>Auto SW</td> <td>10</td> <td>PWR SW+</td> </tr> </tbody> </table> | PIN | SETTING  | PIN | SETTING | 1 | PWR LED- | 2 | PWR LED+ | 3 | HDD LED- | 4 | HDD LED+ | 5 | RST GND | 6 | RST SW+ | 7 | PWR GND | 8 | PWR SW+ | 9 | Auto SW | 10 | PWR SW+ | <b>FPIO1 9-10 AT/ATX Mode Power ON Short --&gt; AT Mode (Default) Open --&gt; ATX Mode</b> |
| PIN   | SETTING  | PIN | SETTING  |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| 1   | PWR LED-   | 2   | PWR LED+ |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| 3   | HDD LED-   | 4   | HDD LED+ |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| 5   | RST GND  | 6   | RST SW+  |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| 7   | PWR GND  | 8   | PWR SW+  |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |
| 9   | Auto SW  | 10  | PWR SW+  |     |         |   |          |   |          |   |          |   |          |   |         |   |         |   |         |   |         |   |         |    |         |  |

**23. CN\_SIM1** (Optional)



Standard SIM Card Connector  
Link to "CN\_MPCIE1" connector  
For 3G Module support.

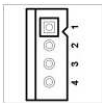
**24. CN\_MPCIE1** (Optional)



Standard mini-PCIe Connector

**Note:**  
Could change the interface to "PCIE3"  
**PS.** For ACM-B6360 need H/W BOM  
changed to support this function.

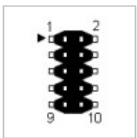
**25. SATA\_PWR1-4**



| PIN | SETTING |
|-----|---------|
| 1   | 12V     |
| 2   | GND     |
| 3   | 3.3V    |
| 4   | 5V      |

JST\_1X4P\_2.5mm

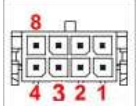
**30. SPI\_CN1**



| PIN | SETTING | PIN | SETTING |
|-----|---------|-----|---------|
| 1   | CS#     | 2   | 3.3V    |
| 3   | MISO    | 4   | HOLD#   |
| 5   | WP#     | 6   | CLK     |
| 7   | GND     | 8   | MOSI    |
| 9   | NC      | 10  | NC      |

HDR\_2X5P\_2.54mm  
For SPI Flash BIOS tools connect.

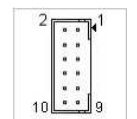
**31. CN\_ATX12V1**



| PIN | SETTING | PIN | SETTING |
|-----|---------|-----|---------|
| 1   | GND     | 5   | 12V     |
| 2   | GND     | 6   | 12V     |
| 3   | GND     | 7   | 12V     |
| 4   | GND     | 8   | 12V     |

MINIFIT-2X4-4P2-FIX2  
ATX Power 12V in

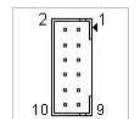
**32. CN\_COM1**



| PIN | SETTING  | PIN | SETTING |
|-----|----------|-----|---------|
| 2   | NC       | 1   | NC      |
| 4   | RS232 RX | 3   | NC      |
| 6   | RS232 TX | 5   | NC      |
| 8   | NC       | 7   | NC      |
| 10  | GND      | 9   | NC      |

JST\_2X5P\_2mm  
RS232 TX / RX

**33. CN\_COM2**



| PIN | SETTING  | PIN | SETTING |
|-----|----------|-----|---------|
| 2   | NC       | 1   | NC      |
| 4   | RS232 RX | 3   | NC      |
| 6   | RS232 TX | 5   | NC      |
| 8   | NC       | 7   | NC      |
| 10  | GND      | 9   | NC      |

JST\_2X5P\_2mm  
RS232 TX / RX

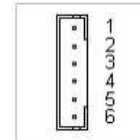
**34. SPI\_SOCKET1**



| PIN | SETTING | PIN | SETTING |
|-----|---------|-----|---------|
| 1   | CS#     | 8   | 3.3V    |
| 2   | MISO    | 7   | HOLD#   |
| 3   | WP#     | 6   | CLK     |
| 4   | GND     | 5   | MOSI    |

SPI BIOS SOCKET

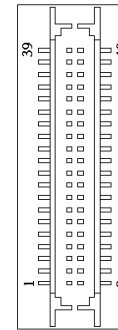
**35. CN\_INVERTER1**



| PIN | SETTING |
|-----|---------|
| 1   | 12V     |
| 2   | 12V     |
| 3   | GND     |
| 4   | BLON    |
| 5   | GND     |
| 6   | BLIM    |

JST\_1X6P\_2mm

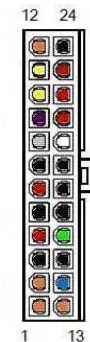
**37. CN\_LVDS1**



| CN_LVDS1 |            | CN_WAF-H_2X20P_1.25mm |    |
|----------|------------|-----------------------|----|
| 39       | GND39      | GND40                 | 40 |
| 37       | A0 CLK P   | B0 CLK P              | 38 |
| 35       | A0 CLK N   | B0 CLK N              | 36 |
| 33       | A0 DATA3 P | B0 DATA3 P            | 34 |
| 31       | A0 DATA3 N | B0 DATA3 N            | 32 |
| 29       | A0 DATA2 P | B0 DATA2 P            | 30 |
| 27       | A0 DATA2 N | B0 DATA2 N            | 28 |
| 25       | GND27      | GND28                 | 26 |
| 23       | A0 DATA1 P | B0 DATA1 P            | 24 |
| 21       | A0 DATA1 N | B0 DATA1 N            | 22 |
| 19       | A0 DATA0 P | B0 DATA0 P            | 20 |
| 17       | A0 DATA0 N | B0 DATA0 N            | 18 |
| 15       | GND15      | GND16                 | 14 |
| 13       | A0 DATA0 P | B0 DATA0 P            | 12 |
| 11       | A0 DATA0 N | B0 DATA0 N            | 10 |
| 9        | GND9       | GND10                 | 8  |
| 7        | SDA        | SCL                   | 6  |
| 5        | GND5       | GND6                  | 4  |
| 3        | VCCM3      | VCCM4                 | 2  |
| 1        | VCCM1      | VCCM2                 |    |

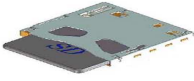
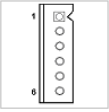

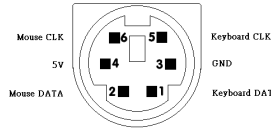
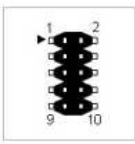
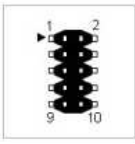
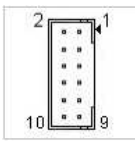
DF13\_2X20P\_1.25mm

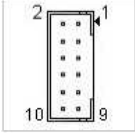
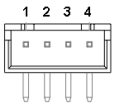
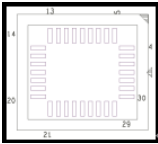
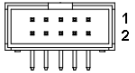
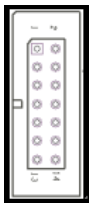

**38. CN\_ATXPWR1**



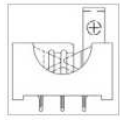
| Pin    | Setting | Pin | Setting |
|--------|---------|-----|---------|
| +3.3V  | COM     | 12  | 24      |
| +12V   | +5V     |     |         |
| +12V   | +5V     |     |         |
| +5VSB  | +5V     |     |         |
| PWR_OK | N/C     |     |         |
| COM    | COM     |     |         |
| +5V    | COM     |     |         |
| COM    | COM     |     |         |
| +5V    | PS_ON   |     |         |
| COM    | COM     |     |         |
| +3.3V  | N/C     |     |         |
| +3.3V  | +3.3V   |     |         |

AXT 24pin Power Supply

| 43. CN_SD1 (Optional)   |   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
|---|---|----------------------------|---------|-------------|------------|---|---------------|---|--------|---|-------------|---|-----------|---|--------------|--------------|---------|---|--------|---|---------|----|--------|----|-----|---|------|------|------|------|---------|-----|----|-----|----|-------------|----|----|-----|----|-------------|
|    | SD Card Connector   | PS. AR-B6360 not supported |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 45. KB_MS1 (Optional)   |   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>1</td><td>Mouse DATA</td></tr> <tr><td>2</td><td>Keyboard DATA</td></tr> <tr><td>3</td><td>GND</td></tr> <tr><td>4</td><td>5V</td></tr> <tr><td>5</td><td>Mouse CLK</td></tr> <tr><td>6</td><td>Keyboard CLK</td></tr> </tbody> </table>   | PIN                        | SETTING | 1           | Mouse DATA | 2 | Keyboard DATA | 3 | GND    | 4 | 5V          | 5 | Mouse CLK | 6 | Keyboard CLK | JST_1X6P_2mm |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| PIN   | SETTING   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 1   | Mouse DATA  |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 2   | Keyboard DATA   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 3   | GND   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 4   | 5V  |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 5   | Mouse CLK   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 6   | Keyboard CLK  |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 47. CN_KM1  |   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
|    |    | PS2 Keyboard / Mouse       |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 48. FP_GPIO2  |   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
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| PIN   | SETTING   | PIN                        | SETTING |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 1   | VCC   | 2                          | GPO0    |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 3   | GPI0  | 4                          | GPO1    |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 5   | GPI1  | 6                          | GPO2    |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 7   | GPI2  | 8                          | GPO3    |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 9   | GPI3  | 10                         | GND     |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 50. FP_GPIO1 (Optional)   |   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
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| PIN   | SETTING   | PIN                        | SETTING |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 1   | VCC   | 2                          | GPIO34  |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 3   | GPIO30  | 4                          | GPIO35  |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 5   | GPIO31  | 6                          | GPIO36  |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 7   | GPIO32  | 8                          | GPIO37  |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 9   | GPIO33  | 10                         | GND     |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 53. CN_422H_485L1 (Optional)  |   |                            |         |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>2</td><td>485_422_TX+</td><td>1</td><td>NC</td></tr> <tr><td>4</td><td>485_422_TX-</td><td>3</td><td>NC</td></tr> <tr><td>6</td><td>NC</td><td>5</td><td>422_RX-</td></tr> <tr><td>8</td><td>NC</td><td>7</td><td>422_RX+</td></tr> <tr><td>10</td><td>GND</td><td>9</td><td>Key</td></tr> </tbody> </table>     | PIN                        | SETTING | PIN         | SETTING    | 2 | 485_422_TX+   | 1 | NC     | 4 | 485_422_TX- | 3 | NC        | 6 | NC           | 5            | 422_RX- | 8 | NC     | 7 | 422_RX+ | 10 | GND    | 9  | Key | JST_2X5P_2mm<br>422/485 Connector<br>PS: setting with "SW3": <table border="1"> <thead> <tr> <th>No.1</th> <th>No.2</th> <th>No.3</th> <th>No.4</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>OFF</td> <td>ON</td> <td>OFF</td> <td>ON</td> <td>RS-422 mode</td> </tr> <tr> <td>ON</td> <td>ON</td> <td>OFF</td> <td>ON</td> <td>RS-485 mode</td> </tr> </tbody> </table> | No.1 | No.2 | No.3 | No.4 | SETTING | OFF | ON | OFF | ON | RS-422 mode | ON | ON | OFF | ON | RS-485 mode |
| PIN   | SETTING   | PIN                        | SETTING |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 2   | 485_422_TX+   | 1                          | NC      |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 4   | 485_422_TX-   | 3                          | NC      |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 6   | NC  | 5                          | 422_RX- |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 8   | NC  | 7                          | 422_RX+ |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| 10  | GND   | 9                          | Key     |             |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| No.1  | No.2  | No.3                       | No.4    | SETTING     |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| OFF   | ON  | OFF                        | ON      | RS-422 mode |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |
| ON  | ON  | OFF                        | ON      | RS-485 mode |            |   |               |   |        |   |             |   |           |   |              |              |         |   |        |   |         |    |        |    |     |   |      |      |      |      |         |     |    |     |    |             |    |    |     |    |             |

| 54. CN_COMS1 (Optional)   |   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
|---|---|----------------------------|-----------|-----|---------|---|---------|---|------|-----------------|---------|--|------|---|--------|---|------|---|------|---|-----|----|------|----|-----------|--|--------|----|----|----|------|----|----|----------------------------|
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>2</td><td>DCD</td><td>1</td><td>DSR</td></tr> <tr><td>4</td><td>RX</td><td>3</td><td>RTS</td></tr> <tr><td>6</td><td>TX</td><td>5</td><td>CTS</td></tr> <tr><td>8</td><td>DTR</td><td>7</td><td>RI</td></tr> <tr><td>10</td><td>GND</td><td>9</td><td>Key</td></tr> </tbody> </table>  | PIN                        | SETTING   | PIN | SETTING | 2 | DCD     | 1 | DSR  | 4               | RX      | 3  | RTS  | 6 | TX     | 5 | CTS  | 8 | DTR  | 7 | RI  | 10 | GND  | 9  | Key       | JST_2X5P_2mm   |        |    |    |    |      |    |    |                            |
| PIN   | SETTING   | PIN                        | SETTING   |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 2   | DCD   | 1                          | DSR       |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 4   | RX  | 3                          | RTS       |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 6   | TX  | 5                          | CTS       |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 8   | DTR   | 7                          | RI        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 10  | GND   | 9                          | Key       |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 56. CN_CCTALK2 (Optional ; Carrier board extended function)                           |   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>4</td><td>DATA</td></tr> <tr><td>3</td><td>GND</td></tr> <tr><td>2</td><td>NC</td></tr> <tr><td>1</td><td>12V</td></tr> </tbody> </table>  | PIN                        | SETTING   | 4   | DATA    | 3 | GND     | 2 | NC   | 1               | 12V     | JST_1X4P_2.5mm<br>Note:<br>Function enable by "JP_COMRX1" & "JP_COMTX1" Jumper setting be 2-3 or 3-4 (default disable) |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| PIN   | SETTING   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 4   | DATA  |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 3   | GND   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 2   | NC  |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 1   | 12V   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 57. FWH_SOCKET1 (Optional)  |   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
|    | PLCC32 SOCKET   | PS. AR-B6360 not supported |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 58. CN_CCTALK1 (Optional ; Carrier board extended function)                           |   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>1</td><td>DATA</td><td>2</td><td>NC</td></tr> <tr><td>3</td><td>NC</td><td>4</td><td>NC</td></tr> <tr><td>5</td><td>NC</td><td>6</td><td>NC</td></tr> <tr><td>7</td><td>12V</td><td>8</td><td>GND</td></tr> <tr><td>9</td><td>NC</td><td>10</td><td>NC</td></tr> </tbody> </table>   | PIN                        | SETTING   | PIN | SETTING | 1 | DATA    | 2 | NC   | 3               | NC      | 4  | NC   | 5 | NC     | 6 | NC   | 7 | 12V  | 8 | GND | 9  | NC   | 10 | NC        | BOX_2X5P_2.5mm<br>Note:<br>Function enable by "JP_COMRX1" & "JP_COMTX1" Jumper setting be 2-3 or 3-4 (default disable) |        |    |    |    |      |    |    |                            |
| PIN   | SETTING   | PIN                        | SETTING   |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 1   | DATA  | 2                          | NC        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 3   | NC  | 4                          | NC        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 5   | NC  | 6                          | NC        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 7   | 12V   | 8                          | GND       |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 9   | NC  | 10                         | NC        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 59. LPC_CN1 (Optional)  |   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>1</td><td>LPC CLK</td><td>2</td><td>LDA1</td></tr> <tr><td>3</td><td>PCIRET#</td><td>4</td><td>LDA0</td></tr> <tr><td>5</td><td>FRAME#</td><td>6</td><td>3.3V</td></tr> <tr><td>7</td><td>LDA3</td><td>8</td><td>GND</td></tr> <tr><td>9</td><td>LDA2</td><td>10</td><td>Pull-high</td></tr> <tr><td>11</td><td>SERIRQ</td><td>12</td><td>NC</td></tr> <tr><td>13</td><td>5VSB</td><td>14</td><td>5V</td></tr> </tbody> </table> | PIN                        | SETTING   | PIN | SETTING | 1 | LPC CLK | 2 | LDA1 | 3               | PCIRET# | 4  | LDA0 | 5 | FRAME# | 6 | 3.3V | 7 | LDA3 | 8 | GND | 9  | LDA2 | 10 | Pull-high | 11   | SERIRQ | 12 | NC | 13 | 5VSB | 14 | 5V | BOX_2X5P_2.54mm<br>LPC BUS |
| PIN   | SETTING   | PIN                        | SETTING   |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 1   | LPC CLK   | 2                          | LDA1      |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 3   | PCIRET#   | 4                          | LDA0      |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 5   | FRAME#  | 6                          | 3.3V      |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 7   | LDA3  | 8                          | GND       |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 9   | LDA2  | 10                         | Pull-high |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 11  | SERIRQ  | 12                         | NC        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 13  | 5VSB  | 14                         | 5V        |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 60. FANSYS4   |   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr><td>3</td><td>FAN_IN</td></tr> <tr><td>2</td><td>12V</td></tr> <tr><td>1</td><td>GND</td></tr> </tbody> </table>   | PIN                        | SETTING   | 3   | FAN_IN  | 2 | 12V     | 1 | GND  | WAF_1X3P_2.54mm |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| PIN   | SETTING   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 3   | FAN_IN  |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 2   | 12V   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |
| 1   | GND   |                            |           |     |         |   |         |   |      |                 |         |  |      |   |        |   |      |   |      |   |     |    |      |    |           |  |        |    |    |    |      |    |    |                            |

**61. CN\_BATTERY1**



Battery CR2032

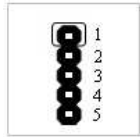
- 62. FANSYS3 (Optional)
- 63. FANSYS2 (Optional)
- 64. FANSYS1 (Optional)
- 65. FANCPU1 (Optional)



| PIN | SETTING |
|-----|---------|
| 3   | FAN_IN  |
| 2   | 12V     |
| 1   | GND     |

WAF\_1X3P\_2.54mm  
No FAN Control

**76. PIC\_CN1**



| PIN | SETTING |
|-----|---------|
| 1   | PIC VCC |
| 2   | ISPDATA |
| 3   | ISPCLK  |
| 4   | ISPPPP  |
| 5   | GND     |

HDR\_1X5P\_2.54mm  
PIC Tool

**84. SATA1-4**

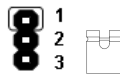


| PIN | SETTING |
|-----|---------|
| 1   | GND     |
| 2   | TX+     |
| 3   | TX-     |
| 4   | GND     |
| 5   | RX-     |
| 6   | RX+     |
| 7   | GND     |

SATA1-4 Connector

## 4.Jumper Setting

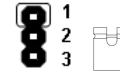
**4. JP\_USBVCC1**



| PIN | SETTING   |
|-----|-----------|
| 1   | RUNSB     |
| 2   | USB_EN    |
| 3   | ATX_PWROK |

1.JP\_USBVCC1 USB Power  
1-2: SVDUAL (Default)  
2-3: VCC5  
2. HDR\_1X3P\_2.54mm

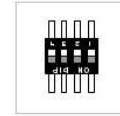
**36. JP\_LVDSVCC1**



| PIN | SETTING  |
|-----|----------|
| 1   | +5V      |
| 2   | LVDS VCC |
| 3   | +3.3V    |

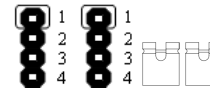
LVDS VCC Power selection:  
1-2: 5V (Default)  
2-3: 3.3V

**39. SW4**



|     | No.4 | No.3 | No.2 | No.1 | SETTING            |
|-----|------|------|------|------|--------------------|
| 0   | ON   | ON   | ON   | ON   | ATX mode (Default) |
| 1   | ON   | ON   | ON   | OFF  | Reserved           |
| ... |      |      |      |      |                    |
| 13  | OFF  | OFF  | ON   | OFF  | Reserved           |
| 14  | OFF  | OFF  | OFF  | ON   | Reserved           |
| 15  | OFF  | OFF  | OFF  | OFF  | Reserved           |

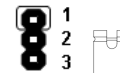
**42. JP\_COMRX1 / TX1** (Optional ; Carrier board extended function)



| PIN | RX1     | PIN | TX1     |
|-----|---------|-----|---------|
| 1   | PIC TX  | 1   | PIC RX  |
| 2   | SIO RX  | 2   | SIO TX  |
| 3   | UART RX | 3   | UART TX |
| 4   | PIC RX  | 4   | PIC TX  |

PIC & ccTalk connection setting:  
1-2: PIC to Carrier board LPC SIO (default)  
2-3: Carrier board LPC SIO to ccTalk1/2  
3-4: PIC to ccTalk1/2

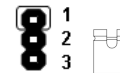
**44. JP\_GPIO1-8**



| PIN | JP_GPIO1-8  |
|-----|-------------|
| 1   | GPIO 1-8    |
| 2   | I/O 1-8     |
| 3   | SD Card 1-8 |

GPIO / SD Card selection:  
1-2:GPIO 1-8 (default)  
2-3:SD Card

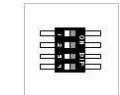
**46. JP\_GPVCC1**



| PIN | SETTING    |
|-----|------------|
| 1   | 5V         |
| 2   | FP_GPIO1-2 |
| 3   | 3.3V       |

FP\_GPIO1-2 Power  
1-2: 5V  
2-3: 3.3V (Default)

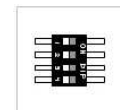
**49. SW3** (Optional)



| No.1 | No.2 | No.3 | No.4 | SETTING     |
|------|------|------|------|-------------|
| OFF  | OFF  | OFF  | OFF  | RS-232 mode |
| OFF  | ON   | OFF  | ON   | RS-422 mode |
| ON   | ON   | OFF  | ON   | RS-485 mode |

No.1: RS-485 Enable SW  
No.2: RS-422/485 Enable SW  
No.3: RS-422/485 Auto RTS SW  
No.4: RS-422/485 Auto TX SW

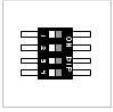
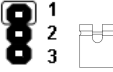
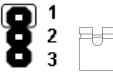
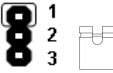
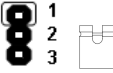
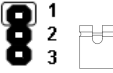

**51. SW2** (Optional)

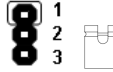
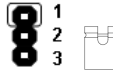
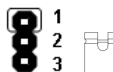
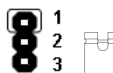

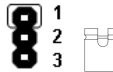




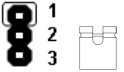
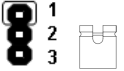
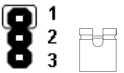
| No. | SETTING         |
|-----|-----------------|
| 1   | 665 ohm 422 RX+ |
| 2   | 120 ohm+        |
| 3   | 120 ohm-        |
| 4   | 665 ohm 422 RX- |

1 & 4 : OFF (Default)








| SW2 2-3 | RS-422 Termination 120 ohm |
|---------|----------------------------|
| ON      | Enable                     |
| OFF     | Disable(Default)           |

| <b>52. SW1</b> (Optional)   |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|---|--|-----|---------|-----|--------------------|-----|----------------|-----------------|----------|--|--------------------|--|---------|----------------------------|----|--------|-----|-------------------|
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>665 ohm 485 TX/RX+</td> </tr> <tr> <td>2</td> <td>120 ohm+</td> </tr> <tr> <td>3</td> <td>120 ohm-</td> </tr> <tr> <td>4</td> <td>665 ohm 485 TX/RX-</td> </tr> </tbody> </table> | PIN | SETTING | 1   | 665 ohm 485 TX/RX+ | 2   | 120 ohm+       | 3               | 120 ohm- | 4  | 665 ohm 485 TX/RX- | <p>1 &amp; 4 : OFF (Default)</p> <table border="1"> <thead> <tr> <th>SW1 2-3</th> <th>RS-485 Termination 120 ohm</th> </tr> </thead> <tbody> <tr> <td>ON</td> <td>Enable</td> </tr> <tr> <td>OFF</td> <td>Disable (Default)</td> </tr> </tbody> </table> | SW1 2-3 | RS-485 Termination 120 ohm | ON | Enable | OFF | Disable (Default) |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1   | 665 ohm 485 TX/RX+   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2   | 120 ohm+   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 3   | 120 ohm-   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 4   | 665 ohm 485 TX/RX-   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| SW1 2-3   | RS-485 Termination 120 ohm   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| ON  | Enable   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| OFF   | Disable (Default)  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| <b>66. JP_DDC3</b> (Optional)   |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|    | <p>"CN_HDMI2" DDC/AUX setting</p> <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>DDC (Default)</td> </tr> <tr> <td>2-3</td> <td>AUX (Reserved)</td> </tr> </tbody> </table>  | PIN | SETTING | 1-2 | DDC (Default)      | 2-3 | AUX (Reserved) | HDR_1X3P_2.54mm |          |  |                    |  |         |                            |    |        |     |                   |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1-2   | DDC (Default)  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2-3   | AUX (Reserved)   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| <b>67. JP_DDC2</b> (Optional)   |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|    | <p>"CN_HDMI1" DDC/AUX setting</p> <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>DDC (Default)</td> </tr> <tr> <td>2-3</td> <td>AUX (Reserved)</td> </tr> </tbody> </table>  | PIN | SETTING | 1-2 | DDC (Default)      | 2-3 | AUX (Reserved) | HDR_1X3P_2.54mm |          |  |                    |  |         |                            |    |        |     |                   |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1-2   | DDC (Default)  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2-3   | AUX (Reserved)   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| <b>68. JP_CCOS1</b> (Warning: MUST AC OFF before clear CMOS)                        |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Battery</td> </tr> <tr> <td>2</td> <td>RTC</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> </tbody> </table>  | PIN | SETTING | 1   | Battery            | 2   | RTC            | 3               | GND      | <p><b>Warning:</b><br/>MUST AC OFF before clear CMOS</p> <p>1. Clear CMOS<br/>1-2: Normal (Default)<br/>2-3: Clear CMOS<br/>2. HDR_1X3P_2.54mm</p> |                    |  |         |                            |    |        |     |                   |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1   | Battery  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2   | RTC  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 3   | GND  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| <b>69. JP_TPM1</b> (Optional)   |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>3.3V</td> </tr> <tr> <td>2</td> <td>TMP PP</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> </tbody> </table>  | PIN | SETTING | 1   | 3.3V               | 2   | TMP PP         | 3               | GND      | <p>1. TPM PP<br/>1-2: Pull-High<br/>2-3: Pull-Low (Default)<br/>2. HDR_1X3P_2.54mm</p>   |                    |  |         |                            |    |        |     |                   |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1   | 3.3V   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2   | TMP PP   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 3   | GND  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| <b>70. JP_LPC_DRQ1</b> (Optional)   |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>DRQ0#</td> </tr> <tr> <td>2</td> <td>LPC I/O DRQ#</td> </tr> <tr> <td>3</td> <td>DRQ1#</td> </tr> </tbody> </table>   | PIN | SETTING | 1   | DRQ0#              | 2   | LPC I/O DRQ#   | 3               | DRQ1#    | <p>1. LPC Super I/O DRQ#<br/>1-2: DRQ0#<br/>2-3: DRQ1# (Default)<br/>2. HDR_1X3P_2.54mm</p>  |                    |  |         |                            |    |        |     |                   |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1   | DRQ0#  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2   | LPC I/O DRQ#   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 3   | DRQ1#  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| <b>71. JP_LPC_RST1</b> (Optional)   |  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>PCIRST#</td> </tr> <tr> <td>2</td> <td>LPC I/O RST#</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> </tbody> </table>   | PIN | SETTING | 1   | PCIRST#            | 2   | LPC I/O RST#   | 3               | GND      | <p>1. LPC Super I/O<br/>1-2: Enable Super I/O<br/>2-3: Disable Super I/O (Default)<br/>2. HDR_1X3P_2.54mm</p>                                      |                    |  |         |                            |    |        |     |                   |
| PIN   | SETTING  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 1   | PCIRST#  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 2   | LPC I/O RST#   |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |
| 3   | GND  |     |         |     |                    |     |                |                 |          |  |                    |  |         |                            |    |        |     |                   |

| <b>72. JP_LPC_ADD1</b> (Optional)   |  |     |         |     |                     |     |                                    |  |            |   |
|---|--|-----|---------|-----|---------------------|-----|------------------------------------|--|------------|---|
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>3.3V</td> </tr> <tr> <td>2</td> <td>LPC I/O ADDR</td> </tr> <tr> <td>3</td> <td>GND</td> </tr> </tbody> </table>                                  | PIN | SETTING | 1   | 3.3V                | 2   | LPC I/O ADDR                       | 3  | GND        | <p>1. LPC Super I/O Address<br/>1-2: Address 4EH (Default)<br/>2-3: Address 2EH<br/>2. HDR_1X3P_2.54mm</p>  |
| PIN   | SETTING  |     |         |     |                     |     |                                    |  |            |   |
| 1   | 3.3V   |     |         |     |                     |     |                                    |  |            |   |
| 2   | LPC I/O ADDR   |     |         |     |                     |     |                                    |  |            |   |
| 3   | GND  |     |         |     |                     |     |                                    |  |            |   |
| <b>73. JP_LPC_PWRSW1</b> (Optional)   |  |     |         |     |                     |     |                                    |  |            |   |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>LPC PWRBT</td> </tr> <tr> <td>2</td> <td>PWRBT</td> </tr> <tr> <td>3</td> <td>FP PWRBT</td> </tr> </tbody> </table>                               | PIN | SETTING | 1   | LPC PWRBT           | 2   | PWRBT                              | 3  | FP PWRBT   | <p>1. PWRBTN# (COM Express Pin.12) signal selection:<br/>1-2: from carrier board SIO PWRSW (ACM-B6360 not supported)<br/>2-3: form "BTN_PWR1" or "FPIO1 Pin.7-8" (Default)<br/>2. HDR_1X3P_2.54mm</p> |
| PIN   | SETTING  |     |         |     |                     |     |                                    |  |            |   |
| 1   | LPC PWRBT  |     |         |     |                     |     |                                    |  |            |   |
| 2   | PWRBT  |     |         |     |                     |     |                                    |  |            |   |
| 3   | FP PWRBT   |     |         |     |                     |     |                                    |  |            |   |
| <b>74. JP_LPC_PSON1</b> (Optional)  |  |     |         |     |                     |     |                                    |  |            |   |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>LPC PSON#</td> </tr> <tr> <td>2</td> <td>PSON#</td> </tr> <tr> <td>3</td> <td>S3#</td> </tr> </tbody> </table>                                    | PIN | SETTING | 1   | LPC PSON#           | 2   | PSON#                              | 3  | S3#        | <p>1. PSON Select<br/>1-2: Super I/O PSON#<br/>2-3: S3 (Default)<br/>2. HDR_1X3P_2.54mm</p>   |
| PIN   | SETTING  |     |         |     |                     |     |                                    |  |            |   |
| 1   | LPC PSON#  |     |         |     |                     |     |                                    |  |            |   |
| 2   | PSON#  |     |         |     |                     |     |                                    |  |            |   |
| 3   | S3#  |     |         |     |                     |     |                                    |  |            |   |
| <b>75. JP_CASE1</b> (Optional)  |  |     |         |     |                     |     |                                    |  |            |   |
|    | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>Case Open</td> </tr> <tr> <td>2</td> <td>GND</td> </tr> <tr> <td>3</td> <td>Case Open#</td> </tr> </tbody> </table>                               | PIN | SETTING | 1   | Case Open           | 2   | GND                                | 3  | Case Open# | <p>1. Case Open<br/>1-2: Case Open Low<br/>2-3: Case Open High (Default)<br/>2. HDR_1X3P_2.54mm</p>   |
| PIN   | SETTING  |     |         |     |                     |     |                                    |  |            |   |
| 1   | Case Open  |     |         |     |                     |     |                                    |  |            |   |
| 2   | GND  |     |         |     |                     |     |                                    |  |            |   |
| 3   | Case Open#   |     |         |     |                     |     |                                    |  |            |   |
| <b>77. JP_PSON1</b>   |  |     |         |     |                     |     |                                    |  |            |   |
|  | <p>ATX PSON# selection</p> <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>PIC PSON# (Default)</td> </tr> <tr> <td>2-3</td> <td>SIO PSON# or S3# (by JP_LPC_PSON1)</td> </tr> </tbody> </table> | PIN | SETTING | 1-2 | PIC PSON# (Default) | 2-3 | SIO PSON# or S3# (by JP_LPC_PSON1) | <p>1. JP_LPC_PSON1 setting:<br/>1-2: carrier board SIO PSON#<br/>2-3: S3# (Default)<br/>2. HDR_1X3P_2.54mm</p> |            |   |
| PIN   | SETTING  |     |         |     |                     |     |                                    |  |            |   |
| 1-2   | PIC PSON# (Default)  |     |         |     |                     |     |                                    |  |            |   |
| 2-3   | SIO PSON# or S3# (by JP_LPC_PSON1)   |     |         |     |                     |     |                                    |  |            |   |
| <b>78. JP_SPIVCC1</b>   |  |     |         |     |                     |     |                                    |  |            |   |
|  | <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>3VDUAL (Default)</td> </tr> <tr> <td>2-3</td> <td>VCC3</td> </tr> </tbody> </table>   | PIN | SETTING | 1-2 | 3VDUAL (Default)    | 2-3 | VCC3                               | <p>1. for carrier board only.<br/>2. HDR_1X3P_2.54mm</p>   |            |   |
| PIN   | SETTING  |     |         |     |                     |     |                                    |  |            |   |
| 1-2   | 3VDUAL (Default)   |     |         |     |                     |     |                                    |  |            |   |
| 2-3   | VCC3   |     |         |     |                     |     |                                    |  |            |   |

| <b>79. JP_BIOS1-2</b>  |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
|--|------------------------|------------------------------------|------------------------------------|------------------------------------|--------------------|-------------------|---|-------------|-------------|--|--------|--------|------|--------|-----------|---|-------------|------------|--------|--------|------|--------|-------------|---|------------|-------------|--------|---------|------|---------|-----------|---|------------|------------|---------|--------|------|--------|-----------|---|--|--|
|   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| Boot SPI / FWH BIOS Selection:<br>(Default from the SPI ROM on Module)   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <table border="1"> <thead> <tr> <th>JP_BIOS2<br/>BIOS_DIS1#</th> <th>JP_BIOS1<br/>BIOS_DIS0#</th> <th>Chipset<br/>SPI_CS1#<br/>Destination</th> <th>Chipset<br/>SPI_CS0#<br/>Destination</th> <th>Carrier<br/>SPI_CS#</th> <th>SPI<br/>Descriptor</th> <th>Bios Entry</th> <th>Ref<br/>Line</th> </tr> </thead> <tbody> <tr> <td>1-2<br/>High</td> <td>1-2<br/>High</td> <td>Module</td> <td>Module</td> <td>High</td> <td>Module</td> <td>SPI0/SPI1</td> <td>0</td> </tr> <tr> <td>1-2<br/>High</td> <td>2-3<br/>Low</td> <td>Module</td> <td>Module</td> <td>High</td> <td>Module</td> <td>Carrier FWH</td> <td>1</td> </tr> <tr> <td>2-3<br/>Low</td> <td>1-2<br/>High</td> <td>Module</td> <td>Carrier</td> <td>SPI0</td> <td>Carrier</td> <td>SPI0/SPI1</td> <td>2</td> </tr> <tr> <td>2-3<br/>Low</td> <td>2-3<br/>Low</td> <td>Carrier</td> <td>Module</td> <td>SPI1</td> <td>Module</td> <td>SPI0/SPI1</td> <td>3</td> </tr> </tbody> </table> | JP_BIOS2<br>BIOS_DIS1# | JP_BIOS1<br>BIOS_DIS0#             | Chipset<br>SPI_CS1#<br>Destination | Chipset<br>SPI_CS0#<br>Destination | Carrier<br>SPI_CS# | SPI<br>Descriptor | Bios Entry  | Ref<br>Line | 1-2<br>High | 1-2<br>High  | Module | Module | High | Module | SPI0/SPI1 | 0 | 1-2<br>High | 2-3<br>Low | Module | Module | High | Module | Carrier FWH | 1 | 2-3<br>Low | 1-2<br>High | Module | Carrier | SPI0 | Carrier | SPI0/SPI1 | 2 | 2-3<br>Low | 2-3<br>Low | Carrier | Module | SPI1 | Module | SPI0/SPI1 | 3 |  |  |
| JP_BIOS2<br>BIOS_DIS1#   | JP_BIOS1<br>BIOS_DIS0# | Chipset<br>SPI_CS1#<br>Destination | Chipset<br>SPI_CS0#<br>Destination | Carrier<br>SPI_CS#                 | SPI<br>Descriptor  | Bios Entry        | Ref<br>Line   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 1-2<br>High  | 1-2<br>High            | Module                             | Module                             | High                               | Module             | SPI0/SPI1         | 0   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 1-2<br>High  | 2-3<br>Low             | Module                             | Module                             | High                               | Module             | Carrier FWH       | 1   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 2-3<br>Low   | 1-2<br>High            | Module                             | Carrier                            | SPI0                               | Carrier            | SPI0/SPI1         | 2   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 2-3<br>Low   | 2-3<br>Low             | Carrier                            | Module                             | SPI1                               | Module             | SPI0/SPI1         | 3   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| Remark:<br>1. HDR_1X3P_2.54mm<br>2. Reference from "COM Express Module Base Specification Revision 2"  |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <b>80. JP_SDVO1-2</b> (Optional)   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
|   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| SVDO Card type Selection   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <table border="1"> <thead> <tr> <th>JP_SDVO1</th> <th>JP_SDVO2</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>1-2</td> <td>B2013 (Default)</td> </tr> <tr> <td>2-3</td> <td>2-3</td> <td>ADD2-N</td> </tr> </tbody> </table>   | JP_SDVO1               | JP_SDVO2                           | SETTING                            | 1-2                                | 1-2                | B2013 (Default)   | 2-3   | 2-3         | ADD2-N      | 1.B2013 is Acrosser B2013 card<br>2.ADD2-N is Intel bridge card<br>3.HDR_1X3P_2.54mm |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| JP_SDVO1   | JP_SDVO2               | SETTING                            |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 1-2  | 1-2                    | B2013 (Default)                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 2-3  | 2-3                    | ADD2-N                             |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <b>81. JP_EXCD1</b> (Optional)   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
|    |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| Virtual ExpressCard CPPE# signal   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <table border="1"> <thead> <tr> <th>PIN</th> <th>SETTING</th> </tr> </thead> <tbody> <tr> <td>1-2</td> <td>High (Default)</td> </tr> <tr> <td>2-3</td> <td>Low</td> </tr> </tbody> </table>  | PIN                    | SETTING                            | 1-2                                | High (Default)                     | 2-3                | Low               | 1. HDR_1X3P_2.54mm<br>2. ACM-B6360 connected to SIO's GPI pin for monitor only. |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| PIN  | SETTING                |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 1-2  | High (Default)         |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 2-3  | Low                    |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <b>82. JP_EXCD2</b> (Optional)   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
|   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| Virtual ExpressCard CPPE# signal   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
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| PIN  | SETTING                |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 1-2  | High (Default)         |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 2-3  | Low                    |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| <b>83. JP_DDC1</b> (Optional)  |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
|   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| "SDVO1" slot DDC/AUX setting   |                        |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
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| PIN  | SETTING                |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 1-2  | DDC (Default)          |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |
| 2-3  | AUX (Reserved)         |                                    |                                    |                                    |                    |                   |   |             |             |  |        |        |      |        |           |   |             |            |        |        |      |        |             |   |            |             |        |         |      |         |           |   |            |            |         |        |      |        |           |   |  |  |

## 5. Onboard Button & others

|   |                                   |  |
|---|-----------------------------------|--|
| <b>20. BZ1</b>  |                                   |  |
|    | System Buzzer                     |  |
| <b>26. BTN_SLEEP1</b>   |                                   |  |
|    | Sleep Button                      | PS: ACM-B6360 not support<br>Normal:<br>COM Express Pin.B103 be Hi<br>Press:<br>COM Express Pin.B103 be Low  |
| <b>27. BTN_BATLOW1</b> (Optional)   |                                   |  |
|    | Virtual Battery Low signal Button | PS: ACM-B6360 not support<br>Normal:<br>COM Express Pin.A27 be Hi<br>Press:<br>COM Express Pin.A27 be Low  |
| <b>28. BTN_LID1</b> (Optional)  |                                   |  |
|    | Virtual LID Switch Button         | PS: ACM-B6360 not support<br>Normal:<br>COM Express Pin.A103 be Hi<br>Press:<br>a. LVDS back light OFF<br>b. COM Express Pin.A103 be Low           |
| <b>29. BTN_ALERT1</b>   |                                   |  |
|    | SMB Alert Button                  | Normal:<br>COM Express Pin.B15 be Hi<br>Press:<br>COM Express Pin.B15 be Low   |
| <b>40. BTN_RST1</b>   |                                   |  |
|  | System Reset Button               | 1.Parallel with "FPIO1 Pin.5-6"<br>2.Normal:<br>COM Express Pin.B49 be Hi<br>Press:<br>COM Express Pin.B49 be Low                                  |
| <b>41. BTN_PWR1</b>   |                                   |  |
|  | Power ON Button                   | 1.Parallel with "FPIO1 Pin.7-8"<br>2. Setting by "JP_LPC_PWRSW1"<br>3.Normal:<br>COM Express Pin.B12 be Hi<br>Press:<br>COM Express Pin.B12 be Low |
| <b>55. LED_77S1</b>   |                                   |  |
|  | 80 port POST code Display         |  |