

ACM-XD15B7

*COM Express 3.0 Basic Size Type 7
with Intel® Xeon® D SoC*



User Manual

Acrosser Technology Co., Ltd.
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Purpose

This document is intended to provide the information about the features and use of the product.

Audience

The intended audiences are technical personnel, not for general audiences.

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1. Introduction

COM Express Concept

COM Express is an open industry standard defined specifically for COMs (Computer-on-Modules). Its creation provides the ability to transfer from legacy interfaces to the newest technologies available today.

COM Express modules are available in three form factors:

- COM Express Extended Module: 155mm x 110mm
- COM Express Basic Module: 125mm x 95mm
- COM Express Compact Module: 95mm x 95mm

Type 7 COM Express Basic Module

Type 7 COM Express Basic Module (Enhanced Version)

An Added Surge Protection Circuit

An Added CPLD Control Circuit

Pulled Up the 3.3V, 5V & 12V's Capacitance value for Enhancing Endurance

The ACM-XD15B7 utilizes the Type 7 COM Express Basic Module that fully complies with the PICMG (PCI Industrial Computer Manufacturers Group), COM.0 R3.0 specification and offers extra ordinarily high bandwidth for high-speed data transmission through two high-performance 220-pin connectors.

The ACM-XD15B7 supports Intel Xeon D SoC(Broadwell-DE) processor. The basic I/O ports support VGA, 4 x USB 2.0, 4 x USB 3.0, COM, 2 x 10GbE LAN, and GPIO with optional carrier board ADB-XD15MB.

Type	Connector Rows	PCI Express Lanes	PEG	PCI	IDE	SATA	LAN	Video	Serial	Other Features	Note
1	AB (Single)	6	No	No	No	4	1	LVDS A & B, VGA			Legacy
2	AB & CD (Double)	22	Yes	Yes	1	4	1	LVDS A & B, VGA, PEG/SDVO			Legacy
3	AB & CD (Double)	22	Yes	Yes	No	4	3	LVDS A & B, VGA, PEG/SDVO			Legacy
4	AB & CD (Double)	32	Yes	No	1	4	1	LVDS A & B, VGA, PEG/SDVO			Legacy
5	AB & CD (Double)	32	Yes	No	No	4	3	LVDS A & B, VGA, PEG/SDVO			Legacy
6	AB & CD (Double)	24	Yes	No	No	4	1	LVDS A & B, VGA, 3 x DDI	2 TX/RX serial pairs with option to overlay CAN interface on 1 port	USB 3.0	
7	AB & CD (Double)	32	Yes, for 16 lanes	Yes		2		None	2 TX/RX serial pairs with option to overlay CAN interface on 1 port	USB 3.0, 4 x 10Gb Ethernet	Added in Rev 3.0
10	AB (Single)	4	No	No	No	2	1	LVDS A only (AB (Single) channel), DDI	2 Serial COM, 1 optional CAN	USB 3.0	

1.1. Specifications

CPU	<ul style="list-style-type: none"> • Intel® Xeon® D Processor (Industrial grade) • Xeon® D1548 2.0/2.6GHz 12MB Cache, 45W (8 core)
Chipset	<ul style="list-style-type: none"> • Integrated in SoC processor
BIOS	<ul style="list-style-type: none"> • Software Protection function • PEG: Enable / Disable(Default) • PXE Booting: Enable(Default) / Disable • DMI Link active state power management (ASPM): Enable / Disable (Default)
Memory	<ul style="list-style-type: none"> • 2x 2400 DDR4 SODIMM (Support ECC function) • Memory up to 32GB (Dual channel) • Default configuration: 8GB+8GB (Micron IC Used)
10 GbE Ethernet	<ul style="list-style-type: none"> • 2x 10GBASE KR (10 GbE with KR interface & NC-SI) • Support PXE Boot function
GbE Ethernet	<ul style="list-style-type: none"> • 1x GbE & (Intel I210IT) • PCI-e Lane7used by GbE LAN • Support PXE Boot function
NC-SI	<ul style="list-style-type: none"> • NC-SI supported on AB connector
SATA	<ul style="list-style-type: none"> • 2x SATA III (Port 0/1)
USB	<ul style="list-style-type: none"> • 4x USB2.0 (Port 0/1/2/3) • 4x USB3.0 (Port 0/1/2/3)
Serial Port	<ul style="list-style-type: none"> • 2x RS-232 (Only TX & RX)
GPIO	<ul style="list-style-type: none"> • 4-bit GPI and 4-bit GPO
PCI-e/PCI Busses Support	<ul style="list-style-type: none"> • 24x PCI Express 3.0 lanes • 1x PCI-e x16 (PCI-e lanes 16~31, reserved 2x PCI-e x8 by bios setting) • 1x PCI-e x8 (PCI-e lanes 8~15) • 8x PCI Express 2.0 lanes • 6x PCI-e x1: AB connector, PCI-e Lanes 0~5) • 1x PCI-e x1: CD connector, PCI-e Lanes 6)
OS Support	<ul style="list-style-type: none"> • Linux Kernel 4.9
Heat sink	<ul style="list-style-type: none"> • Aluminum material heat-sink with fan(Default) • CPU Heat spreader(Optional item)
Watchdog Timer	<ul style="list-style-type: none"> • Software programmable 0~255 seconds, 0 = disable timer.
Power Mode	<ul style="list-style-type: none"> • AT Mode (Auto power on after power input)

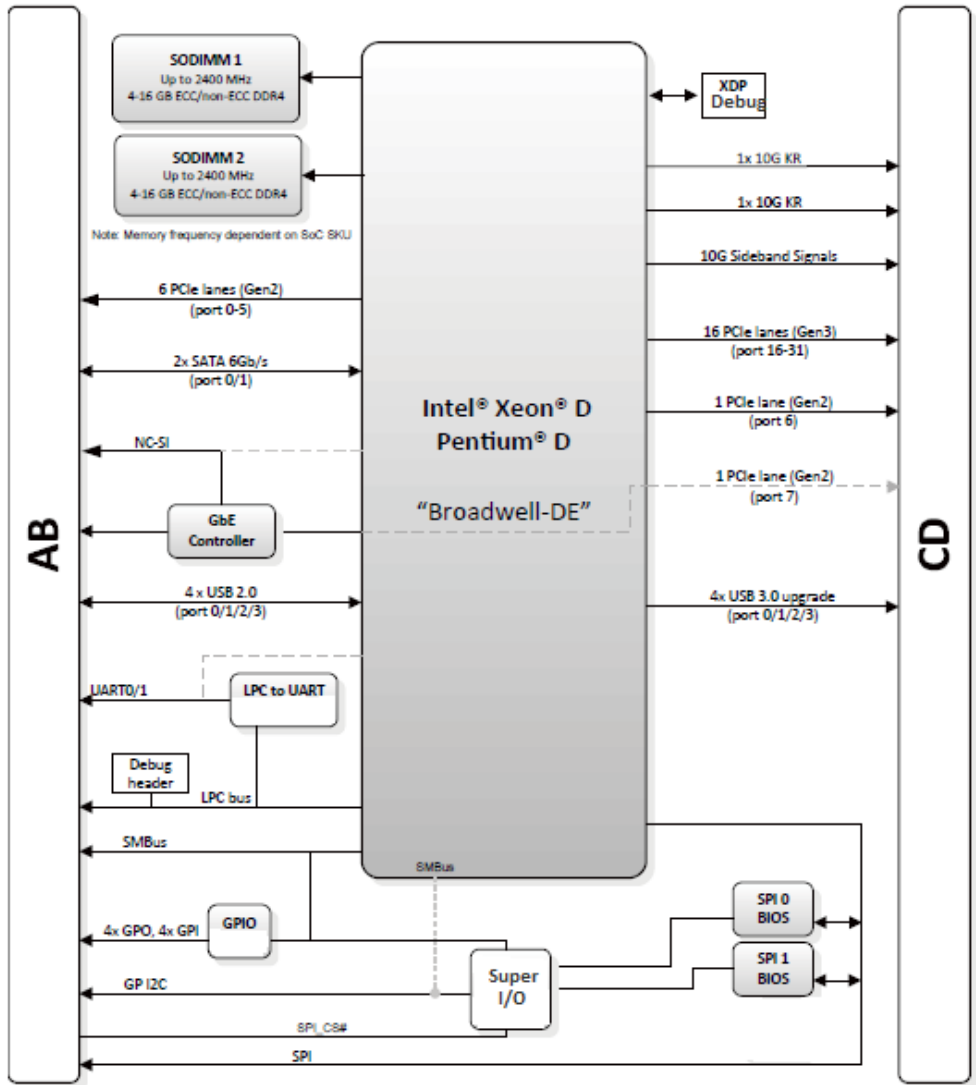
CMOS Battery	<ul style="list-style-type: none">Without CMOS Battery.
Dimension	<ul style="list-style-type: none">125 x 95mm (4.92" x 3.74") COM Express Basic Size
Safety	<ul style="list-style-type: none">CE, FCC class A, ASTM D4169 Level 3, REACH

1.2. Packing List

Check if the following items are included in the package.

	Item	Q'ty
<input type="checkbox"/>	ACM-XD15B7	1
<input type="checkbox"/>	Quick Guide	1
<input type="checkbox"/>	CD Disk	1

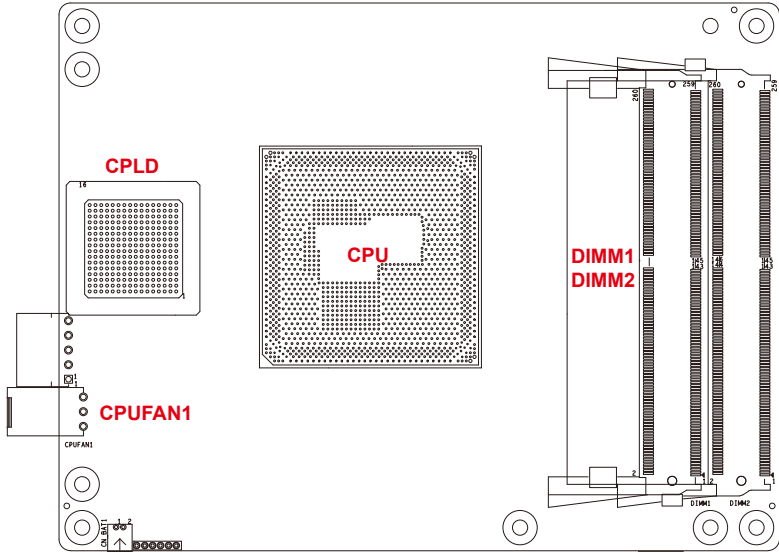
1.3. Block Diagram



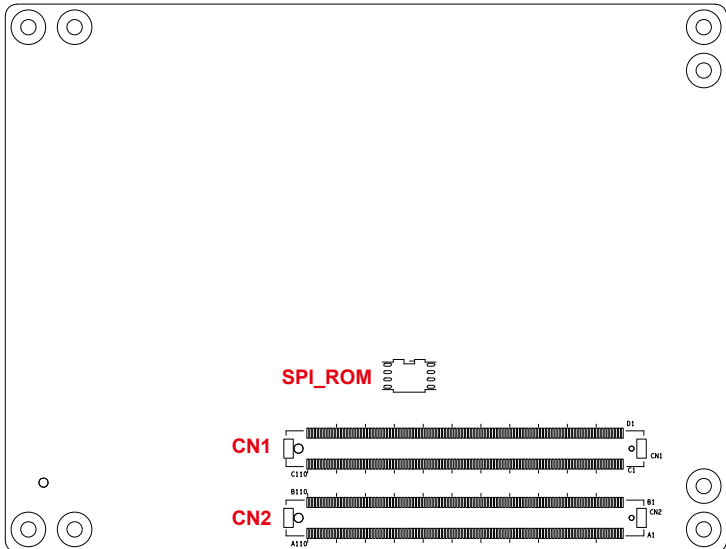
2. Hardware Information

2.1. Mainboard Layout

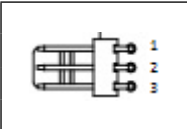
Top View



Bottom View



2.2. Connector Pin Definition

CPU	Intel Xeon D Processor (Industrial grade) Xeon® D1548 2.0/2.6GHz 12MB Cache, 45W (8 core) FCBGA1667 37.5x37.5mm								
DIMM1/DIMM2	CN_DIMM_1&2 DDR4 SO-DIMM 260P								
CPUFAN1	3-pin Fan Connector								
	 <table border="1" style="display: inline-table; vertical-align: top;"> <thead> <tr> <th>Pin #</th> <th>Signal</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>GND</td> </tr> <tr> <td>2</td> <td>+12V</td> </tr> <tr> <td>3</td> <td>FAN Speed Sensor</td> </tr> </tbody> </table>	Pin #	Signal	1	GND	2	+12V	3	FAN Speed Sensor
Pin #	Signal								
1	GND								
2	+12V								
3	FAN Speed Sensor								
	Header: 3-pin. Pitch: 2.54mm.								
CPLD	CPLD Power Manager								
CN1	COM-Express 220Pin male H3.25mm Pitch:0.5mm TYPE 7 C&D								
CN2	COM-Express 220Pin male H3.25mm Pitch:0.5mm TYPE 7 A&B								
SPI_ROM	For BIOS Firmware SPI_128Mb								

2.2.1. CN1, CN2 Pin Assignments

This module is connected to the carrier board via two 220-pin connectors for a total of 440 pins connectivity. These connectors are broken down into four rows. The CN2 connector consists of rows A and B, while the CN1 connector consists of rows C and D.

Pin #	CN2 – Row A	CN2 – Row B	CN1 – Row C	CN1 – Row D
1	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
2	GBE0_MDI3-	GBE0_ACT#	GND (FIXED)	GND (FIXED)
3	GBE0_MDI3+	LPC_FRAME#	USB_SSRX0-	USB_SSTX0-
4	GBE0_LINK100#	LPC_AD0	USB_SSRX0+	USB_SSTX0+
5	GBE0_LINK1000#	LPC_AD1	GND (FIXED)	GND (FIXED)
6	GBE0_MDI2-	LPC_AD2	USB_SSRX1-	USB_SSTX1-
7	GBE0_MDI2+	LPC_AD3	USB_SSRX1+	USB_SSTX1+
8	No Connect	LPC_DRQ0#	GND (FIXED)	GND (FIXED)
9	GBE0_MDI1-	LPC_DRQ1#	USB_SSRX2-	USB_SSTX2-

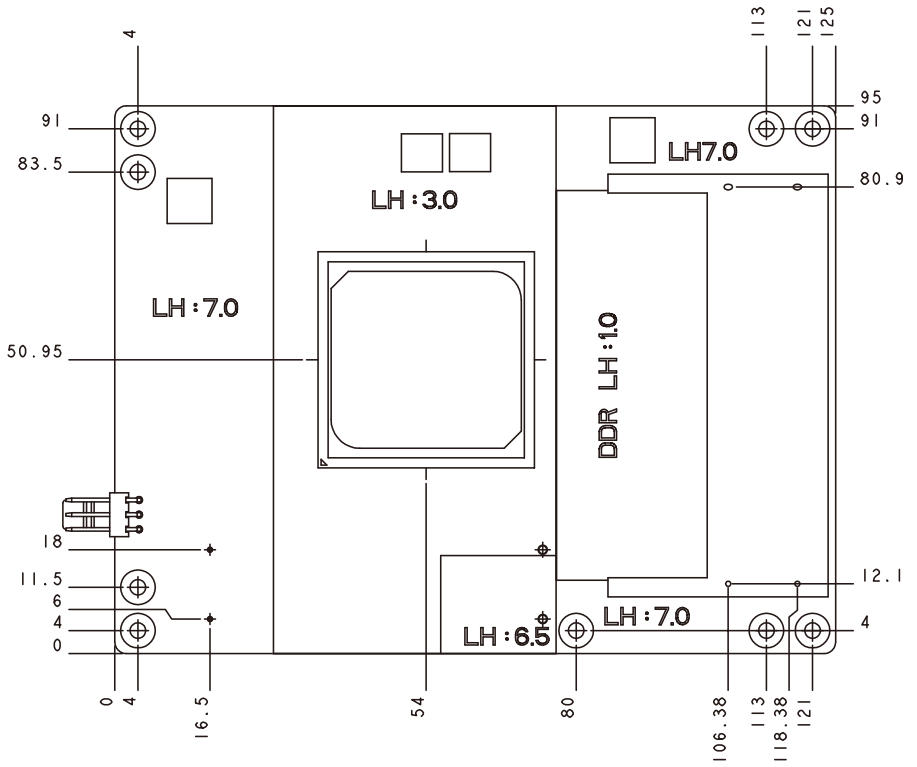
Pin #	CN2 – Row A	CN2 – Row B	CN1 – Row C	CN1 – Row D
10	GBE0_MDI1+	LPC_CLK	USB_SSRX2+	USB_SSTX2+
11	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
12	GBE0_MDI0-	PWRBTN#	USB_SSRX3-	USB_SSTX3-
13	GBE0_MDI0+	SMB_CK	USB_SSRX3+	USB_SSTX3+
14	GBE0_CTREF	SMB_DAT	GND (FIXED)	GND (FIXED)
15	SUS_S3#	SMB_ALERT#	10G_PHY_MDC_SCL3	PHY_MDIO_SDA3
16	SATA0_TX+	SATA1_TX+	10G_PHY_MDC_SCL2	10G_PHY_MDIO_SDA2
17	SATA0_TX-	SATA1_TX-	10G_SDP2	10G_SDP3
18	SUS_S4#	SUS_STAT#	GND	GND
19	SATA0_RX+	SATA1_RX+	PCIE_RX6+	PCIE_TX6+
20	SATA0_RX-	SATA1_RX-	PCIE_RX6-	PCIE_TX6-
21	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
22	PCIE_TX15+	PCIE_RX15+	No Connect	No Connect
23	PCIE_TX15-	PCIE_RX15-	No Connect	No Connect
24	SUS_S5#	PWR_OK	10G_INT2	10G_INT3
25	PCIE_TX14+	PCIE_RX14+	GND	GND
26	PCIE_TX14-	PCIE_RX14-	10G_KR_RX3+	10G_KR_TX3+
27	BATLOW#	WDT	10G_KR_RX3-	10G_KR_TX3-
28	SATA_ACT#	RSVD	GND	GND
29	RSVD	RSVD	10G_KR_RX2+	10G_KR_TX2+
30	RSVD	RSVD	10G_KR_RX2--	10G_KR_TX2-
31	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
32	RSVD	SPKR	10G_SFP_SDA3	10G_SFP_SCL3
33	RSVD	I2C_CK	10G_SFP_SDA2	10G_SFP_SCL2
34	BIOS_DISABLE0#	I2C_DAT	10G_PHY_RST_23	10G_PHY_SEL_23
35	THRMTRIP#	THRM#	10G_PHY_RST_01	10G_PHY_SEL_01
36	PCIE_TX13+	PCIE_RX13+	10G_LED_SDA	RSVD
37	PCIE_TX13-	PCIE_RX13-	10G_LED_SCL	RSVD
38	GND	GND	10G_SFP_SDA1	10G_SFP_SCL1
39	PCIE_TX12+	PCIE_RX12+	10G_SFP_SDA0	10G_SFP_SCL0
40	PCIE_TX12-	PCIE_RX12-	10G_SDP0	10G_SDP1
41	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
42	USB2-	USB3-	10G_KR_RX1+	10G_KR_TX1+
43	USB2+	USB3+	10G_KR_RX1-	10G_KR_TX1-

Pin #	CN2 – Row A	CN2 – Row B	CN1 – Row C	CN1 – Row D
44	USB_2_3_OC#	USB_0_1_OC#	GND	GND
45	USB0-	USB1-	10G_PHY_MDC_SCL1	10G_PHY_MDC_SDA1
46	USB0+	USB1+	10G_PHY_MDC_SCL0	10G_PHY_MDC_SDA0
47	VCC_RTC	EXCD1_PERST#	DDI3_PAIR2-	DDI2_PAIR2-
48	RSVD	USB0_HOST_PRSENT	GND	GND
49	GBE0_SDP	SYS_RESET#	10G_KR_RX0+	10G_KR_TX0+
50	LPC_SERIRQ	CB_RESET#	10G_KR_RX0-	10G_KR_TX0-
51	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
52	PCIE_TX5+	PCIE_RX5+	PCIE_TX16+	PCIE_RX16+
53	PCIE_TX5-	PCIE_RX5-	PCIE_TX16-	PCIE_RX16-
54	GPI0	GPO1	TYPE0#	RSVD
55	PCIE_TX4+	PCIE_RX4+	PCIE_TX17+	PCIE_RX17+
56	PCIE_TX4-	PCIE_RX4-	PCIE_TX17-	PCIE_RX17-
57	GND	GPO2	TYPE1#	TYPE2#
58	PCIE_TX3+	PCIE_RX3+	PCIE_TX18+	PCIE_RX18+
59	PCIE_TX3-	PCIE_RX3-	PCIE_TX18-	PCIE_RX18-
60	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
61	PCIE_TX2+	PCIE_RX2+	PCIE_TX19+	PCIE_RX19+
62	PCIE_TX2-	PCIE_RX2-	PCIE_TX19-	PCIE_RX19-
63	GPI1	GPO3	RSVD	RSVD
64	PCIE_TX1+	PCIE_RX1+	RSVD	RSVD
65	PCIE_TX1-	PCIE_RX1-	PCIE_TX20+	PCIE_RX20+
66	GND	WAKE0#	PCIE_TX20-	PCIE_RX20-
67	GPI2	WAKE1#	RSVD	GND
68	PCIE_TX0+	PCIE_RX0+	PCIE_TX21+	PCIE_RX21+
69	PCIE_TX0-	PCIE_RX0-	PCIE_TX21-	PCIE_RX21-
70	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
71	PCIE_TX8+	PCIE_RX8+	PCIE_TX22+	PCIE_RX22+
72	PCIE_TX8-	PCIE_RX8-	PCIE_TX22-	PCIE_RX22-
73	GND	GND	GND(FIXED)	GND(FIXED)
74	PCIE_TX9+	PCIE_RX9+	PCIE_TX23+	PCIE_RX23+
75	PCIE_TX9-	PCIE_RX9-	PCIE_TX23-	PCIE_RX23-
76	GND	GND	GND	GND
77	PCIE_TX10+	PCIE_RX10+	RSVD	RSVD

Pin #	CN2 – Row A	CN2 – Row B	CN1 – Row C	CN1 – Row D
78	PCIE_TX10-	PCIE_RX10-	PCIE_TX24+	PCIE_RX24+
79	GND	GND	PCIE_TX24-	PCIE_RX24-
80	GND(FIXED)	GND(FIXED)	GND(FIXED)	GND(FIXED)
81	PCIE_TX11+	PCIE_RX11+	PCIE_TX25+	PCIE_RX25+
82	PCIE_TX11-	PCIE_RX11-	PCIE_TX25-	PCIE_RX25-
83	GND	GND	RSVD	RSVD
84	NCSI_TX_EN	VCC_5V_SBY	GND	GND
85	GPI3	VCC_5V_SBY	PCIE_TX26+	PCIE_RX26+
86	RSVD	VCC_5V_SBY	PCIE_TX26-	PCIE_RX26-
87	RSVD	VCC_5V_SBY	GND	GND
88	PCIE0_CK_REF+	BIOS_DISABLE1#	PCIE_TX27+	PCIE_RX27+
89	PCIE0_CK_REF-	NCSI_TRI_EN	PCIE_TX27-	PCIE_RX27-
90	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
91	SPI_POWER (3.3V)	NCSI_CLK_IN	PCIE_TX28+	PCIE_RX28+
92	SPI_MISO	NCSI_RXD1	PCIE_TX28-	PCIE_RX28-
93	GPO0	NCSI_RXD0	GND	GND
94	SPI_CLK	NCSI_CRS_DV	PCIE_TX29+	PCIE_RX29+
95	SPI_MOSI	CSI_TXD1	PCIE_TX29-	PCIE_RX29-
96	TPM_PP	CSI_TXD0	GND	GND
97	TYPE10#	SPI_CS#	RSVD	RSVD
98	SER0_TX	NCSI_ARB_IN	PCIE_TX30+	PCIE_RX30+
99	SER0_RX	NCSI_ARB_OUT	PCIE_TX30-	PCIE_RX30-
100	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)
101	SER1_TX	No Connect	PCIE_TX31+	PCIE_RX31+
102	SER1_RX	FAN_TACHIN	PCIE_TX31-	PCIE_RX31-
103	LID#	No Connect	GND	GND
104	VCC_12V	VCC_12V	VCC_12V	VCC_12V
105	VCC_12V	VCC_12V	VCC_12V	VCC_12V
106	VCC_12V	VCC_12V	VCC_12V	VCC_12V
107	VCC_12V	VCC_12V	VCC_12V	VCC_12V
108	VCC_12V	VCC_12V	VCC_12V	VCC_12V
109	VCC_12V	VCC_12V	VCC_12V	VCC_12V
110	GND (FIXED)	GND (FIXED)	GND (FIXED)	GND (FIXED)

2.3. Board Dimension

(Unit: mm)



3. BIOS Settings

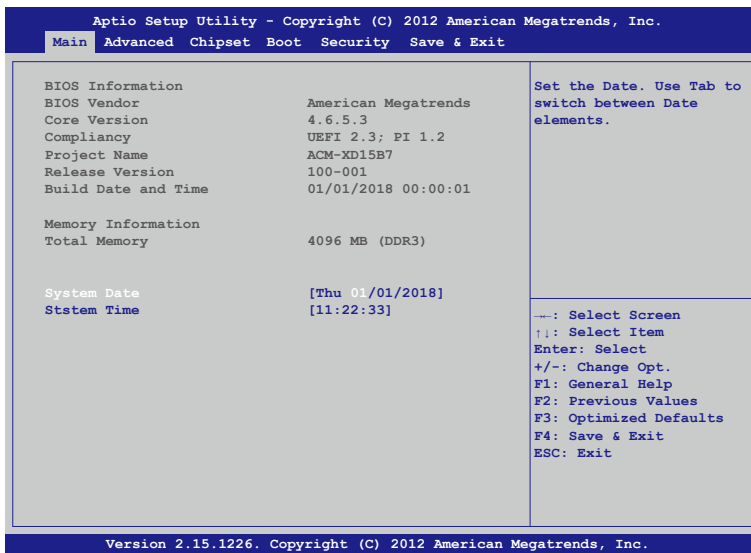
This chapter describes the BIOS menu displays and explains how to perform common tasks needed to get the system up and running. It also gives detailed explanation of the elements found in each of the BIOS menus. The following topics are covered:

- Main Setup
- Advanced Setup
- Chipset Setup
- Boot Setup
- Security Setup
- Save & Exit Setup

Once you enter the Award BIOS™ CMOS Setup Utility, the Main Menu will appear on the screen. Use the arrow keys to highlight the item and then use the <Pg Up> <Pg Dn> keys to select the value you want in each item.

3.1. Main Setup

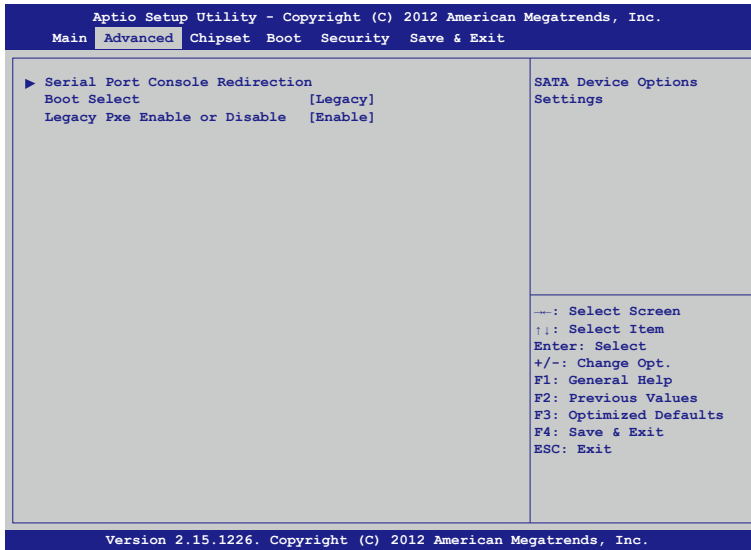
The BIOS setup main menu includes some options. Use the [Up/Down] arrow key to highlight the option, and then press the [Enter] key to select the item and configure the functions.



Note: Listed at the bottom of the menu are the control keys. If you need any help with the item fields, you can press <F1> key, and it will display the relevant information.

- **System Date**
Set the system date.
- **System Time**
Set the system time.

3.2. Advanced Setup



3.2.1. Serial Port Console Redirection



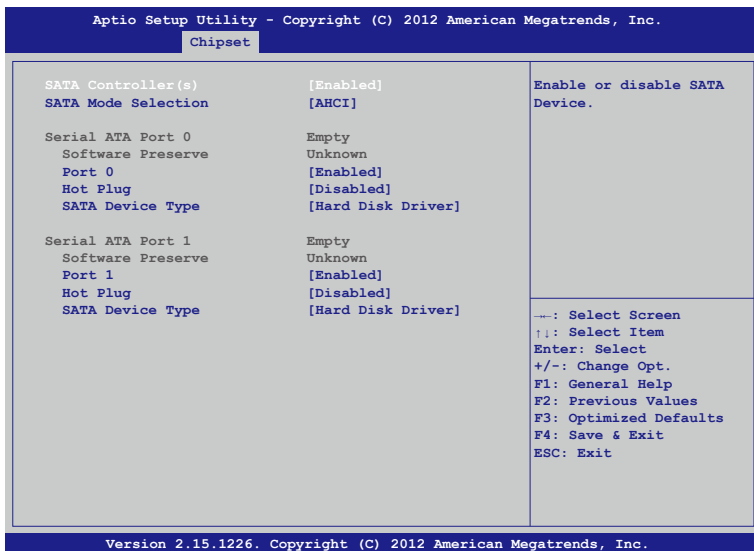
- **Console Redirection**

The setting specify how the host computer and the remote computer will exchange data. Both computers should have the same or compatible settings.

3.3. Chipset Setup

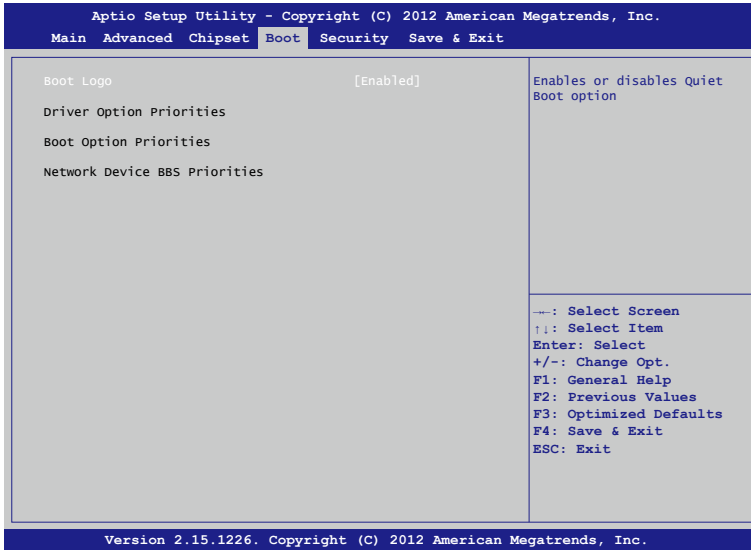


3.3.1. PCH SATA Configuration



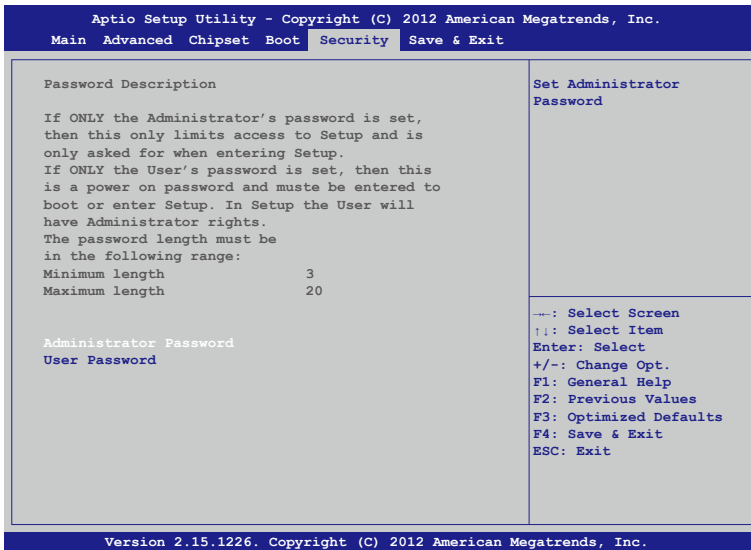
- **SATA Controller(s)**
Enable or disable SATA Device.

3.4. Boot Setup



- **Boot Logo**
Enables or disabled Quiet Boot option.

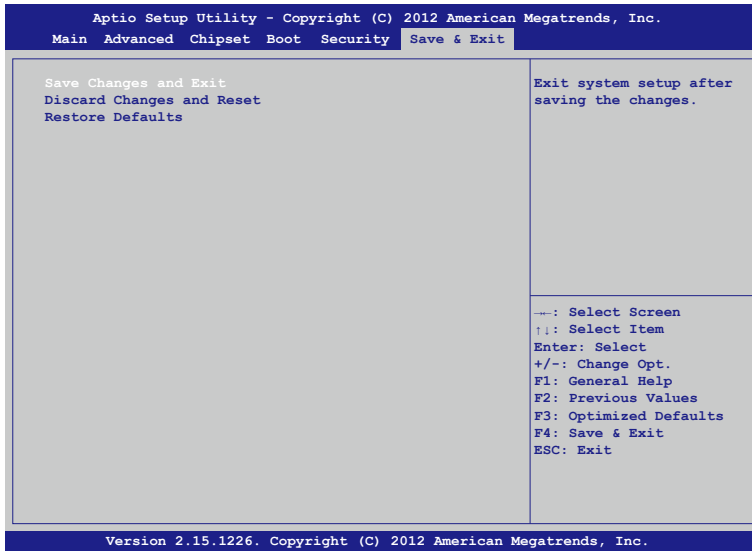
3.5. Security Setup



Once a password is effective, you have to enter the administrator password or user password before you access into the BIOS setup interface.

- **Administrator Password**
Set Administrator Password.
- **User Password**
Set User Password.

3.6. Save & Exit Setup



- **Save Changes and Reset**
Exit system setup after saving the changes.
- **Discard Changes and Reset**
Exit system setup without saving any changes.
- **Restore Defaults**
Restore/Load the default values for all the set up options.

4. Software Installation and Programming Guide

4.1. Introduction

4.1.1. Environment

This test utility develop is based on kernel 4.9 or above (Ubuntu 18.04.1 Server 64bit).

4.1.2. GPIO and Watchdog

This model provides both a GPIO interface and a Watchdog timer. Users can use the GPIO and Watchdog APIs to configure and to access the GPIO interface and the Watchdog timer. The GPIO has four input pins and four output pins. The Watchdog timer can be set to 1~255 seconds. Setting the timer to zero disables the timer. The remaining seconds of the timer to reboot can be read from the timer.

4.2. API List and Descriptions

4.2.1. General

Syntax:	lib_init(void)
Description:	Library initialization, using this library must be call this function first. Note: initialization may be wait 1 mins, because scan pic port
Parameters:	None
Return Value:	0:Successful, -1:Fail.
Syntax:	lib_close(void)
Description:	Library close, when you not used this library must be call this function.
Parameters:	None
Return Value:	0:Successful, -1:Fail.

4.2.2. GPIO

Syntax:	int get_gpo_status(int pin)
Description:	Get the status of GPIO output pins
Parameters:	This function fills in an integer variable as the parameter. The pin0 ~ pin3 is the status of the output pins.
Return Value:	0 or 1 (0 is Low, 1 is High)
Syntax:	int get_gpi_status(int pin)
Description:	Set the status of GPIO input pins.
Parameters:	This function fills in an integer variable as the parameter. The pin0 ~ pin3 is the status of the input pins.
Return Value:	0 or 1 (0 is Low, 1 is High)
Syntax:	void set_gpo_status(int pin, int value)
Description:	Set the status of GPIO Output pins and Value.
Parameters:	Set pin0~pin3 value 0 is Low, 1 is High
Return Value:	None

4.2.3. Watchdog

Syntax:	void wdt_start(int timevalue)
Description:	This function sets the watchdog timer register to the value 'val' and starts to count down. The value could be 0 ~ 255. The unit is second. Setting the timer register to 0 disables the watchdog function and stops the countdown.
Parameters:	The parameter 'timevalue' is the value to set to watchdog timer register. The range is 0~255.
Return Value:	None
Syntax:	int get_wdt_count(void)
Description:	This function read the value of the watchdog time counter and retruns it to the caller.
Parameters:	None
Return Value:	This function returns the value of the time counter and return it to the caller as an integer.

Syntax:	void wdt_stop(void)
Description:	This function reads the watchdog timer stop.
Parameters:	None
Return Value:	None

5. FAQ

Q 1. *Where can I find the serial number of this product?*

- The serial number (S/N) is a label printed with alpha-numeric character. You can find the S/N label on the bottom of this product or on its packing box.

Technical Support Form

We deeply appreciate your purchase of Acrosser products. Please find the “**tech_form.doc**” file in our utility CD. If you have any questions or problems about Acrosser products, please fill in the following information. We will answer your questions in the shortest time possible.

Describe Your Info and Acrosser System Info

- Your Company Name: _____
- Your Contact Info: _____ Phone Number: _____
- Your E-Mail Address: _____
- Your Company Address: _____

- Acrosser Model Name: _____
- Acrosser Serial Number: _____

Describe System Configuration

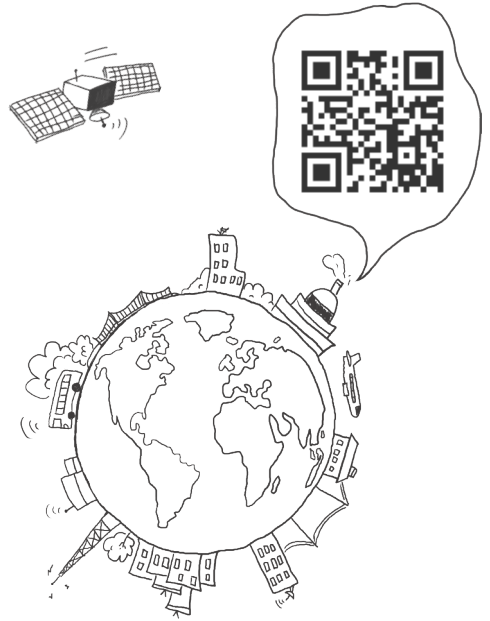
- CPU Type: _____
- Memory Size: _____
- Storage Device (e.g. HDD, CF, or SSD): _____
- Additional Peripherals (e.g. Graphic Card): _____
- Operating System & Version (e.g. Windows 7 Embedded): _____
- Special API or Driver: _____
(If yes, please provide it for debug.)
- Running Applications: _____
- Others: _____

Describe Your Problems or Questions:

Send the above information to one of the following Acrosser contacts:

- Acrosser Local Sales Representative
- Acrosser Authorized Sales Channels
- Acrosser Inquiry --- <http://www.acrosser.com/inquiry.html>
- Acrosser FAX Number --- 886-2-29992887

To Make Your
Embedded
Idea a Reality



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